

SW-314-PIN

Rev. V7

Features

- Integral TTL Driver
- Isolation: 50 dB @ 1 GHz
- Ultra Low DC Power Consumption
- Hermetic Surface Mount Package
- 50 Ω Nominal Impedance
- MIL-STD-883 Screening Available
- Lead-Free CR-14 Package
- RoHS* Compliant

Applications

RF/IF communications

Description

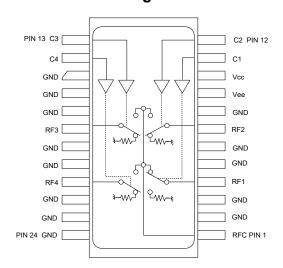
The SW-314-PIN is a GaAs MMIC SP4T absorptive switch with an integral silicon ASIC driver. This device is in a lead-free 24-lead ceramic surface mount package. This switch exhibits excellent performance from DC - 3 GHz, with very low DC power dissipation. Environmental screening is available. Contact the factory for information.

Ordering Information¹

Part Number	Package		
SW-314-PIN	Bulk		
SW-314-TB	Sample Board		

^{1.} Reference Application Note M513 for reel size information.

Functional Block Diagram



Pin Configuration²

J			
Pin #	Function		
1	RFC		
2,3,5,6,8,15,16, 17,19,20,22,23,24	GND		
4	RF1		
7	RF2		
9	V _{EE}		
10	V _{CC}		
11	C1		
12	C2		
13	C3		
14	C4		
18	RF3		
21	RF4		

The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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Electrical Specifications^{3,4}: Freq. = -55°C - +85°C, Z_0 = 50 Ω

Parameter	Test Conditions	Test Conditions Units Min.		Тур.	Max.
Insertion Loss	DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz DC - 3.0 GHz	dB —		_	1.3 1.4 1.6 1.8
Isolation	DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz DC - 3.0 GHz	DC - 1.0 GHz DC - 2.0 GHz dB 40 35		_	_
VSWR	RFC, RF1 - RF4 (On) DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz DC - 3.0 GHz	ratio	_	_	1.6:1 1.6:1 1.6:1 1.8:1
VSWR	RF1 - RF4 (Off) DC - 0.5 GHz DC - 1.0 GHz DC - 2.0 GHz DC - 3.0 GHz	ratio		_	1.3:1 1.5:1 1.9:1 2.4:1
T_{RISE} , T_{FALL}	10% to 90%	ns		7	_
T _{ON} , T _{OFF}	50% Control to 90% / 10% RF	/ 10% RF ns —		25	_
Transients	In-Band (peak-peak) mV — 20		20	_	
1 dB Compression	Input Power 0.05 GHz 0.5 - 3 GHz	dBm	_	20 27	_
IP3	Two-Tone Input Power up to 5 dBm 0.05 GHz 0.5 - 3 GHz	dBm	_	35 46	_
IP2	Two-Tone Input Power up to 5 dBm 0.05 GHz 0.5 - 3 GHz	dBm	_	45 60	_
V _{cc}	_	V	4.5	5.0	5.5
V_{EE}	_	V	-8.0	_	-5.0
Icc	$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$ $V_{CTL} = 0 \text{ to } 0.8 \text{ V}, \text{ or } V_{CC} - 2.1 \text{ V to } V_{CC}$		_	0.2	4.0
I _{EE}	V _{EE} = -5 V to -8 V	mA	_	0.1	1.0
V _{CTL}	Logic 0 (TTL) Logic 1 (TTL)	V	0.0 2.0	_	0.8 5.0
Input Leakage Current Low High	0 to 0.8 V 2 to 5 V	μΑ	_	_	1

^{3.} All specifications apply when operated with bias voltages of +5 V for V_{CC} and -5 V for V_{EE} .

^{4.} When DC blocks are used, a 10 $\rm K\Omega$ return to GND is required on the RFC port.



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Absolute Maximum Ratings^{5,6}

Parameter	Absolute Maximum		
Input Return Loss 0.05 GHz 0.5 - 3.0 GHz ⁷	27 dBm 34 dBm		
V _{CC}	-0.5 V ≤ V _{CC} ≤ +7.0 V		
V _{EE}	-8.5 V ≤ V _{EE} ≤ +0.5 V		
V _{CC} - V _{EE}	-0.5 V ≤ V _{CC} - V _{EE} ≤ 14.5 V		
V _{IN} ⁸	$-0.5 \text{ V} \le \text{V}_{\text{IN}} \le \text{V}_{\text{CC}} + 0.5 \text{ V}$		
Operating Temperature	-55°C to +125°C		
Storage Temperature	-65°C to +150°C		

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- When the input power is applied to the terminated port, the absolute maximum is +30 dBm.
- 8. Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

Truth Table (Switch)9

тт	L Cont	rol Inpu	uts		Comm		of Switch on to Each Port	
C1	C2	С3	C4	RF1	RF2	RF3	RF4	
1	0	0	0	On	Off	Off	Off	
0	1	0	0	Off	On	Off	Off	
0	0	1	0	Off	Off	On	Off	
0	0	0	1	Off	Off	Off	On	

9. 0 = TTL Low; 1 = TTL High

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

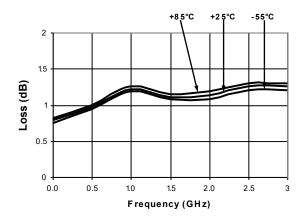
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



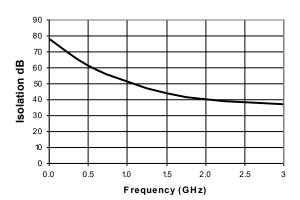
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Typical Performance Curves

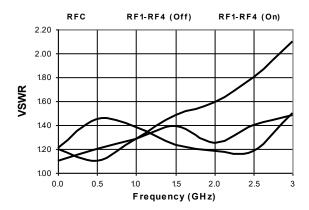
Insertion Loss



Isolation



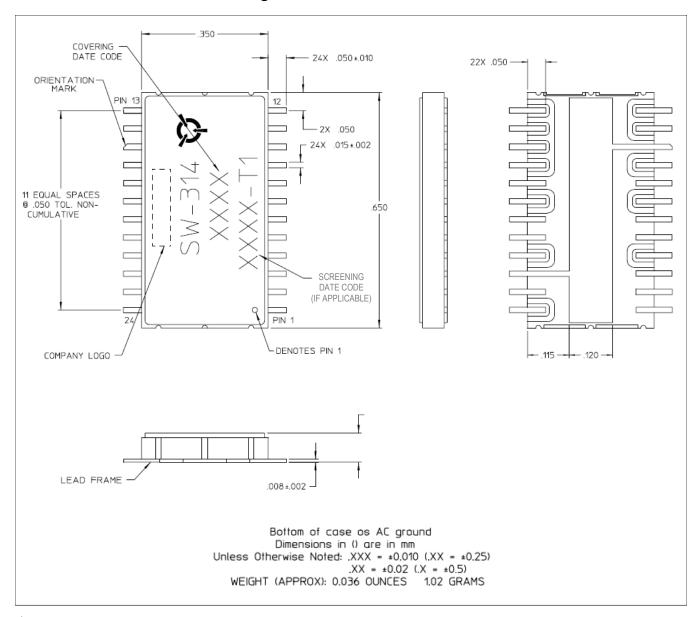
VSWR





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Lead-Free CR-14 Ceramic Package[†]



 $[\]ensuremath{^{\dagger}}$ Reference Application Note M538 for lead-free solder reflow recommendations.



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