MASW-011184
Rev. V1

## Features

- 0.35 dB TX Insertion Loss
- 0.5 dB RX Insertion Loss
- 41 dBm Input P0.1dB on TX Path
- 18 dB Typical Return Loss at Each RF Port
- Compatible with 1.8 V CMOS Logic
- 3 mm 16-Lead PQFN Package
- RoHS* Compliant


## Applications

- ISM
- Multi Market


## Description

The MASW-011184 is a single pole double throw (SPDT) switch with 0.35 dB of insertion loss in the TX path and 0.5 dB insertion loss in the RX path. The TX path is capable of handling 10 W input power. The input and output return losses in the thru path are typically 18 dB . The logic levels are standard 1.8 V CMOS. Only a single positive supply of +5 V is required.

The MASW-011184 is designed for transmit/receive applications between 1 and 5 GHz . The 3 mm PQFN package is lead free and RoHS compliant.

## Ordering Information ${ }^{1,2}$

| Part Number | Package |
| :---: | :---: |
| MASW-011184-TR1000 | 1000 piece reel |
| MASW-011184-TR3000 | 3000 piece reel |
| MASW-011184-SMB | Sample Board |

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

## Functional Schematic



Pin Configuration ${ }^{3}$

| Pin \# | Pin Name | Description |
| :---: | :---: | :---: |
| $1,2,4,6,7,10,11$, <br> $12,14,15$ | $\mathrm{NC}^{4}$ | No Connection |
| 3 | $\mathrm{TX}^{5}$ | TX Input/Output |
| 5 | $\mathrm{ANT}^{5}$ | Common Port |
| 8 | VDD | +5 V |
| 9 | GND | Ground |
| 13 | Vcntrl | Control Input |
| 16 | RX | RX Input/Output |

3. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.
4. MACOM recommends connecting No Connection (N/C) pins to ground
5. There are internal DC blocking capacitors at ANT and TX ports.
6. External DC blocking capacitor is required at RX port.
[^0]Electrical Specifications: $\mathrm{V}_{\mathrm{DD}}=+5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{BASE}}=+\mathbf{2 5}{ }^{\circ} \mathrm{C}, \mathrm{Z}_{\mathbf{0}}=50 \Omega$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss, ANT to TX | $\begin{aligned} & 1.0 \mathrm{GHz} \\ & 2.5 \mathrm{GHz} \\ & \text { 5.0 GHz } \end{aligned}$ | dB | - | $\begin{aligned} & 0.38 \\ & 0.30 \\ & 0.38 \end{aligned}$ | $\begin{aligned} & \overline{0.9} \\ & 1.0 \end{aligned}$ |
| Insertion Loss, ANT to RX | $\begin{aligned} & 1.0 \mathrm{GHz} \\ & 2.5 \mathrm{GHz} \\ & 5.0 \mathrm{GHz} \end{aligned}$ | dB | - | $\begin{aligned} & 0.57 \\ & 0.34 \\ & 0.65 \end{aligned}$ | $\begin{aligned} & \overline{1.0} \\ & 1.3 \end{aligned}$ |
| Isolation, ANT to RX in TX Mode | $\begin{aligned} & 1.0 \mathrm{GHz} \\ & 2.5 \mathrm{GHz} \\ & 5.0 \mathrm{GHz} \end{aligned}$ | dB | $\begin{aligned} & \overline{32} \\ & 24 \end{aligned}$ | $\begin{aligned} & 39 \\ & 38 \\ & 29 \end{aligned}$ | - |
| Isolation, TX to RX in TX Mode |  | dB | - | 39 35 27 | - |
| Isolation, ANT to TX in RX Mode | 1.0 GHz 2.5 GHz 5.0 GHz | dB | $\begin{aligned} & \overline{23} \\ & 17 \end{aligned}$ | 36 27 21 | - |
| Isolation, TX to RX in RX Mode | $\begin{aligned} & 1.0 \mathrm{GHz} \\ & 2.5 \mathrm{GHz} \\ & 5.0 \mathrm{GHz} \end{aligned}$ | dB | - | $\begin{aligned} & 39 \\ & 34 \\ & 27 \end{aligned}$ | - |
| ANT Port Return Loss | $1.0-5.0 \mathrm{GHz}$ | dB | - | 18 | - |
| TX Port Return Loss in TX Mode | $1.0-5.0 \mathrm{GHz}$ | dB | - | 18 | - |
| RX Port Return Loss in RX Mode | $1.0-5.0 \mathrm{GHz}$ | dB | - | 18 | - |
| TX Input P0.1dB | $1.0-5.0 \mathrm{GHz}$ | dBm | - | 41 | - |
| RX Input P0.1dB | $1.0-5.0 \mathrm{GHz}$ | dBm | - | 30.5 | - |
| TX Input IP3 | +20 dBm per tone, 10 MHz spacing | dBm | - | 54.5 | - |
| RX Input IP3 | +15 dBm per tone, 10 MHz spacing 1.0 GHz 2.5 GHz 5.0 GHz | dBm | - | 49 55 51 | - |

Electrical Specifications: $\mathrm{V}_{\mathrm{DD}}=+\mathbf{5 . 0} \mathrm{V}, \mathrm{T}_{\mathrm{BASE}}=+25^{\circ} \mathrm{C}, \mathrm{Z}_{\mathbf{0}}=50 \Omega$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Switching Speed, TX Mode <br> Ton <br> Toff <br> TRISE <br> $\mathrm{T}_{\text {FALL }}$ | 50\% control to 90\% Voltage $50 \%$ control to $10 \%$ Voltage $10 \%$ to $90 \%$ Voltage $90 \%$ to $10 \%$ Voltage | ns | - | $\begin{gathered} 410 \\ 135 \\ 140 \\ 45 \end{gathered}$ | - |
| Switching Speed, RX Mode <br> Ton <br> Toff <br> TRISE <br> $\mathrm{T}_{\text {FALL }}$ | 50\% control to 90\% Voltage $50 \%$ control to $10 \%$ Voltage 10\% to $90 \%$ Voltage $90 \%$ to $10 \%$ Voltage | ns | - | $\begin{gathered} 190 \\ 80 \\ 40 \\ 26 \end{gathered}$ | - |
| Supply Voltage, VDD | - | V | +4.75 | +5.0 | +5.25 |
| VDD Quiescent Current | TX Mode RX Mode | mA | - | $\begin{aligned} & 1.4 \\ & 6.0 \end{aligned}$ | - |
| Vcntrl Control Voltage | Logic High, $\mathrm{V}_{\mathrm{IH}}$ Logic Low, $\mathrm{V}_{\text {IL }}$ | V | $\begin{gathered} +1.17 \\ 0 \end{gathered}$ | $\begin{gathered} +1.8 \\ 0 \end{gathered}$ | $\begin{aligned} & +1.8 \\ & 0.63 \end{aligned}$ |
| T/R Logic Input Current | Logic High, $\mathrm{V}_{\mathrm{IH}}$ Logic Low, $\mathrm{V}_{\text {IL }}$ | $\mu \mathrm{A}$ | - | $\begin{gathered} 40 \\ +/-0.03 \end{gathered}$ | - |

## Maximum Operating Ratings

| Parameter | Maximum |
| :---: | :---: |
| Input Power, TX Path $^{7}$ | 40 dBm |
| Input Power, RX Path $^{7}$ | 29 dBm |
| VDD | -0.25 to +5.25 V |
| Vcntrl | 0 to 1.8 V |
| Junction Temperature $^{8}$ | $+125^{\circ} \mathrm{C}$ |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+105^{\circ} \mathrm{C}$ |

7. Based on testing with input power applied for 30 seconds.
8. Guarantees $10^{6}$ hour lifetime.

## Truth Table

| Control Input | T/R Path |  |
| :---: | :---: | :---: |
| VcntrI | RX | TX |
| $V_{I H}$ | On | Off |
| $V_{I L}$ | Off | On |

Absolute Maximum Ratings ${ }^{9,10}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power, TX Path | 41 dBm |
| Input Power, RX Path | 30 dBm |
| VDD | -0.5 to +5.5 V |
| Vcntrl | -0.5 to +2.75 V |
| Storage Temperature | $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ |

9. Exceeding any one or combination of these limits may cause permanent damage to this device.
10.MACOM does not recommend sustained operation near these survivability limits.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1 B devices.

## PCB Layout



## Parts List

| Part | Value | Case Style |
| :---: | :---: | :---: |
| C 1 | 10 pF | 0402 |
| C 2 | 1000 pF | 0402 |
| C 3 | $1 \mu \mathrm{~F}$ | 0402 |
| C 4 | 8.2 pF | 0402 |
| C5 | 0.2 pF | $0402(100 \mathrm{~V})$ |
| C 6 | 5 pF | 0402 |
| R1,R2 | $0 \Omega$ | 0402 |
| $\mathrm{~J} 1-\mathrm{J} 5$ | $142-0761-841$ | SMA, End Launch |

## Application Schematic



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## Typical Performance Curves

Insertion Loss, ANT to TX


Isolation, ANT to RX in TX Mode


Isolation, $T X$ to $R X$ in $T X$ Mode


Insertion Loss, ANT to RX


Isolation, ANT to TX in RX Mode


Isolation, $T X$ to $R X$ in $R X$ Mode


## Typical Performance Curves

## ANT RL in TX Mode


$T X R L$


## ANT RL in RX Mode


$R X R L$


## Lead-Free 3 mm 12-Lead PQFN ${ }^{\dagger}$



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[^0]:    * Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

[^1]:    ${ }^{\dagger}$ Reference Application Note S2083 for lead-free solder reflow recommendations.
    Meets JEDEC moisture sensitivity level (MSL) 1 requirements.
    Plating is $100 \%$ matte tin over copper.

