

Rev. V2

#### **Features**

• Low Loss: 0.6 dB, 28 to 34 GHz

• High Isolation: >26 dB

>40 W CW Power Handling @ +85°C

Switching Speed: <65 ns</li>

Integrated DC Blocks and RF Bias Networks

Die with G-S-G RF Pads and DC Bias Pads

RoHS\* Compliant

### **Description and Applications**

The MASW-011094 is a high power SPDT with 50  $\Omega$  terminated RF ports. This broadband, high linearity, SPDT switch was developed for Ka–Band applications that require up to 40 Watts CW power handling at an environmental temperature of +85°C while maintaining low insertion loss and high isolation.

The SPDT MMIC utilizes MACOM's proven AlGaAs PIN diode technology. The switch is fully passivated with silicon nitride and has an added polymer layer for scratch protection. The protective coating prevents damage to the junction and the air-bridges during handling and assembly. The die has backside metallization to facilitate an epoxy die attach process.

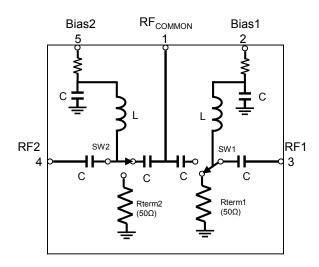
These switches are ideally suited for satellite and Point-to-Point communications systems, radar systems, radiometers, test and instrumentation equipment and other high frequency applications.

# Ordering Information<sup>1</sup>

Part Number	Package
MASW-011094-DIE0G	Die in Gel Pack
MASW-011094-DIERI	Die, Inked on Wafer Ring

1. Die quantity varies.

#### **Functional Schematic**



# Pin Configuration (Backside metal is RF, DC, and thermal ground)

Pin No.	Function	
1	RF <sub>COMMON</sub>	
2	Bias 1	
3	RF1	
4	RF2	
5	Bias 2	

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<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

# MASW-011094



# Ka-Band High Power Terminated SPDT PIN Switch 24 - 37 GHz

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# Electrical Specifications: $T_A = 25$ °C, $V_R^2 = -5$ V, $V_F = +7.5$ V, $Z_0 = 50$ $\Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss (RF <sub>COMMON</sub> to RFx ON state)	26 - 28 GHz 28 - 32 GHz 32 - 36 GHz 36 - 37 GHz	dB	_	0.8 0.6 0.8 1.0	1.0 —
Isolation (RF <sub>COMMON</sub> to RFx OFF state) <sup>3</sup>	26 - 28 GHz 28 - 32 GHz 34 GHz 32 - 36 GHz 36 - 37 GHz	dB		26 27 28 28 29	_
Return Loss (RF <sub>COMMON</sub> )	26 - 28 GHz 28 - 32 GHz 32 - 36 GHz 36 - 37 GHz	dB	_	14 17 12 11	_
Return Loss (RFx ON state)	26 - 28 GHz 28 - 32 GHz 32 - 36 GHz 36 - 37 GHz	dB	_	14 18 12 10	_
Return Loss (RFx OFF state)	26 - 28 GHz 28 - 32 GHz 32 - 36 GHz 36 - 37 GHz	dB	10 —	10.5 15.0 11.0 9.5	_
CW Power Handling (ON state) <sup>2</sup>	28.5 GHz, -60 V @ +85°C	dBm	_	46	_
Switching Speed T <sub>RISE</sub> / T <sub>FALL</sub>	10% - 90% RF, 26.5 GHz	ns	_	16 / 34	_
Switching Speed T <sub>ON</sub> / T <sub>OFF</sub>	50% control to 90% RF, 26.5 GHz	ns	_	42 / 65	_
I <sub>R</sub> - Reverse Bias Current	V <sub>R</sub> = -60 V	nA	_	50	_
I <sub>F</sub> - Forward Bias Current <sup>4</sup>	V <sub>F</sub> = +7.5 V	mA	30	38	45

<sup>2.</sup> Reverse bias voltage should be determined based on working conditions. For example, -60 V @ 46 dBm input power. For lower power applications, a less negative voltage can be used. R. Caverly and G. Hiller, "Establishing the Minimum Reverse Bias for a P-I-N Diode in a High Power Switch," IEEE Transactions on Microwave Theory and Techniques, Vol.38, No.12, December 1990.

<sup>3.</sup> Isolation defined with 1 port in low loss state.

<sup>4.</sup> Forward bias voltage should be determined based on working conditions.



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## **Absolute Maximum Ratings**<sup>5,6,7</sup>

Parameter	Absolute Maximum	
Reverse Bias Voltage	-100 V	
Forward Bias Current	83 mA	
Forward Bias Voltage	11 V	
Incident Power (ON path)	47 dBm	
Junction Temperature	+150°C	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-55°C to +150°C	

<sup>5.</sup> Exceeding any one or combination of these limits may cause permanent damage to this device.

#### **Truth Table**

State	B1	B2
RF <sub>COMMON</sub> to RF1 ON	-5 V	+7.5 V
RF <sub>COMMON</sub> to RF2 ON	+7.5 V	-5 V

### **Handling Procedures**

Please observe the following precautions to avoid damage:

### Static Sensitivity

Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM class 1A devices.

MACOM does not recommend sustained operation near these survivability limits.

Operating at nominal conditions with junction temperature less than 150°C will ensure MTTF >1 x 10<sup>6</sup> hours.

# MASW-011094

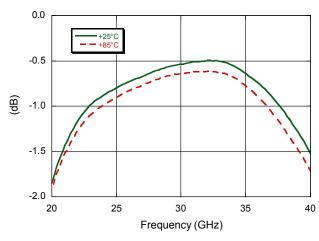


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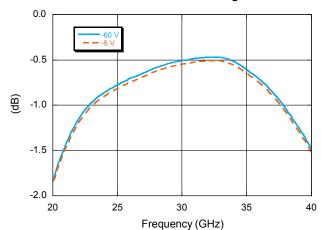
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### **Typical Performance Curves**

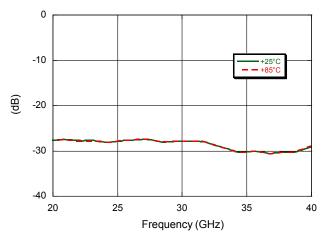
#### Insertion Loss



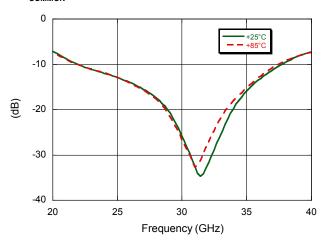
#### Insertion Loss over Reverse Bias Voltage



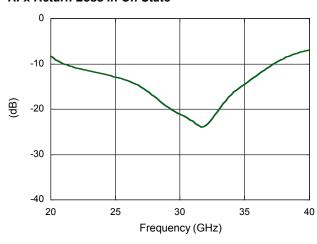
#### Isolation RF<sub>COMMON</sub> to RFx



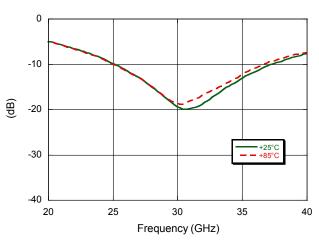
#### RF<sub>COMMON</sub> Return Loss in ON State



#### RFx Return Loss in On State



#### Return Loss of Terminated Port RFx



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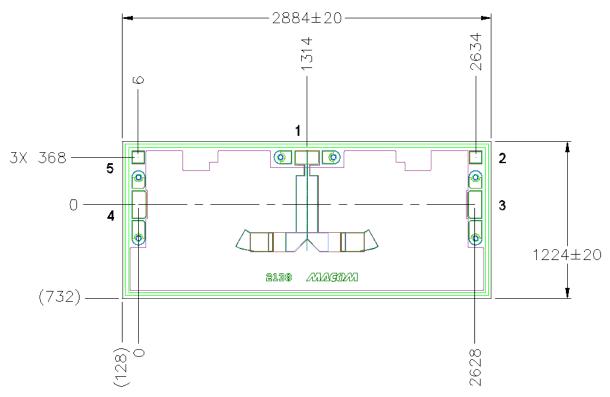
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### **Outline Drawing**



#### Notes:

Unless otherwise specified, All dimensions shown as  $\mu m$  with a tolerance of  $\pm 5~\mu m$ . Die thickness is 100  $\mu m$   $\pm 12.5~\mu m$ .

Pad 1 = 178 x 101  $\mu$ m.

Pads 2 & 5 = 88  $\mu m$  sq.

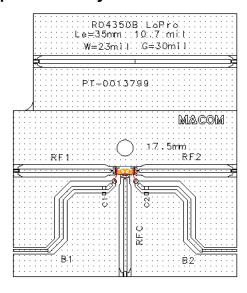
Pads 3 & 4 =  $101 \times 208 \mu m$ .



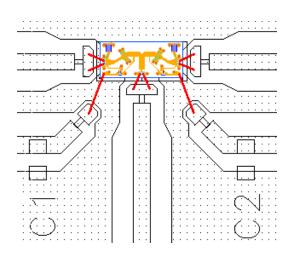
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### **Applications Section**

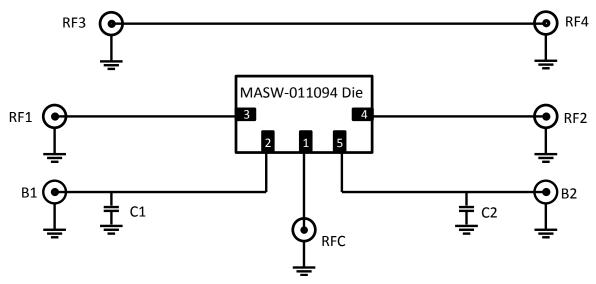
## **Sample Board Layout**



## Sample Board Layout - Detailed View



## **Sample Board Schematic**



Component Designator	Description	P/N
RFC, RF1, 2, 3, and 4	2.4 mm - Southwest Microwave connector	1492-03A
B1, B2	Johnson/Emerson RF connector Or SSMA - Southwest Microwave connector	142-0761-821 292-06A
C1, C2	22 pF High Frequency Capacitor	ATC600L220

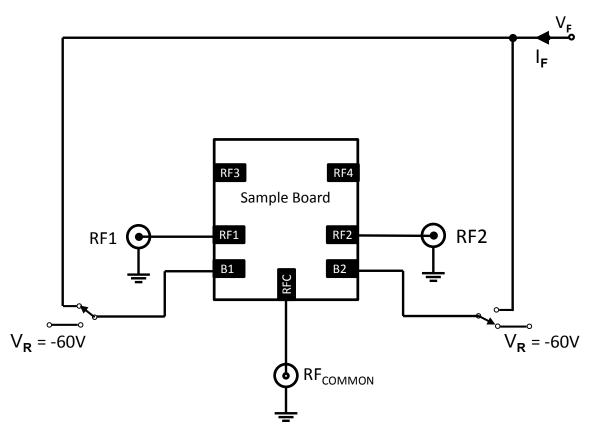


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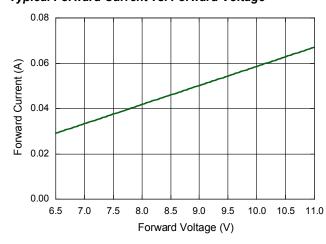
### **Applications Section**

### **Sample Board Application Schematic**

The schematic shown below depicts the switch in ON state of the RF<sub>COMMON</sub> to RF2 and RF1 input in OFF state. The switch bias conditions are set for high power (40 W) application.



#### Typical Forward Current vs. Forward Voltage



### **Truth Table**

State	B1	B2
RF <sub>COMMON</sub> to RF1 ON	-60 V	+7.5 V
RF <sub>COMMON</sub> to RF2 ON	+7.5 V	-60 V

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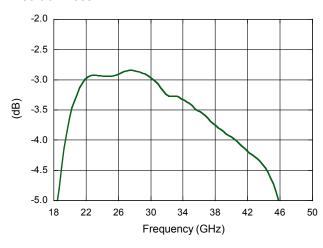


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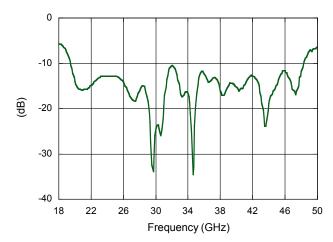
## **Applications Section**

# **Typical Sample Board Performance (Not De-embedded)**

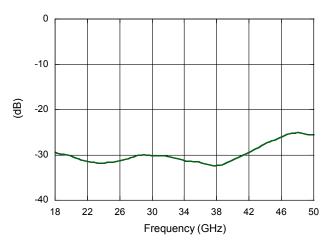
#### Insertion Loss



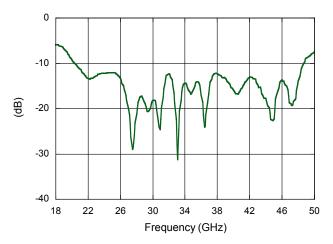
#### RF1 and RF2 Return Loss in On State



#### Isolation RF1 and RF2



#### RF<sub>COMMON</sub> Return Loss in ON State



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