## Digital Phase Shifter 6-Bit, 2.3 - 3.8 GHz

#### Features

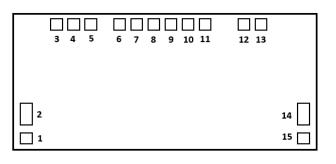
- 6 Bit Digital Phase Shifter
- 360° Coverage with LSB = 5.6°
- Low DC Power Consumption
- Minimal Attenuation Variation over Phase Shift Range
- 50 Ω Impedance
- EAR99
- RoHS\* Compliant

#### Description

The MAPS-010164-DIE is a GaAs pHEMT 6-bit digital phase shifter. Step size is 5.6° providing phase shift from 0° to 360° in 5.6° steps. This design has been optimized to minimize variation in attenuation over the phase shift range.

The MAPS-010164-DIE is ideally suited for use where high phase accuracy with minimum loss variation over the phase shift range are required. Typical applications include communications antennas and phased array radars.

### **Functional Schematic**



## Pad Configuration<sup>1</sup>

Pad #	Name	Function			
1, 15	GND	Ground			
2	RF <sub>IN</sub>	RF Input			
3	A1	5.6° Control			
4	B1	5.6° Control			
5	A2	11.2° Control			
6	A3	22.5° Control			
7	B3	22.5° Control			
8	A4	45° Control			
9	B4	45° Control			
10	A5	90° Control			
11	B5	90° Control			
12	A6	180° Control			
13	B6	180° Control			
14	RF <sub>out</sub>	RF Output			

1. The backside of the die must be connected to RF, DC, and thermal ground.

## Ordering Information

Part Number	Package				
MAPS-010164-DIE	50 piece Gel Pak				

\* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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## Electrical Specifications: Freq. = 2.3 - 3.8 GHz, $T_A$ = 25°C, $Z_0$ = 50 $\Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	Any Phase State	dB	—	3.6	4.5
Attenuation Variation	Across All Phase States	dB	—	± 0.6	—
RMS Attenuation Error <sup>2</sup>	All Values Relative to Insertion Loss at Reference Phase	dB	_	0.3	_
RMS Phase Error <sup>2</sup>	All Values Relative to Reference Phase	deg.	—	3	—
Phase Relative to Reference Loss State	5.6° Bit 11.2° Bit 22.5° Bit 45° Bit 90° Bit 180° Bit Sum of All Bits	deg.	_	7.0 10.5 24 47 90 178 356	_
VSWR	RF IN RF OUT	Ratio	_	1.3:1 1.3:1	_
1 dB Compression	Reference State	dBm	_	25	—
Input IP3	Two-tone inputs up to 5 dBm	dBm	—	45	—
T <sub>RISE</sub> , T <sub>FALL</sub>	10% to 90% RF, 90% to 10% RF	ns	_	50	_
VL VH	LOW-level input voltage HIGH-level input voltage	V		-5 0	—

2. RMS is calculated across all 63 amplitude or phase states relative to the amplitude or phase in the 0° phase state at a given frequency.

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### Truth Table<sup>3</sup>

A1	B1	A2	A3	В3	A4	B4	A5	B5	A6	B6	Phase Shift
VL	V <sub>H</sub>	$V_{\text{L}}$	VL	V <sub>H</sub>	VL	$V_{\text{H}}$	VL	V <sub>H</sub>	VL	V <sub>H</sub>	Reference Phase
V <sub>H</sub>	VL	$V_{\text{L}}$	VL	V <sub>H</sub>	VL	$V_{\text{H}}$	VL	V <sub>H</sub>	VL	V <sub>H</sub>	5.6°
VL	V <sub>H</sub>	$V_{\text{H}}$	VL	V <sub>H</sub>	VL	$V_{\text{H}}$	VL	V <sub>H</sub>	VL	V <sub>H</sub>	11.2°
VL	V <sub>H</sub>	VL	V <sub>H</sub>	VL	VL	V <sub>H</sub>	VL	V <sub>H</sub>	VL	V <sub>H</sub>	22.5°
VL	V <sub>H</sub>	$V_{\text{L}}$	VL	V <sub>H</sub>	V <sub>H</sub>	$V_{L}$	VL	V <sub>H</sub>	VL	V <sub>H</sub>	45°
VL	V <sub>H</sub>	$V_{\text{L}}$	VL	V <sub>H</sub>	VL	$V_{\text{H}}$	V <sub>H</sub>	VL	VL	V <sub>H</sub>	90°
VL	V <sub>H</sub>	VL	VL	V <sub>H</sub>	VL	V <sub>H</sub>	VL	V <sub>H</sub>	V <sub>H</sub>	VL	180°
V <sub>H</sub>	VL	V <sub>H</sub>	V <sub>H</sub>	VL	354.4°						

3. V<sub>L</sub> = -5 V, V<sub>H</sub> = 0 V.

## Absolute Maximum Ratings<sup>4,5</sup>

Parameter	Absolute Maximum				
Input Power 2.3 - 3.8 GHz	27 dBm				
Operating Temperature	-40°C to +85°C				
Storage Temperature	-65°C to +150°C				

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

5. MACOM does not recommend sustained operation near these survivability limits.

#### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



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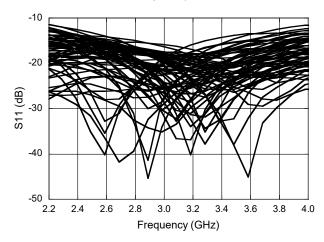
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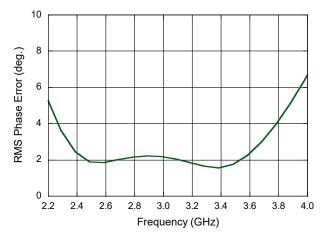
## Digital Phase Shifter 6-Bit, 2.3 - 3.8 GHz

## **Typical Performance Curves**

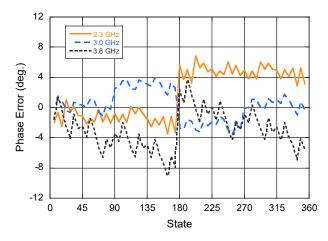
RF<sub>IN</sub> Return Loss vs. Frequency (All States)



Mean RMS Phase Error vs. Frequency

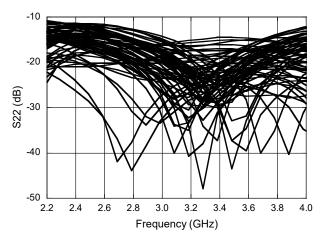


Phase Error vs. State

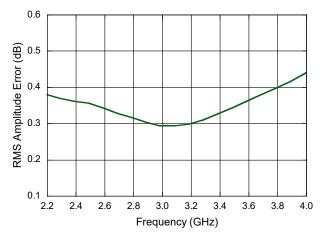


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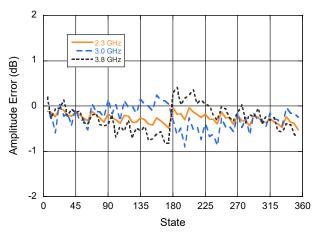
RFout Return Loss vs. Frequency (All States)



Mean RMS Amplitude Error vs. Frequency





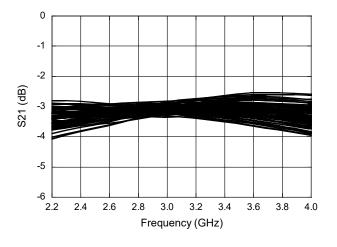


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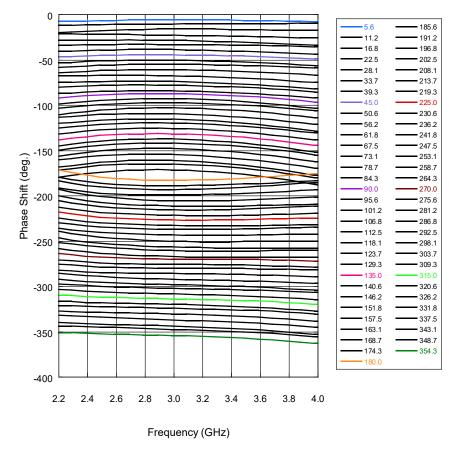


### **Typical Performance Curves**

#### Amplitude Variation vs. Phase State



#### Phase Shift vs. Frequency (All States)





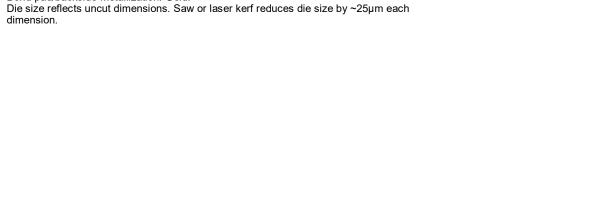
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## Outline Drawing 6,7,8,9



Unless otherwise specified, all dimensions are um with a tolerance of  $\pm5\mu\text{m}.$ 

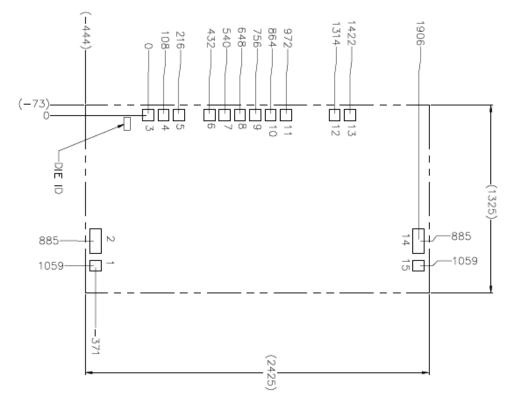
6. 7.

8. 9. Die thickness is 100 ±10µm.

Bond pad/backside metallization: Gold.

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