Features

- Dual Channel Architecture
- Broadband: 2.3 - 4.2 GHz
- High Power Switch Handling (T_C = 105°C):
  43 dBm LTE 8 dB PAR (<10 s, single event)
  40 dBm LTE 8 dB PAR (Lifetime)
- Second LNA has Bypass Mode
- Rx High Gain Mode:
  Gain: 37.5 dB at 2.6 GHz, 37 dB @ 3.5 GHz
  NF: 1.25 dB at 2.6 GHz, 1.4 dB @ 3.5 GHz
  OIP3: 35 dBm
- Rx Low Gain Mode:
  Gain: 18.5 dB at 2.6 GHz, 19.0 dB @ 3.5 GHz
  NF: 1.25 dB at 2.6 GHz, 1.35 dB @ 3.5 GHz
  OIP3: 32 dBm
- Single 5 V Supply, 104 mA per channel
- Compatible with 1.8 V and 3.3 V logic
- Lead-Free 6 mm 40-Lead QFN Package
- RoHS* Compliant

Applications

- 5G Massive MIMO
- Wireless Infrastructure
- TDD-based communication systems

Description

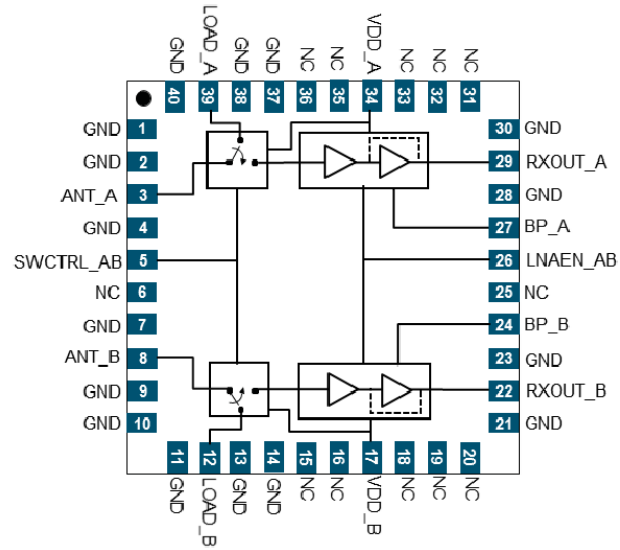
The highly integrated Dual Channel Switch and LNA Module includes two Antenna Switches and two 2-stage low noise amplifiers in a compact low cost 6 mm QFN package. The second stage LNAs can be bypassed. Mixed technologies are used to achieve high power handling, low noise figure, and low power consumption. The module only needs a single +5 V supply. T/R switch, LNA enable, and bypass function can be controlled with 1.8 V or 3.3 V logic.

Ordering Information[1]

Part Number	Package
MAMF-011145-TR1000	1K Reel
MAMF-011145-001SMB	Sample Board

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration[2,3,4]

Pin #	Function
1, 2, 4, 7, 9-11, 14, 21, 23, 28, 30, 37, 40	Ground
3	Antenna Input ChA
5	Switch Control ChA&B
6, 13, 15, 16, 18-20, 25, 31-33, 35, 36, 38	No Connect
8	Antenna Input ChB
12	Load ChB
17	Switch/LNA V_{DD} ChB
22	Rx Output ChB
24	LNA Bypass ChB
26	LNA Enable ChA&B
27	LNA Bypass ChA
29	Rx Output ChA
34	Switch/LNA V_{DD} ChA
39	Load ChA

2. Blocking Capacitors are required on all RF Ports.
3. MACOM recommends connecting unused package pins to ground.
4. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

Electrical Specifications: Freq. = 2.6 GHz, P_{IN} = -35 dBm, T_C = +25°C, V_{DD} = 5 V, Z_0 = 50 Ω

Parameter	Conditions	Units	Min.	Typ.	Max.
Gain @ Rx High Gain Mode	2.6 GHz 3.5 GHz	dB	34	37.5 37.0	—
Noise Figure @ Rx High Gain Mode	2.6 GHz 3.5 GHz	dB	—	1.25 1.40	—
Input RL @ Rx High Gain Mode	2.6 GHz 3.5 GHz	dB	—	17 15	—
Output RL @ Rx High Gain Mode	2.6 GHz 3.5 GHz	dB	—	13 13	—
Output IP3 @ Rx High Gain Mode	Tone Spacing = 10 MHz P_{OUT} / Tone = +3 dBm P_{OUT} / Tone = +10 dBm	dBm	—	33 35	—
Output P1dB @ Rx High Gain Mode	2.6 GHz 3.5 GHz	dBm	—	19 18	—
Gain @ Rx Low Gain Mode	2.6 GHz 3.5 GHz	dB	17	18.5 19.0	—
Noise Figure @ Rx Low Gain Mode	2.6 GHz 3.5 GHz	dB	—	1.25 1.35	—
Input RL @ Rx Low Gain Mode	2.6 GHz 3.5 GHz	dB	—	14 15	—
Output RL @ Rx Low Gain Mode	2.6 GHz 3.5 GHz	dB	—	11 13	—
Output IP3 @ Rx Low Gain Mode	Tone Spacing = 10 MHz P_{OUT} / Tone = +3 dBm	dBm	—	32	—
Output P1dB @ Rx Low Gain Mode	2.6 GHz 3.5 GHz	dBm	—	16 15	—
Insertion Loss @ Tx Mode	2.6 GHz 3.5 GHz	dB	—	0.25 0.30	0.7
Return Loss @ Tx Mode	2.6 GHz 3.5 GHz	dB	—	25 20	—
Power Handling @ Tx Mode	Average Power (8 dB PAR)	W	—	10	—
Supply Voltage	—	V	4.75	5	5.25
Control Voltage	Logic High Logic Low	V	1.2 0	—	3.45 0.6
Logic Input Current	Logic High Logic Low	µA	—	+80 -2	—
Supply Current (V_{DD}) per Channel	Rx High Gain Rx Low Gain Tx mode	mA	—	104 40 1.5	—

DC-0025669

Electrical Specifications: Freq. = 2.6 GHz, P_{IN} = -35 dBm, T_C = +25°C, V_{DD} = 5 V, Z_0 = 50 Ω

Parameter	Conditions	Units	Min.	Typ.	Max.
RF Switching Time	50% CTL to 10/90% RF	ns	—	500	—
High/Low Gain Mode Switching Time	50% CTL to 10/90% RF	ns	—	150	—
Isolation Between Rx Channels[5]	—	dB	—	45	—
Switch Isolation, ANT to Load	Rx Mode	dB	—	22	—
Switch Isolation, ANT to Rx Output	Tx Mode	dB	—	72	—

5. Test conditions: both Rx channels are enabled. RF signal is present at Antenna port on one of the channels only. The isolation is defined as the difference between the 2 RX output signal levels.

Control Truth Table

Mode	SWCTRL_AB	LNAEN_AB	BP_A/B	Note
RX mode	Low or open	Low or open	Low	HGM[6]
RX mode	Low or open	Low or open	High	LGM[7]
TX mode	High	High	Low	Power down
TX mode	High	High	High	Power down

6. HGM: High Gain Mode.
7. LGM: Low Gain Mode.

Absolute Maximum Ratings[8,9]

Parameter	Absolute Maximum
Antenna Input Power[10] Freq. = 2.6 GHz: RX Mode TX Mode	 22 dBm LTE (8 dB PAR), 22 dBm CW 43 dBm LTE (8 dB PAR), 43 dBm CW
DC Voltages: ANT_A/B, LOAD_A/B, RX_{OUT}_A/B V_{DD}_A/B, SWCTRL_A/B, LNAEN_A/B, BP_A/B	 -0.3 to +3.6 V -0.3 to +5.5 V -0.3 to +3.6 V
Junction Temperature: RX Mode[11,13] TX Mode[11,13] TX Mode[10]	 +150°C +125°C +140°C
Operating Temperature[12]	-40°C to +105°C
Storage Temperature	-55°C to +150°C

8. Exceeding any one or combination of these limits may cause permanent damage to this device.
9. MACOM does not recommend sustained operation near these survivability limits.
10. Single event, up to 10 seconds duration.
11. Operating at nominal conditions with $T_J \leq +150°C$ (RX Mode) and $T_J \leq +125°C$ (TX Mode) will ensure MTTF >> 1 x 10^6 hours.
12. Operating/Case temperature (T_C) is the temperature of the exposed paddle.
13. Junction Temperature (T_J) = T_C + Θ_{JC} * P_{DISS} where P_{DISS} is the total DC & RF dissipated power.
    - RX Mode: Typical thermal resistance (Θ_{JC}) = 33.4 ºC/W.
    - TX Mode: Typical thermal resistance (Θ_{JC}) = 9.8 ºC/W.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

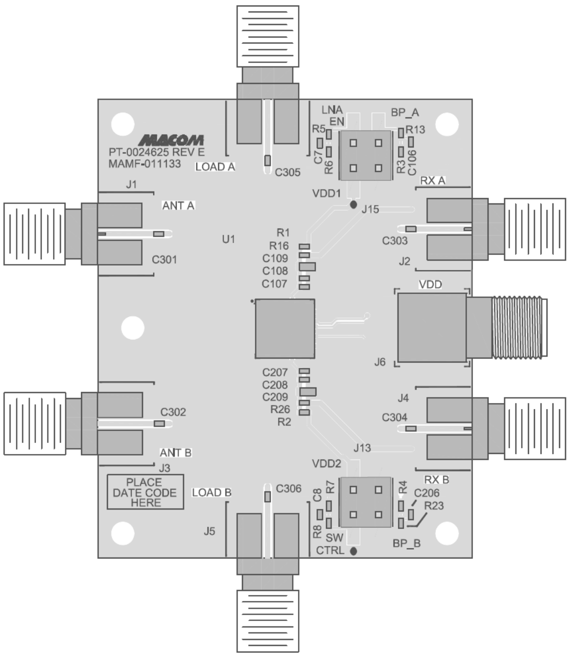
These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Parameter	Rating	Standard
Human Body Model (HBM)	500 V (Class 1B)	ESDA/JEDEC JS-001
Charged Device Model (CDM)	1000 V (Class C3)	ESDA/JEDEC JS-002

Power Supplies

De-coupling capacitors should be placed at the V_{DD} supply pin to minimize noise and fast transients. Supply voltage change or transients should have a slew rate smaller than 1 V / 10 µs. In addition, all control pins should remain at 0 V (+/- 0.3 V) and no RF power should be applied while the supply voltage ramps or while it returns to zero.

PCB Layout

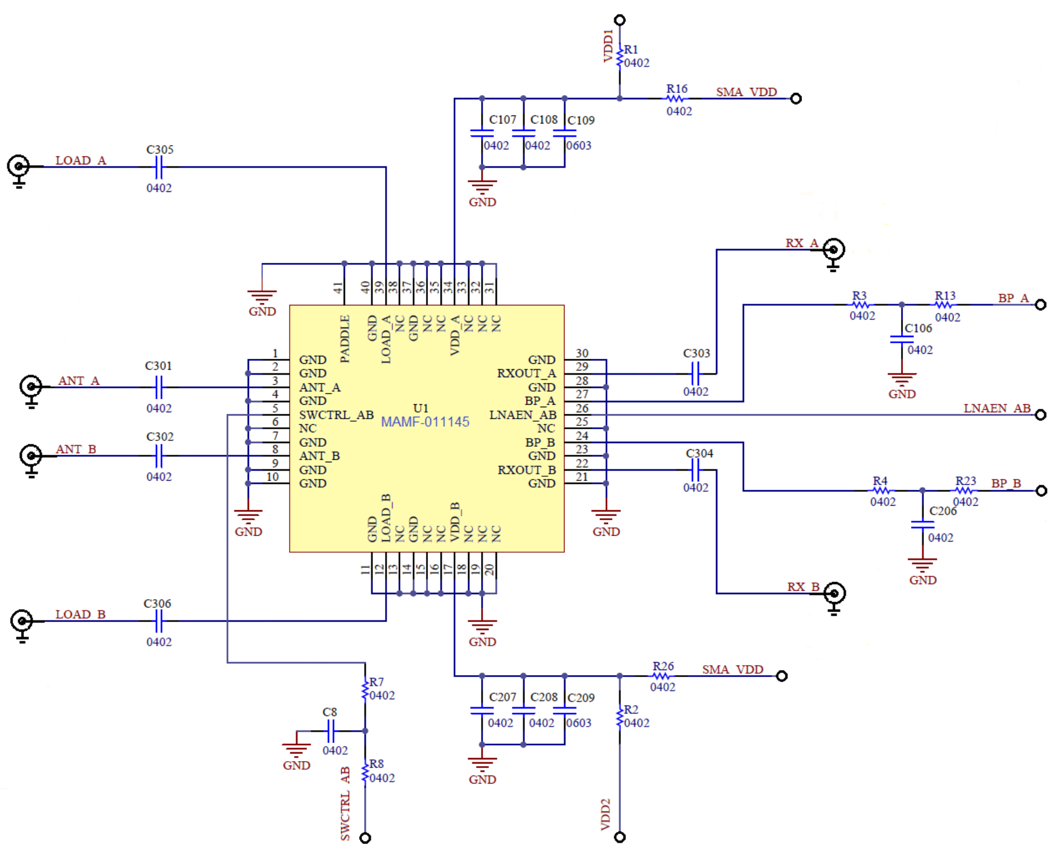


Parts List

Part	Value	Case Style
C7, C8, C106, C206	5 pF	0402
C107, C207	470 pF	0402
C108, C208	10 nF	0402
C109, C209	10 µF	0603
C301 - C306	20 pF	0402
R1, R2, R3, R4, R6, R7	0 R	0402
R16, R26	DNP	0402
R5, R8, R13, R23	1 kΩ	0402

14. Proposed SMB parts list provides supply biasing for CH1 and CH2 via DC headers (J15/J13) with separate V_{DD1} and V_{DD2} supplies. A single V_{DD} supply may also be provided at the SMA connector (J6) by removing R1/R2 and populating R16/R26 with 0 R instead.

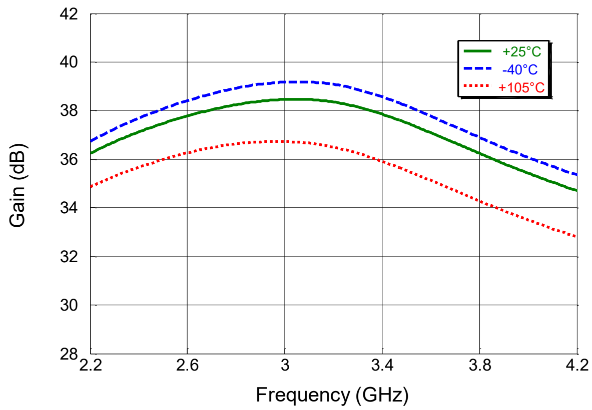
Application Schematic



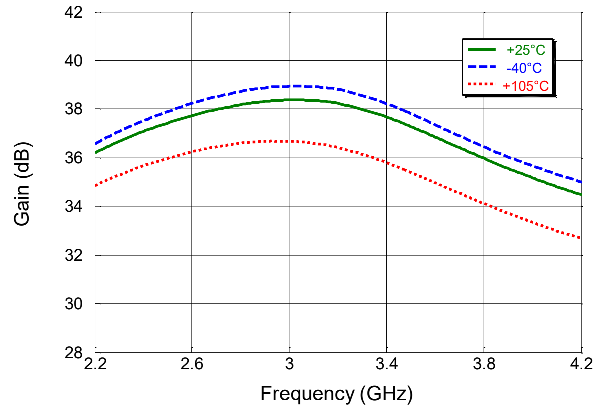
Typical Performance Curves

P_{IN} = -35 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

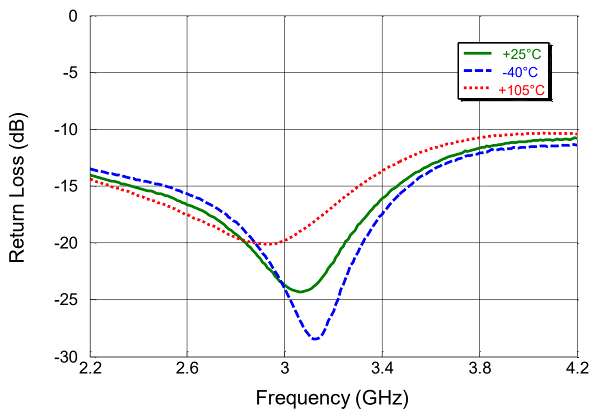
Channel A LNA Gain over swept Frequency (& Temp.) in Rx High Gain Mode



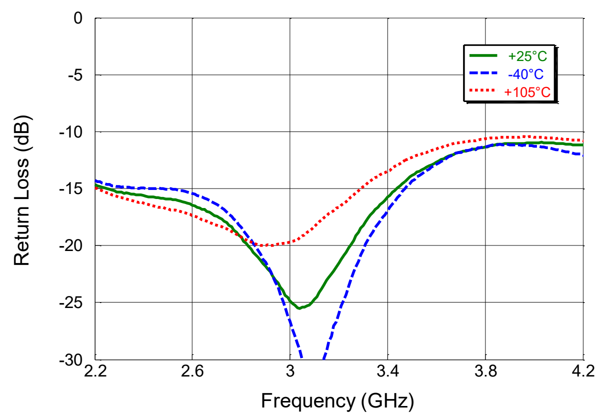
Channel B LNA Gain over swept Frequency (& Temp.) in Rx High Gain Mode



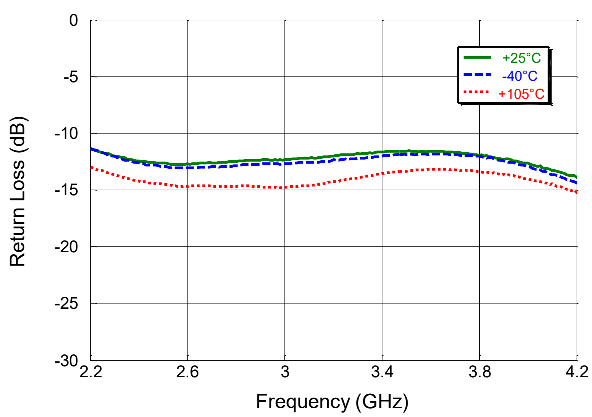
Channel A ANT Port Return Loss over swept Frequency (& Temp.) in Rx High Gain Mode



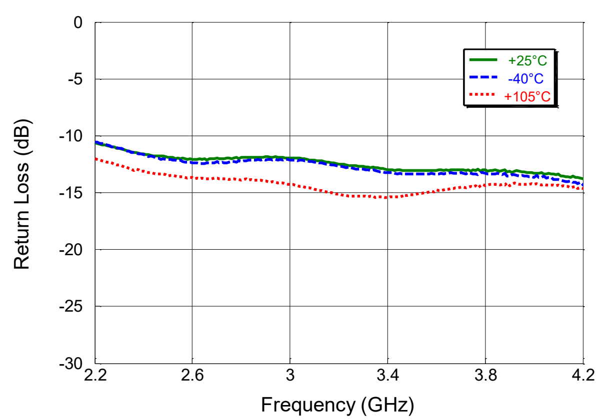
Channel B ANT Port Return Loss over swept Frequency (& Temp.) in Rx High Gain Mode



Channel A RXOUT Port Return Loss over swept Frequency (& Temp.) in Rx High Gain Mode



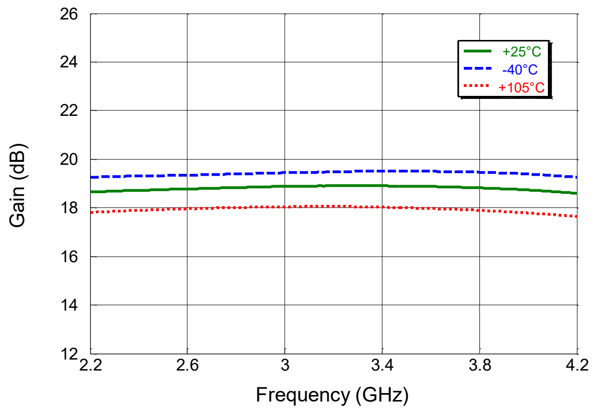
Channel B RXOUT Port Return Loss over swept Frequency (& Temp.) in Rx High Gain Mode



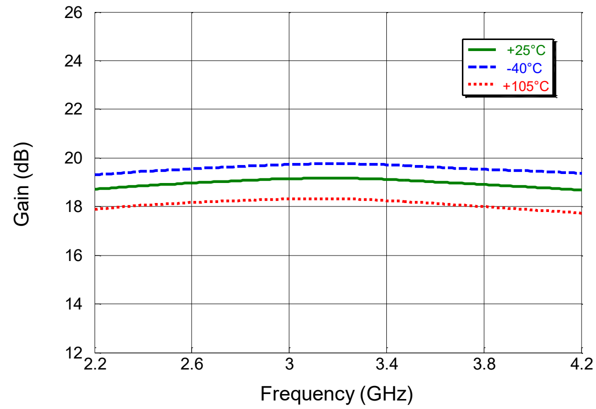
Typical Performance Curves

P_{IN} = -35 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

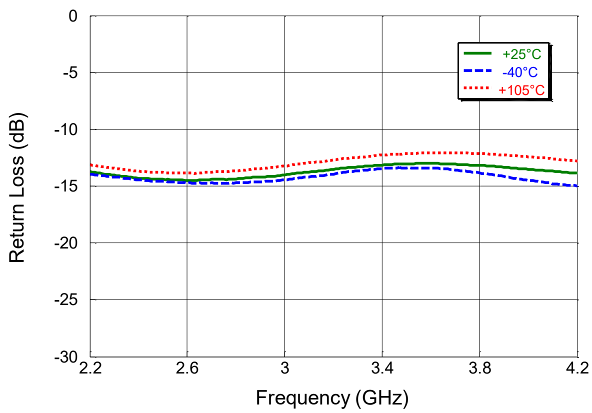
Channel A LNA Gain over swept Frequency (& Temp.) in Rx Low Gain Mode



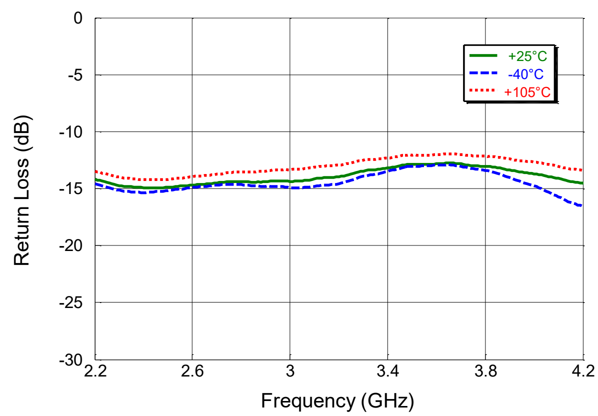
Channel B LNA Gain over swept Frequency (& Temp.) in Rx Low Gain Mode



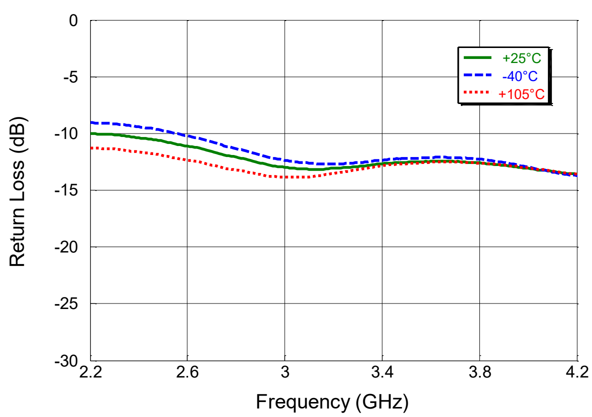
Channel A ANT Port Return Loss over swept Frequency (& Temp.) in Rx Low Gain Mode



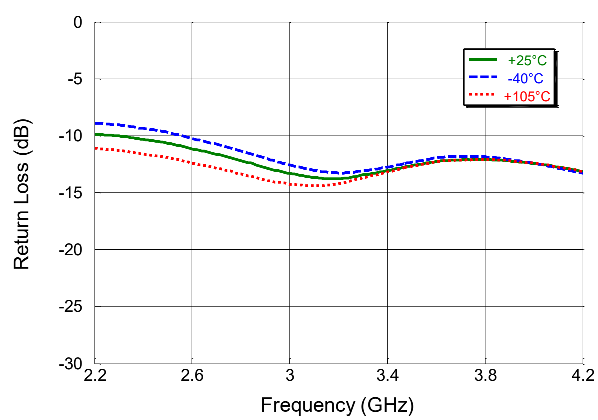
Channel B ANT Port Return Loss over swept Frequency (& Temp.) in Rx Low Gain Mode



Channel A RXOUT Port Return Loss over swept Frequency (& Temp.) in Rx Low Gain Mode

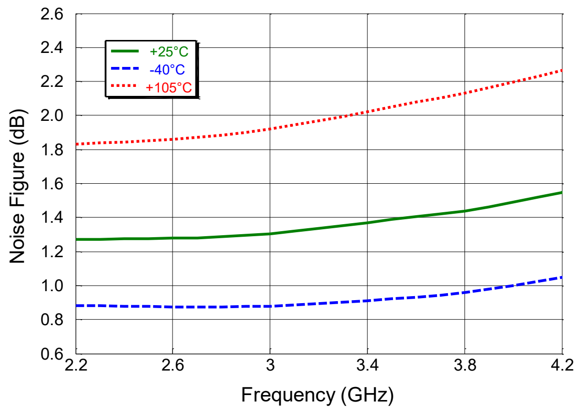


Channel B RXOUT Port Return Loss over swept Frequency (& Temp.) in Rx Low Gain Mode

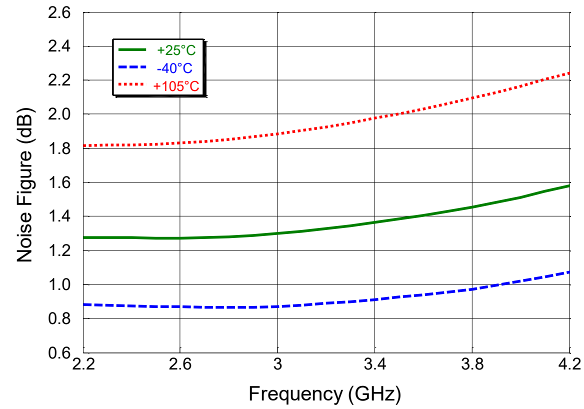


Typical Performance Curves
P_{IN} = -35 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

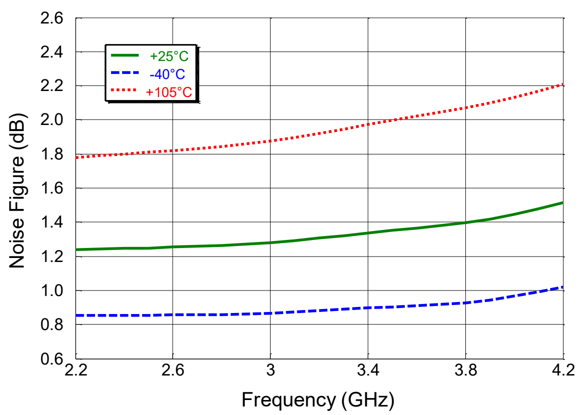
Channel A LNA Noise Figure over swept Frequency (& Temp.) in Rx High Gain Mode



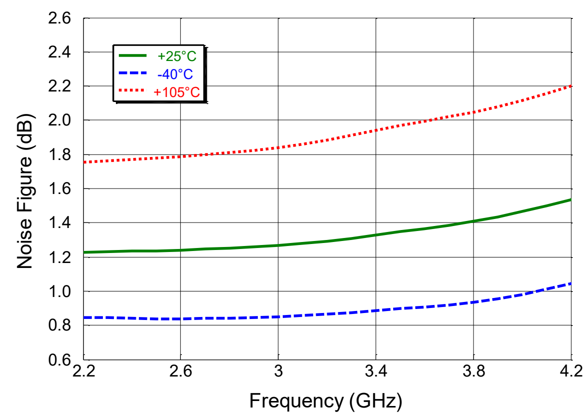
Channel B LNA Noise Figure over swept Frequency (& Temp.) in Rx High Gain Mode



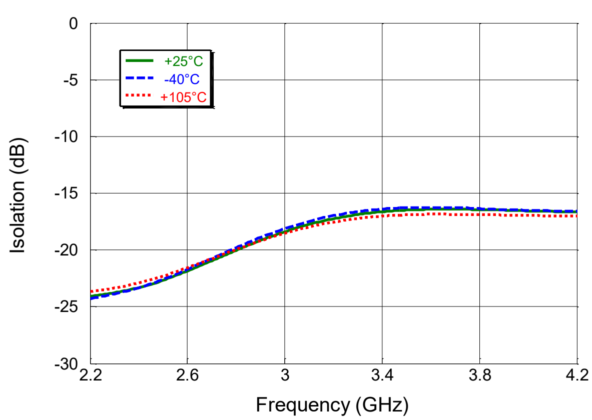
Channel A LNA Noise Figure over swept Frequency (& Temp.) in Rx Low Gain Mode



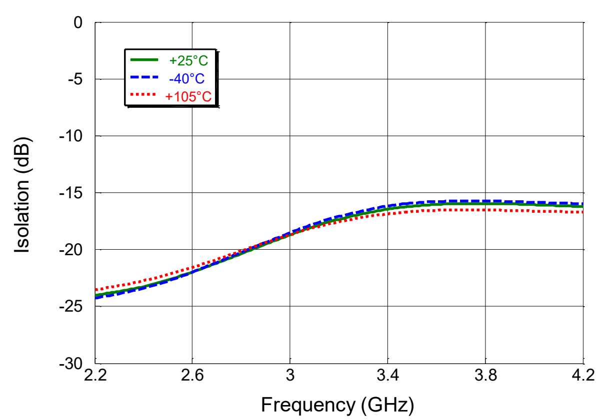
Channel B LNA Noise Figure over swept Frequency (& Temp.) in Rx Low Gain Mode



Channel A ANT to LOAD Isolation over swept Frequency (& Temp.) in Rx High Gain Mode



Channel B ANT to LOAD Isolation over swept Frequency (& Temp.) in Rx High Gain Mode



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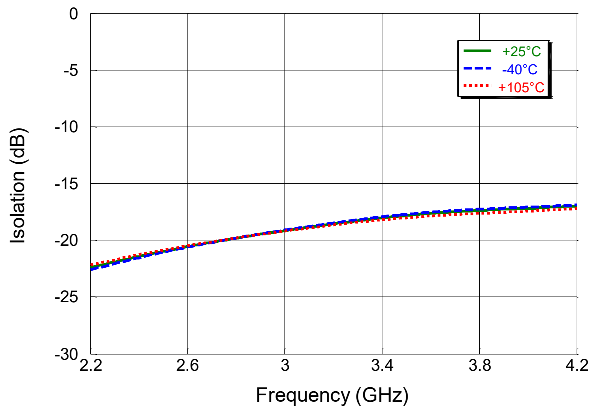
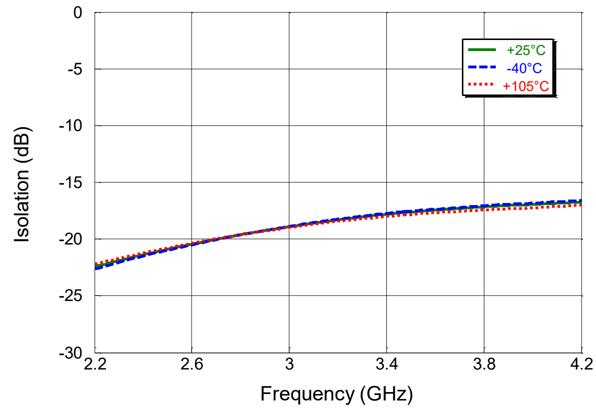
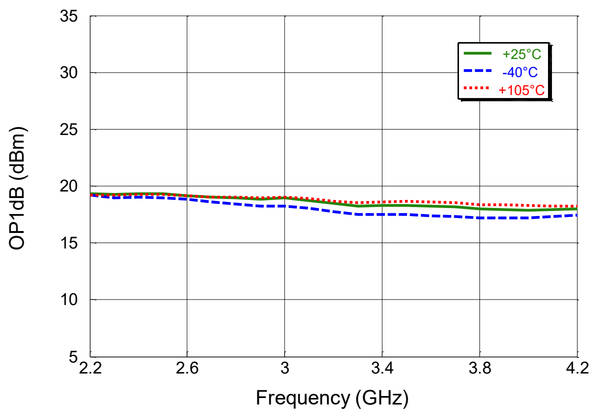
Channel A ANT to LOAD Isolation over swept Frequency (& Temp.) in Rx Low Gain Mode



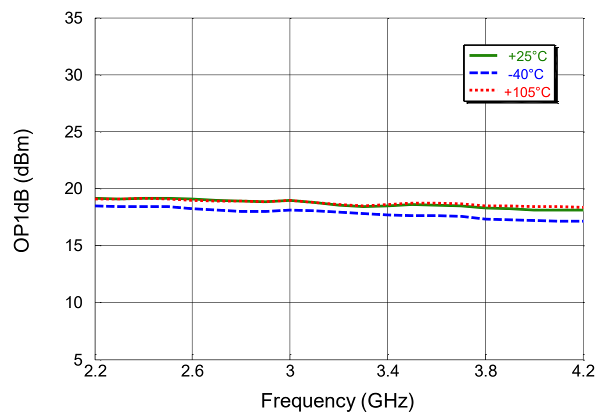
Channel B ANT to LOAD Isolation over swept Frequency (& Temp.) in Rx Low Gain Mode



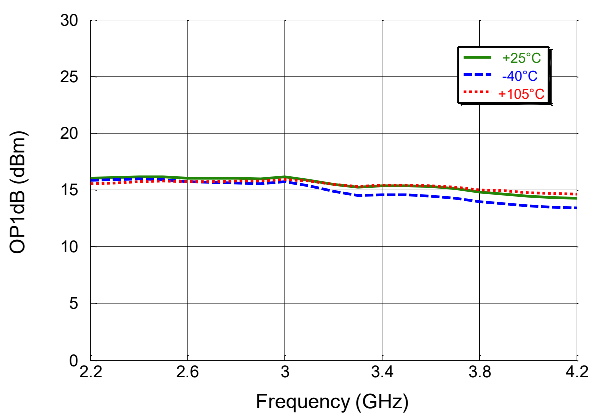
Channel A LNA Output P1dB over swept Frequency (& Temp.) in Rx High Gain Mode



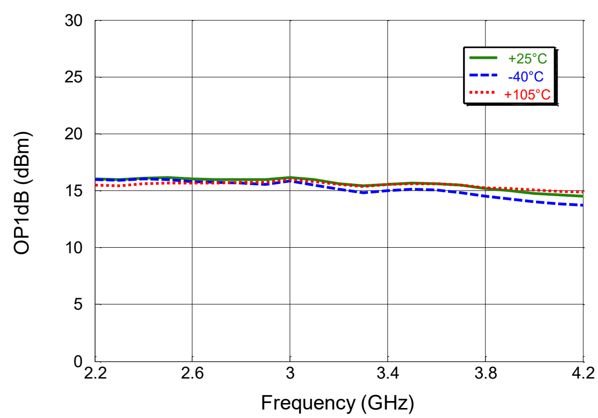
Channel B LNA Output P1dB over swept Frequency (& Temp.) in Rx High Gain Mode



Channel A LNA Output P1dB over swept Frequency (& Temp.) in Rx Low Gain Mode

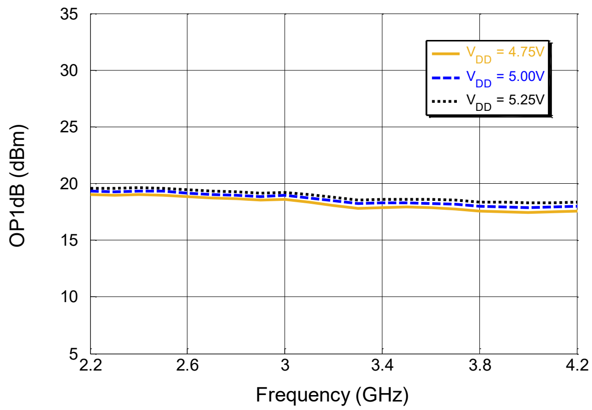


Channel B LNA Output P1dB over swept Frequency (& Temp.) in Rx Low Gain Mode

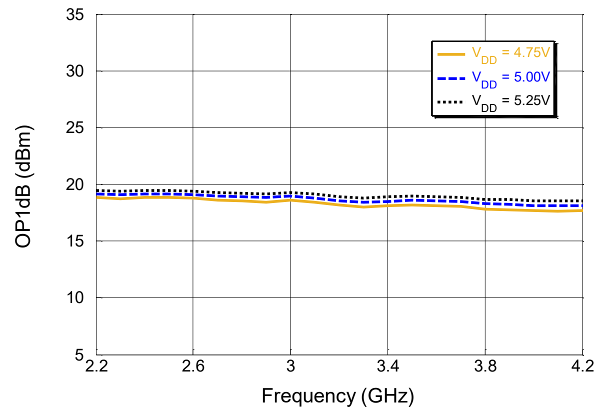


Typical Performance Curves
P_{IN} = -35 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

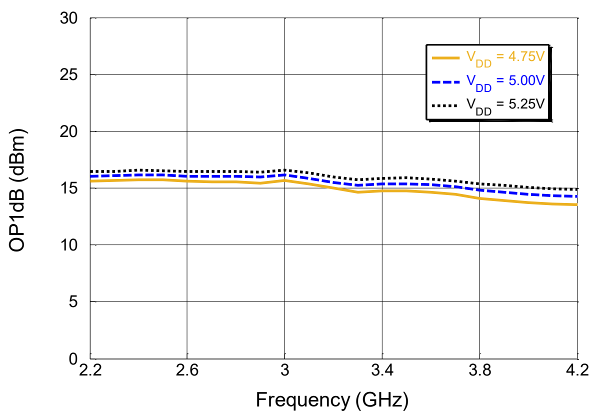
Channel A LNA Output P1dB over swept Frequency (& V_{DD}) in Rx High Gain Mode



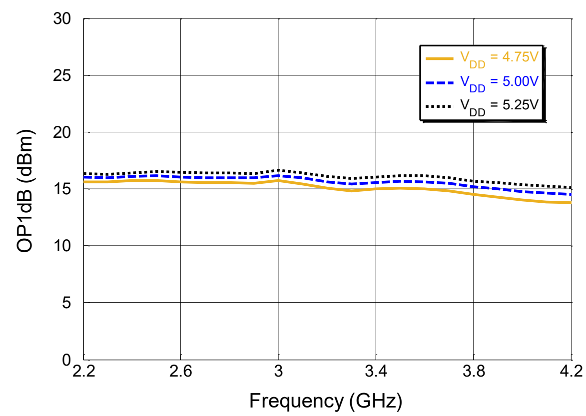
Channel B LNA Output P1dB over swept Frequency (& V_{DD}) in Rx High Gain Mode



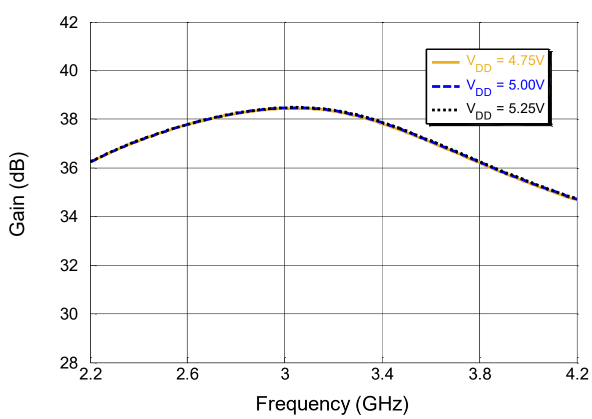
Channel A LNA Output P1dB over swept Frequency (& V_{DD}) in Rx Low Gain Mode



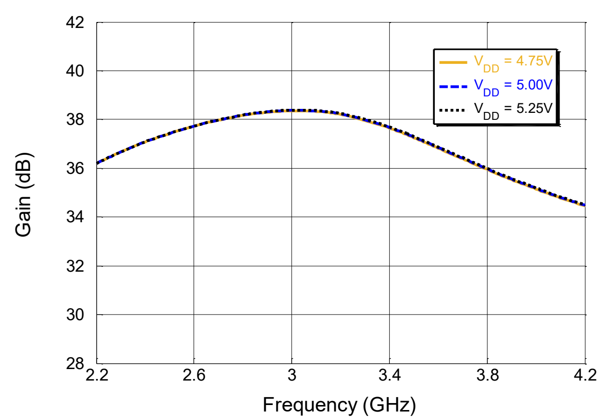
Channel B LNA Output P1dB over swept Frequency (& V_{DD}) in Rx Low Gain Mode



Channel A LNA Gain over Frequency (& V_{DD}) in Rx High Gain Mode

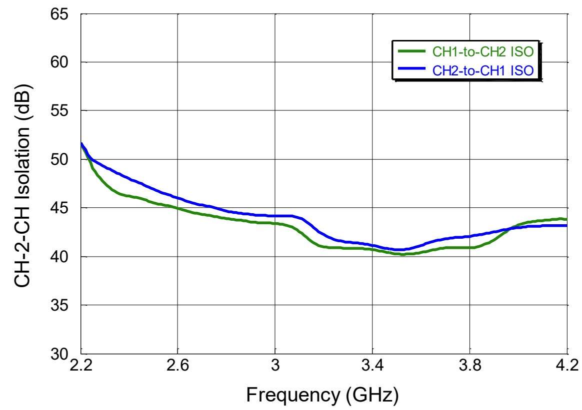


Channel B LNA Gain over Frequency (& V_{DD}) in Rx High Gain Mode

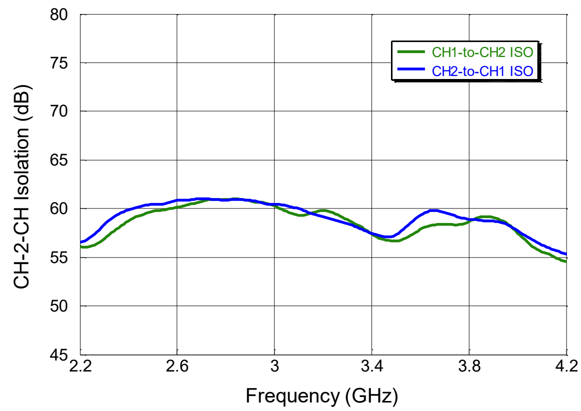


Typical Performance Curves
P_{IN} = -35 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

Ch-to-Ch Isolation over swept Frequency in High Gain Mode

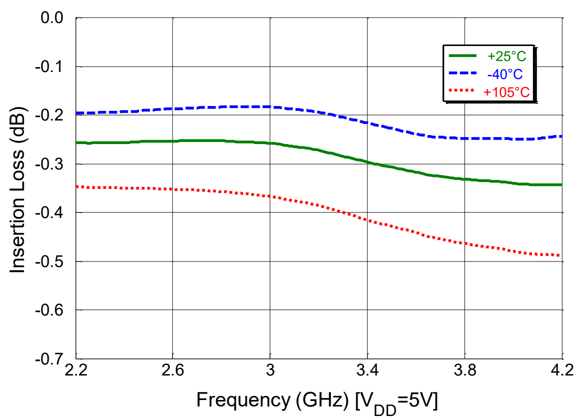


Ch-to-Ch Isolation over swept Frequency in Low Gain Mode

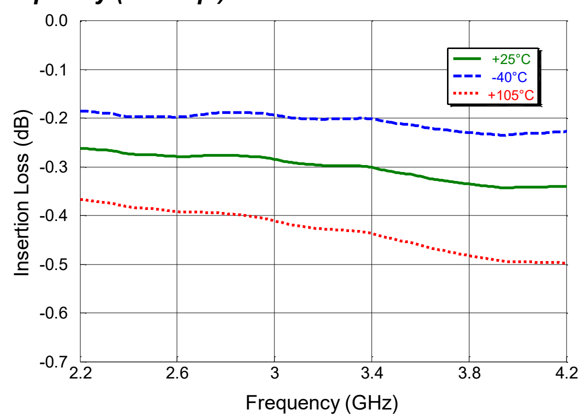


Typical Performance Curves
P_{IN} = -10 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

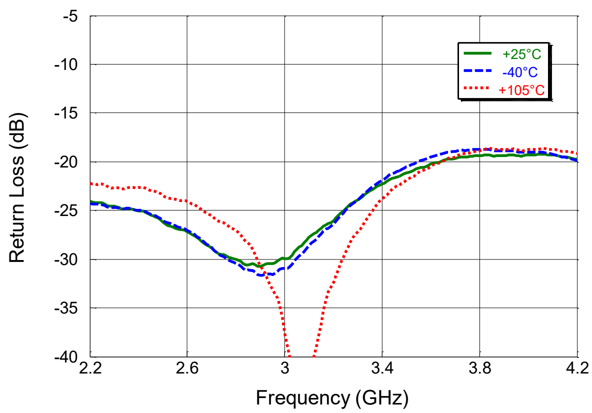
Channel A Switch Insertion Loss over swept Frequency (& Temp.) in Tx Mode



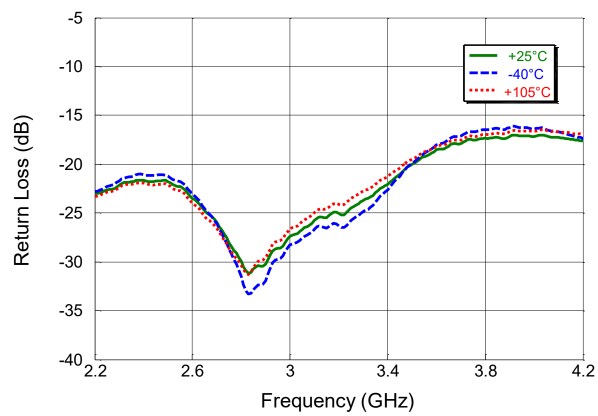
Channel B Switch Insertion Loss over swept Frequency (& Temp.) in Tx Mode



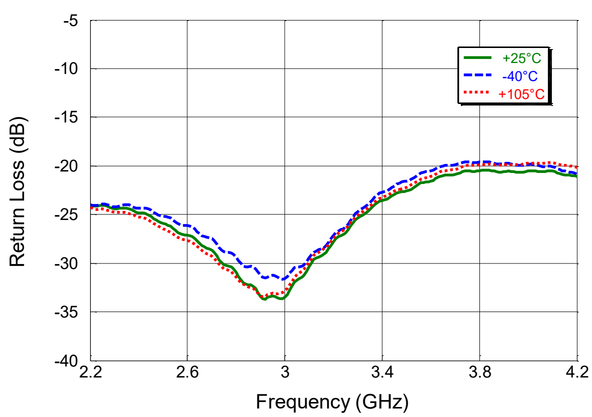
Channel A ANT Port Return Loss over swept Frequency (& Temp.) in Tx Mode



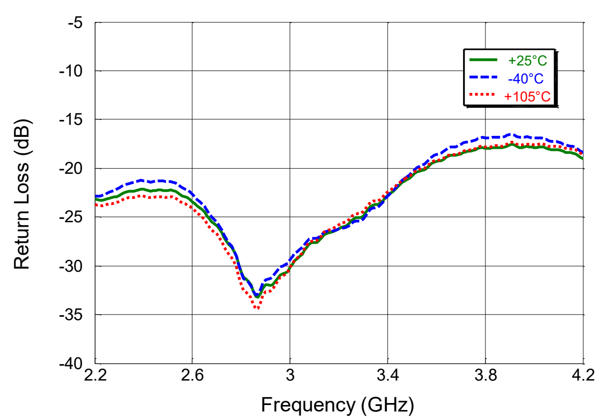
Channel B ANT Port Return Loss over swept Frequency (& Temp.) in Tx Mode



Channel A LOAD Port Return Loss over swept Frequency (& Temp.) in Tx Mode

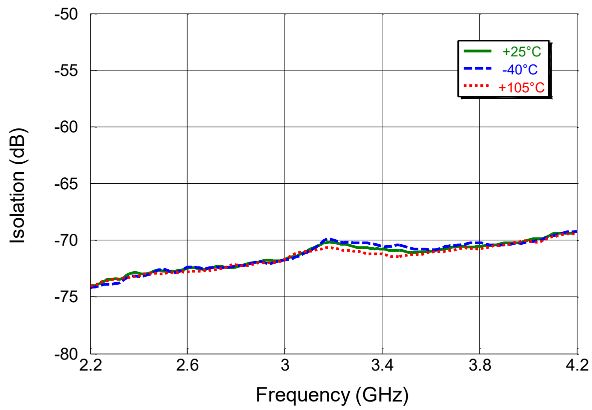


Channel B LOAD Port Return Loss over swept Frequency (& Temp.) in Tx Mode

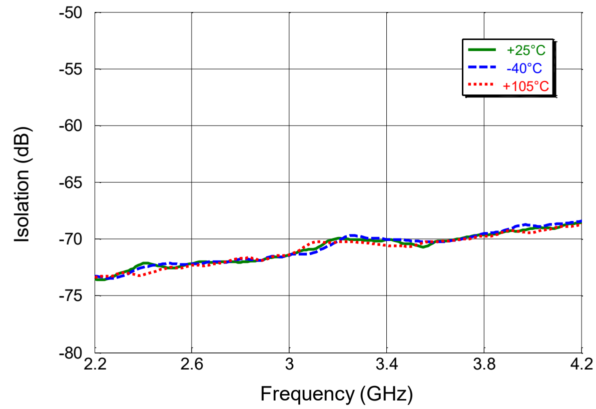


Typical Performance Curves
P_{IN} = -10 dBm, V_{DD} = 5 V, T_C = +25°C, Z_0 = 50 Ω (unless otherwise indicated)

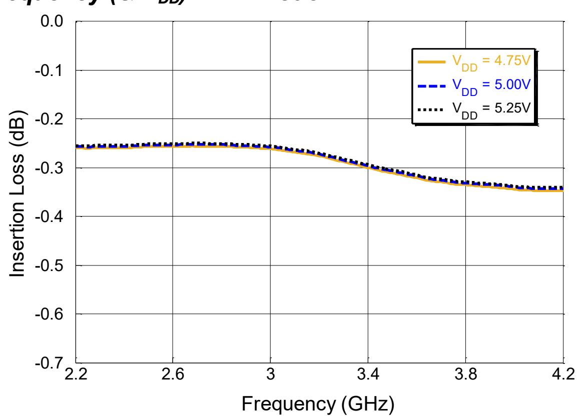
Channel A ANT to RXOUT Isolation over swept Frequency (& Temp.) in Tx Mode



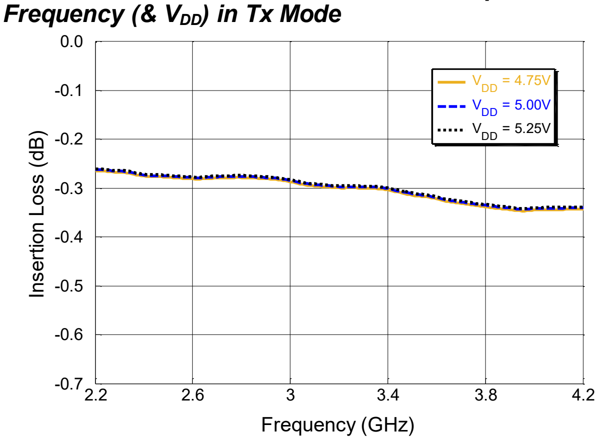
Channel B ANT to RXOUT Isolation over swept Frequency (& Temp.) in Tx Mode



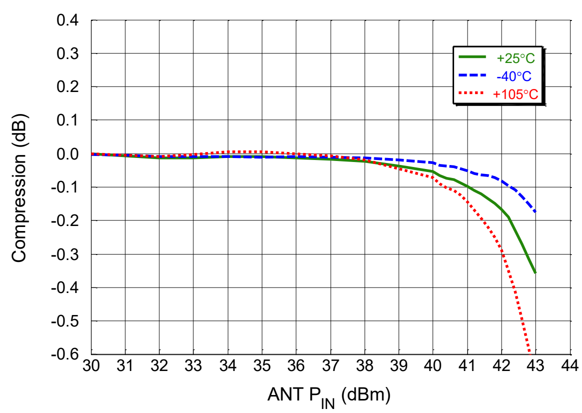
Channel A Switch Insertion Loss over swept Frequency (& V_{DD}) in Tx Mode



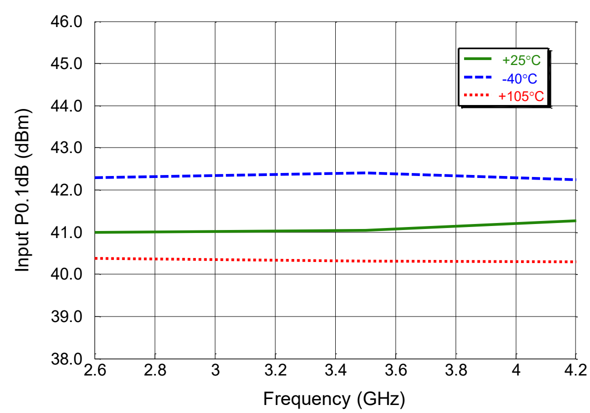
Channel B Switch Insertion Loss over swept Frequency (& V_{DD}) in Tx Mode



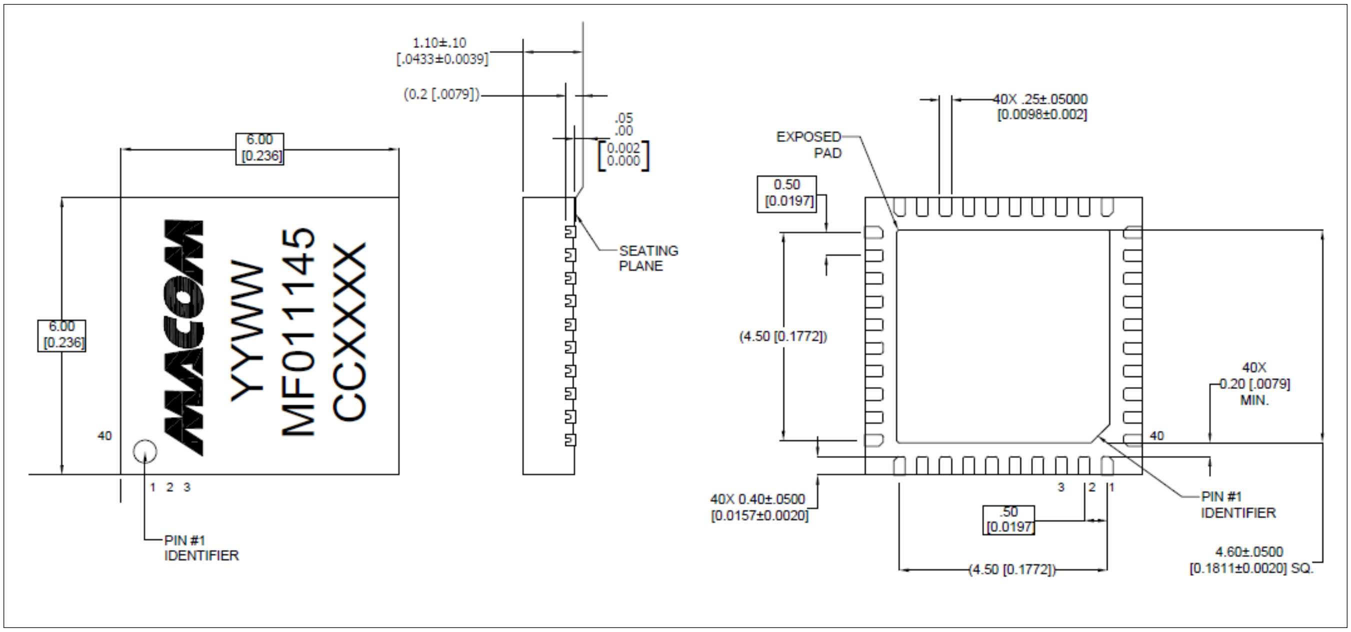
Switch Compression over swept ANT Input Power (& Temp.) at 2.6 GHz in Tx Mode



Switch ANT Input P0.1dB Compression Point over swept Frequency (& Temp.) in Tx Mode



Lead-Free 6 mm 40-Lead QFN†



† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is NiPdAuAg