

Rev. V1

Features

- Suitable for Linear and Saturated Applications
- Optimized for Cellular Base Station Applications
- Designed for Digital Predistortion Error Correction Systems
- High Terminal Impedances for Broadband Performance
- 48 V Operation
- 100% RF Tested
- RoHS* Compliant

Description

The MAGB-104550-005B0P is a wideband GaN HEMT D-mode amplifier designed for base station applications and optimized for 4.5 - 5.0 GHz modulated signal operation. This device supports pulsed and linear operation with peak output power levels to 5 W (37 dBm) in a 4 mm DFN package.

Typical Performance:

 V_{DS} = 48 V, I_{DQ} = 20 mA, T_C = 25°C. Measured under load-pull at 2.5 dB compression, 100 µs pulse width, 10% duty cycle.

Frequency (GHz)	Output Power ¹ (dBm)	Gain² (dB)	η _D ² (%)
4.5	38.7	17.9	54.5
4.7	38.7	17.4	54.1
5.0	38.6	17.2	53.7

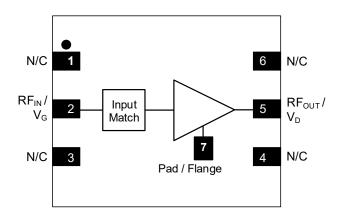
- 1. Load impedance tuned for maximum output power.
- 2. Load impedance tuned for maximum drain efficiency.

Ordering Information

Part Number	Package
MAGB-104550-005B0P	Bulk Quantity
MAGB-104550-005BTP	Tape and Reel
MAGB-114550-005B0P	Class-AB Sample Board



Functional Schematic



Pin Configuration³

Pin#	Pin Name	Function
1,3,4,6	N/C	No Connection
2	RF _{IN} / V _G	RF Input / Gate
5	RF _{OUT} / V _D	RF Output / Drain
7	Pad ³	Ground / Source

3. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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RF Electrical Characteristics: $T_C = 25^{\circ}C$, $V_{DS} = 48 \text{ V}$, $I_{DQ} = 20 \text{ mA}$ Note: Performance in MACOM Single-ended Class-AB Evaluation Circuit, 50 Ω system.

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Small Signal Gain	Pulsed ⁴ , 4.9 GHz	G _{SS}	-	19.0	-	dB
Saturated Output Power	Pulsed ⁴ , 4.9 GHz	P _{SAT}	-	38.1	-	dBm
Drain Efficiency at Saturation	Pulsed ⁴ , 4.9 GHz	η_{SAT}	-	52.3	-	%
AM/PM	Pulsed ⁴ , 4.9 GHz	Φ	-	2	-	0
Modulated Peak Power	WCDMA ⁵ , 4.9 GHz	P2.5dB ⁶	-	38.6	-	dBm
Gain Flatness in 60MHz	WCDMA ⁵ , P _{OUT} = 25 dBm	G_{F}	-	0.4	-	dB
Gain Variation (-25°C to +105°C)	WCDMA ⁵ , 4.9 GHz, P _{OUT} = 25 dBm	ΔG	-	0.02	-	dB/°C
Power Variation (-25°C to +105°C)	Pulsed ⁴ , 4.9 GHz	ΔP2.5dB	-	0.01	-	dB/°C
Power Gain	WCDMA ⁵ , 4.9 GHz, P _{OUT} = 25 dBm	G₽	-	18.9	-	dB
Drain Efficiency	WCDMA ⁵ , 4.9 GHz, P _{OUT} = 25 dBm	η	-	13.7	-	%
Output CCDF @ 0.01%	WCDMA ⁵ , 4.9 GHz, P _{OUT} = 25 dBm	PAR	-	10.0	-	dB
Adjacent Channel Power	WCDMA ⁵ , 4.9 GHz, P _{OUT} = 25 dBm	ACP	-	-34	-	dBc
Input Return Loss	WCDMA ⁵ , 4.9 GHz, P _{OUT} = 25 dBm	IRL		-12	-	dB
Ruggedness: Output Mismatch	All phase angles	Ψ	VSWR	= 10:1, No	Device D	amage

RF Electrical Characteristics: $T_A = 25^{\circ}\text{C}$, $V_{DS} = 48 \text{ V}$, $I_{DQ} = 20 \text{ mA}$ Note: Performance in MACOM Single-ended Class-AB Production Test Fixture, 50 Ω system.

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Power Gain	WCDMA ⁵ , 4.8 GHz, P _{OUT} = 30 dBm	G₽	15.6	17.0	-	dB
Drain Efficiency	WCDMA ⁵ , 4.8 GHz, P _{OUT} = 30 dBm	η	20.3	24.3	-	%
Output CCDF @ 0.01%	WCDMA ⁵ , 4.8 GHz, P _{OUT} = 30 dBm	PAR	7.2	8.0	-	dB
Adjacent Channel Power	WCDMA ⁵ , 4.8 GHz, P _{OUT} = 30 dBm	ACP	-	-34	-30	dBc
Input Return Loss	WCDMA ⁵ , 4.8 GHz, P _{OUT} = 30 dBm	IRL	-	-15	-5	dB

DC Electrical Characteristics: T_A = 25°C

Parameter	Test Conditions S		Min.	Тур.	Max.	Units
Drain-Source Leakage Current	V _{GS} = -8 V, V _{DS} = 130 V	I _{DLK}	-	-	0.96	mA
Gate-Source Leakage Current	$V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$	I_{GLK}	-	-	0.96	mA
Gate Threshold Voltage	$V_{DS} = 48 \text{ V}, I_{D} = 0.96 \text{ mA}$	V_T	-2.6	-2.2	-	V
Gate Quiescent Voltage	$V_{DS} = 48 \text{ V}, I_{D} = 20 \text{ mA}$	V_{GSQ}	-2.4	-2.0	-1.4	V
On Resistance	$V_{GS} = 2 \text{ V}, I_{D} = 9.6 \text{ mA}$	R _{ON}	-	4.3	-	Ω
Maximum Drain Current	V _{DS} = 7 V, pulse width 300 μs	I _{D, MAX}	1	0.5	-	Α

^{4.} Pulse details: 100 μs pulse width, 10% Duty Cycle

^{5.} Modulated Signal: 3.84 MHz, WCDMA 3GPP TM1 64 DPCH, 9.9 dB PAR @ 0.01% CCDF

^{6.} P2.5dB = P_{OUT} + 7.5 dB where P_{OUT} is the average output power measured using a modulated signal⁵ where the output PAR is compressed to 7.5 dB @ 0.01% probability CCDF.



Absolute Maximum Ratings 7,8,9,10,11

Parameter	Absolute Maximum		
Drain Source Voltage, V _{DS}	130 V		
Gate Source Voltage, V _{GS}	-10 to 3 V		
Gate Current, I _G	1.92 mA		
Storage Temperature Range	-65°C to +150°C		
Case Operating Temperature Range	-40°C to +120°C		
Channel Operating Temperature Range, T _{CH}	-40°C to +210°C		
Absolute Maximum Channel Temperature	+225°C		

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation above maximum operating conditions.

- Operating at drain source voltage V_{DS} < 55 V will ensure MTTF > 4 x 10⁶ hours.
 Operating at nominal conditions with T_{CH} ≤ 210°C will ensure MTTF > 4 x 10⁶ hours.
 MTTF may be estimated by the expression MTTF (hours) = A e ^[B+C/(T+273)] where *T* is the channel temperature in degrees Celsius, A = 1.76, $\dot{B} = -33.83$, and $\dot{C} = 23.476$.

Thermal Characteristics¹²

Parameter		Test Conditions	Symbol	Typical	Units
	nermal Resistance using Finite Element Analysis	$V_{DS} = 48 \text{ V},$ $T_{C} = 85^{\circ}\text{C}, T_{CH} = 225^{\circ}\text{C}$	$R_{\theta}(FEA)$	27.3	°C/W
	nermal Resistance using urement of Die Surface Temperature	V _{DS} = 48 V, T _C = 85°C, T _{CH} = 225°C	$R_{\theta}(IR)$	20.0	°C/W

^{12.} Case temperature measured using thermocouple embedded in heat-sink. Contact local applications support team for more details on this measurement.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

electronic devices are electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



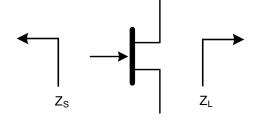
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Pulsed⁴ Load-Pull Performance Reference Plane at Device Leads

			Maximum Output Power					
			$V_D = 48 \text{ V}, I_{DQ} = 20 \text{ mA}, T_C = 25^{\circ}\text{C}, P2.5 dB$					
Frequency (GHz)	Z _{SOURCE} (Ω)	Z _{LOAD} ¹³ (Ω)	Gain (dB)	P _{OUT} (dBm)	Р _{оит} (W)	η _□ (%)	AM/PM (°)	
4.5	21.2 - j53.0	20.7 + j14.6	16.7	38.7	7.4	49.5	-37.5	
4.7	48.5 - j61.2	19.4 + j13.7	16.7	38.7	7.4	49.9	-53.5	
5.0	77.5 + j9.0	16.3 + j10.5	16.6	38.6	7.2	49.7	-83.9	

		Maximum Drain Efficiency						
			$V_D = 48 \text{ V}, I_{DQ} = 20 \text{ mA}, T_C = 25^{\circ}\text{C}, P2.5 dB$					
Frequency (GHz)	Z _{SOURCE} (Ω)	Z _{LOAD} ¹⁴ (Ω)	Gain (dB)	P _{OUT} (dBm)	Р _{оит} (W)	η _□ (%)	AM/PM (°)	
4.5	29.5 - j59.4	13.4 + j19.1	17.9	38.2	6.6	54.5	-43.4	
4.7	71.5 - j52.6	14.0 + j18.2	17.4	38.1	6.5	54.1	-63.1	
5.0	56.1 + j15.4	12.1 + j15.7	17.2	38.0	6.3	53.7	-98.5	

Impedance Reference



- Z_{SOURCE} = Measured impedance presented to the input of the device at package reference plane
- device at package reference plane.

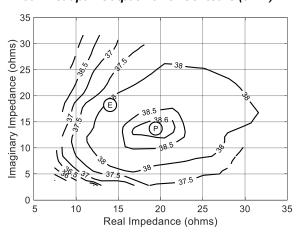
 Z_{LOAD} = Measured impedance presented to the output of the device at package reference plane.
- 13. Load impedance for optimum output power.
- 14. Load impedance for optimum efficiency.



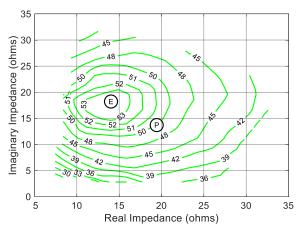
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Pulsed⁴ Load-Pull Performance @ 4.7 GHz

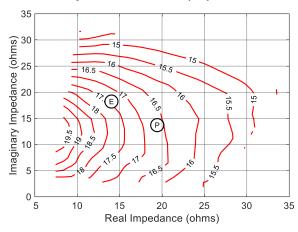
P2.5dB Loadpull Output Power Contours (dBm)



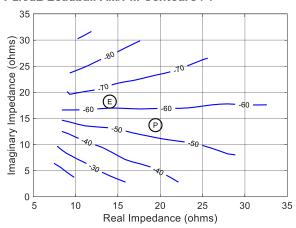
P2.5dB Loadpull Drain Efficiency Contours (%)



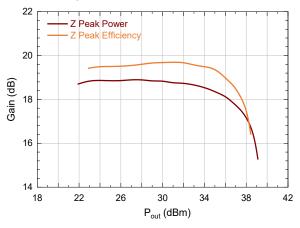
P2.5dB Loadpull Gain Contours (dB)



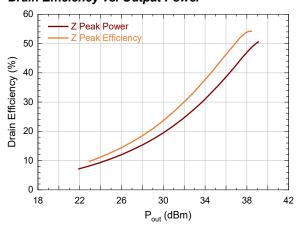
P2.5dB Loadpull AM/PM Contours (°)



Gain vs. Output Power



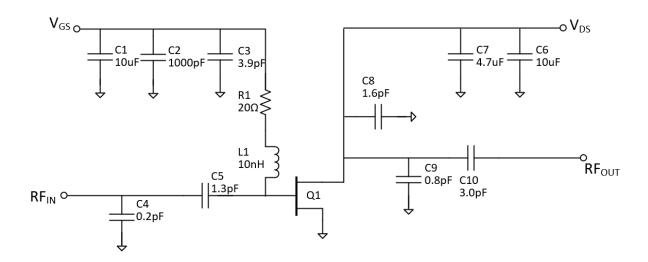
Drain Efficiency vs. Output Power





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Evaluation Board and Recommended Tuning Solution: 4.8 - 5.0 GHz



Description

Parts measured on evaluation board (20-mil thick RO4350). Matching is provided using a combination of lumped elements and transmission lines as shown in the simplified schematic above. Recommended tuning solution component placement, transmission lines, and details are shown on the next page.

Bias Sequencing Turning the device ON

- Set V_{Gs} to the pinch-off Voltage (V_P), typically -3 V.
- Turn on V_{DS} to nominal Voltage (48 V).
- 3. Increase V_{GS} until I_{DS} current is reached.
- 5. Apply RF power to desired level.

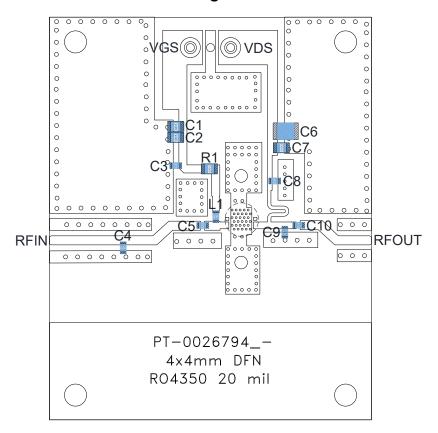
Turning the device OFF

- Turn the RF power off.
- 2. Decrease both V_{GS} down to V_{P}
- 3. Decrease V_D down to 0 V.
- 4. Turn off V_{GS}.



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Evaluation Board and Recommended Tuning Solution: 4.8 - 5.0 GHz



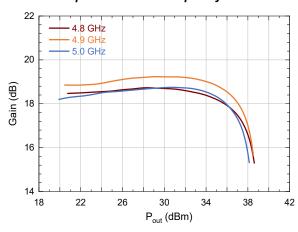
Reference Designator	Value	Tolerance	Manufacturer	Part Number
C1	10 μF	+/- 20%	TDK Corporation	C2012X5R1C106M085AC
C2	1000 pF	+/- 10%	KEMET	C0805C102K2RACTU
C3	3.9 pF	+/- 0.1 pF	Murata	GQM1875C2E3R9BB12D
C4	0.2 pF	+/- 0.1 pF	Murata	GQM1875C2ER20BB12D
C5	1.3 pF	+/- 0.1 pF	Murata	GQM1875C2E1R3BB12D
C6	10 μF	+/- 10%	Murata	GRM32EC72A106KE05L
C7	4.7 µF	+/- 10%	Murata	GRM21BC81H475KE11L
C8	1.6 pF	+/- 0.1 pF	Murata	GQM1875C2E1R6BB12D
C9	0.8 pF	+/- 0.1 pF	Murata	GQM1875C2ER80BB12D
C10	3 pF	+/- 0.1 pF	Murata	GQM1875C2E3R0BB12D
R1	20 Ω	+/- 0.5%	Yageo	RT0805DRE0720RL
L1	10 nH	+/- 5%	Coilcraft	0603CS-10NXJ
PCB		RO4350, 2	20 mil, 1oz Cu, Au Finis	sh



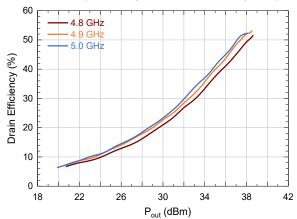
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Typical Performance Curves as Measured in the 4.8 - 5.0 GHz Evaluation Board: Pulsed⁴ 4.9 GHz, V_D = 48 V, I_{DQ} = 20 mA, T_C = 25°C (Unless Otherwise Noted)

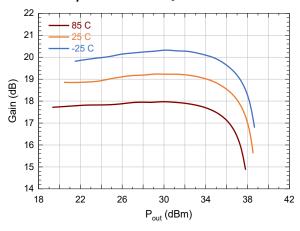
Gain vs. Output Power and Frequency



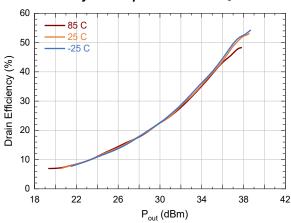
Drain Efficiency vs. Output Power and Frequency



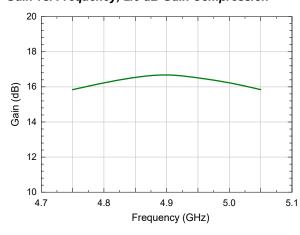
Gain vs. Output Power and Tc



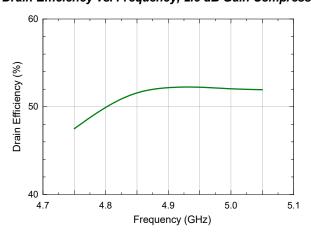
Drain Efficiency vs. Output Power and Tc



Gain vs. Frequency, 2.5 dB Gain Compression



Drain Efficiency vs. Frequency, 2.5 dB Gain Compression

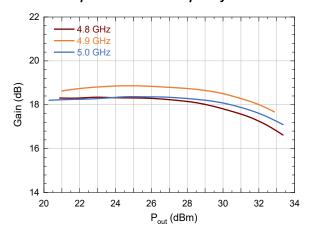




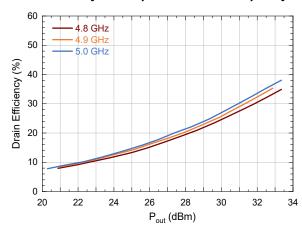
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Typical Performance as Measured in the 4.8 - 5.0 GHz Evaluation Board: WCDMA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF , V_D = 48 V, I_{DQ} = 20 mA, T_C = 25°C

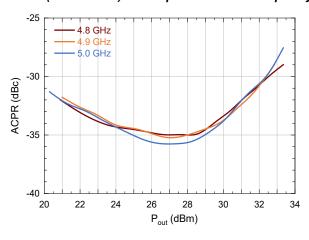
Gain vs. Output Power and Frequency



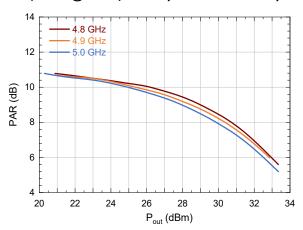
Drain Efficiency vs. Output Power and Frequency



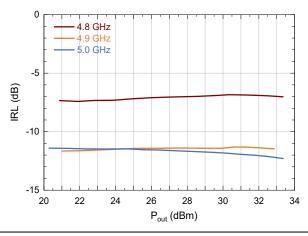
ACPR (Max ±5 MHz) vs. Output Power and Frequency



PAR (CCDF @ 0.01%) vs. Output Power and Frequency



Input Return Loss vs. Output Power and Frequency

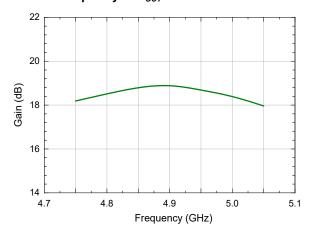




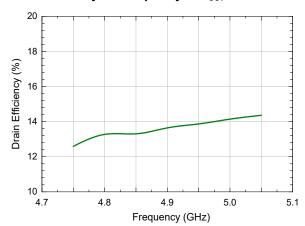
Rev. V

Typical Performance as Measured in the 4.8 - 5.0 GHz Evaluation Board: WCDMA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF, V_D = 48 V, I_{DQ} = 20 mA, T_C = 25°C

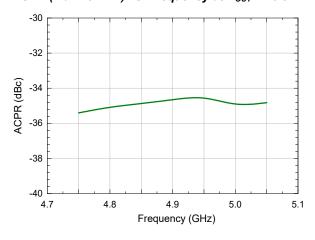
Gain vs. Frequency at $P_{OUT} = 25 \text{ dBm}$



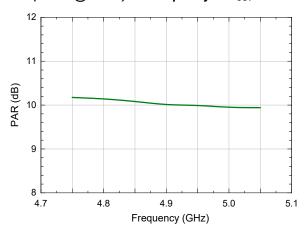
Drain Efficiency vs. Frequency at $P_{OUT} = 25 \text{ dBm}$



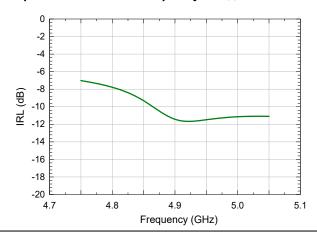
ACPR (Max ± 5 MHz) vs. Frequency at $P_{OUT} = 25$ dBm



PAR (CCDF @ 0.01%) vs. Frequency at Pout = 25 dBm



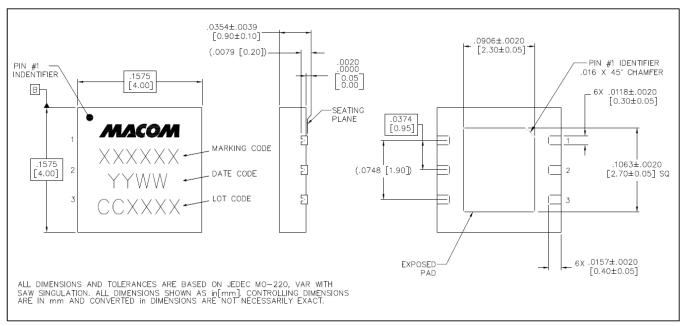
Input Return Loss vs. Frequency at $P_{OUT} = 25 \text{ dBm}$





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Lead-Free 4 mm 6-Lead Package Dimensions[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level (MSL) 3 requirements. Plating is NiPdAu.

GaN Amplifier 48 V, 5 W 4.5 - 5.0 GHz



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