

Rev. V2

Features

- · Suitable for Linear and Saturated Applications
- Optimized for Cellular Base Station Applications
- Designed for Digital Predistortion Error Correction Systems
- High Terminal Impedances for Broadband Performance
- 48 V Operation
- 100 % RF Tested
- RoHS* Compliant

Description

The MAGB-103740-008B0P is a wideband GaN HEMT D-mode amplifier designed for base station applications and optimized for 3.7 - 4.0 GHz modulated signal operation. This device supports pulsed and linear operation with peak output levels to 8 W (39 dBm) in a 4x4mm DFN package.

Typical Performance

V_{DS} = 48 V, I_{DQ} = 20 mA, T_C = 25°C. Measured under load-pull at 2.5 dB compression, 100 μs pulse width, 10% duty cycle.

Frequency (GHz)	Output Power ¹ (dBm)	Gain ² (dB)	η _D ² (%)
3.7	38.9	18.8	61.8
3.8	38.8	18.9	62.2
4.0	38.9	18.1	59.6

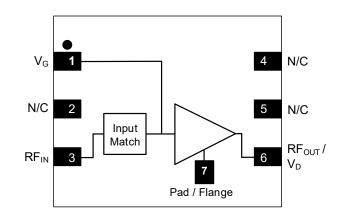
- 1. Load impedance tuned for maximum output power.
- 2. Load impedance tuned for maximum drain efficiency.

Ordering Information

Part Number	Package
MAGB-103740-008B0P	Bulk Quantity
MAGB-103740-008BTP	Tape and Reel
MAGB-1B3740-008B0P	Class-AB Sample Board



Functional Schematic



Pin Configuration³

Pin#	Pin Name	Function
1	V_{G}	Gate
2,4,5	N/C	No Connection
3	RF _{IN}	RF Input
6	RF _{OUT} / V _D	RF Output / Drain
7	Pad ²	Ground / Source

3. The pad on the package bottom must be connected to RF, DC or thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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RF Electrical Characteristics: $T_{CASE} = 25^{\circ}C$, $V_{DS} = 48V$, $I_{DQ} = 20$ mA Note: Performance in MACOM Single-ended Class-AB Evaluation Circuit, 50 Ω system.

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Small Signal Gain	Pulsed ⁴ , 3.85 GHz	G _{SS}	-	18.5	-	dB
Saturated Output Power	Pulsed ⁴ , 3.85 GHz	P _{SAT}	-	37.7	-	dBm
Drain Efficiency at Saturation	Pulsed ⁴ , 3.85 GHz	η_{SAT}	1	51.0	•	%
AM/PM	Pulsed ⁴ , 3.85 GHz	Φ	-	4	-	0
Modulated Peak Power	WCDMA ⁵ , 3.85 GHz	P2.5dB ⁶	-	39.4	-	dBm
Gain Flatness in 60MHz	WCDMA ⁵ , P _{OUT} = 26 dBm	G _F	-	0.3	-	dB
Gain Variation (-25°C to +105°C)	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 26 dBm	ΔG	1	0.02	•	dB/°C
Power Variation (-25°C to +105°C)	Pulsed ⁴ , 3.85 GHz	Δ P2.5dB	-	0.01	-	dBm/°C
Power Gain	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 26 dBm	G _P	-	17.9	-	dB
Drain Efficiency	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 26 dBm	η	-	16.2	-	%
Output CCDF @ 0.01%	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 26 dBm	PAR	-	9.4	-	dB
Adjacent Channel Power	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 26 dBm	ACP	-	-41	-	dBc
Input Return Loss	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 26 dBm	IRL	-	-7	ı	dB
Ruggedness: Output Mismatch	All phase angles	Ψ	VSWR = 10:1, No Device Damag		Damage	

RF Electrical Characteristics: T_A = 25°C, V_{DS} = 48V, I_{DQ} = 20 mA Note: Performance in MACOM Single-ended Class-AB Production Test Fixture, 50 Ω system.

Parameter	Test Conditions	Symbol	Min.	Тур.	Max.	Units
Power Gain	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 31 dBm	G₽	15.2	16.8	-	dB
Drain Efficiency	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 31 dBm	η	24	28.4	-	%
Output CCDF @ 0.01%	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 31 dBm	PAR	7.4	7.7	-	dB
Adjacent Channel Power	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 31 dBm	ACP	-	-38	-35	dBc
Input Return Loss	WCDMA ⁵ , 3.85 GHz, P _{OUT} = 31 dBm	IRL	-	-9	-7	dB

DC Electrical Characteristics: T_{CASE} = 25°C

Parameter	Test Conditions S		Min.	Тур.	Max.	Units
Drain-Source Leakage Current	V_{GS} = -8 V, V_{DS} = 130 V	I _{DLK}	-	-	0.96	mA
Gate-Source Leakage Current	V_{GS} = -8 V, V_{DS} = 0 V	I _{GLK}	-	-	-0.96	mA
Gate Threshold Voltage	$V_{DS} = 48 \text{ V}, I_{D} = 0.96 \text{ mA}$	V_T	-2.6	-2.4	-	V
Gate Quiescent Voltage	$V_{DS} = 48 \text{ V}, I_{D} = 20 \text{ mA}$	V_{GSQ}	-2.4	-2.0	-1.4	V
On Resistance	V _{GS} = 2 V, I _D = 9.6 mA	R _{ON}	-	4.4	-	Ω
Maximum Drain Current	V _{DS} = 7 V, pulse width 300 μs	I _{D, MAX}	-	0.6	-	Α

^{4.} Pulse details: 100 μs pulse width, 10% Duty Cycle

^{5.} Modulated Signal: 3.84MHz, WCMDA 3GPP TM1 64 DPCH, 9.9dB PAR @ 0.01% CCDF

P2.5dB = P_{OUT} + 7.5 dB where P_{OUT} is the average output power measured using a modulated signal⁵ where the output PAR is compressed to 7.5 dB @ 0.01% probability CCDF.



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Absolute Maximum Ratings 7,8,9,10,11

Parameter	Absolute Maximum		
Drain Source Voltage, V _{DS}	130 V		
Gate Source Voltage, V _{GS}	-10 to 3 V		
Gate Current, I _G	1.92 mA		
Storage Temperature Range	-65°C to +150°C		
Case Operating Temperature Range	-40°C to +120°C		
Channel Operating Temperature Range, T _{CH}	-40°C to +210°C		
Absolute Maximum Channel Temperature	+225°C		

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation above maximum operating conditions.

- Operating at drain source voltage V_{DS} < 55 V will ensure MTTF > 4 x 10⁶ hours.
 Operating at nominal conditions with T_{CH} ≤ 210°C will ensure MTTF > 4 x 10⁶ hours.
 MTTF may be estimated by the expression MTTF (hours) = A e ^[B + C/(T+273)] where *T* is the channel temperature in degrees Celsius, A = 1.76, B = -33.83, and C = 23,476.

Thermal Characteristics¹²

Parameter	Test Conditions	Symbol	Typical	Units
Thermal Resistance using Finite Element Analysis	$V_{DS} = 48 \text{ V},$ $T_{C} = 85^{\circ}\text{C}, T_{CH} = 225^{\circ}\text{C}$	$R_{\theta}(FEA)$	27.3	°C/W
Thermal Resistance using Infrared Measurement of Die Surface Temperature	V _{DS} = 48 V, T _C = 85°C, T _{CH} = 225°C	$R_{\theta}(IR)$	20.0	°C/W

^{12.} Case temperature measured using thermocouple embedded in heat-sink. Contact local applications support team for more details on this measurement.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1A, CDM Class 3 devices.

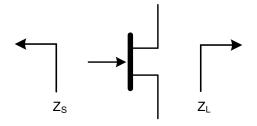


Pulsed⁴ Load-Pull Performance Reference Plane at Device Leads

		Maximum Output Power						
			V _{DS} = 48 V, I _{DQ} = 20 mA, T _C = 25°C, P2.5dB					
Frequency (GHz)	Z _{SOURCE} (Ω)	Z _{LOAD} ¹³ (Ω)	Gain (dB)	P _{OUT} (dBm)	P _{OUT} (W)	η _□ (%)	AM/PM (°)	
3.7	10.7 - j54.1	19.5 + j17.6	17.7	38.9	7.8	57.8	-36.1	
3.8	12.7 - j58.5	17.9 + j13.6	17.9	38.8	7.6	57.3	-32.4	
4.0	24.2 - j67.3	18.5 + j11.4	17.4	38.9	7.8	56.9	-56.9	

		Maximum Drain Efficiency						
		$V_{DS} = 48 \text{ V}, I_{DQ} = 20 \text{ mA}, T_C = 25^{\circ}\text{C}, P2.5 dB}$						
Frequency (GHz)	Z _{SOURCE} (Ω)	Z _{LOAD} ¹⁴ (Ω)	Gain (dB)	P _{OUT} (dBm)	P _{OUT} (W)	η _□ (%)	AM/PM (°)	
3.7	12.7 - j57.5	13.5 + j21.0	18.8	38.2	6.6	61.8	-39.0	
3.8	12.7 - j60.8	13.4 + j14.9	18.9	38.7	7.4	62.2	-33.9	
4.0	31.4 - j70.1	18.5 + j15.8	18.1	38.4	6.9	59.6	-58.5	

Impedance Reference



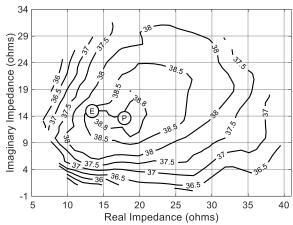
- $$\begin{split} Z_{\text{SOURCE}} &= \text{Measured impedance presented to the input of the} \\ \text{device at package reference plane.} \\ Z_{\text{LOAD}} &= \text{Measured impedance presented to the output of the} \\ \text{device at package reference plane.} \end{split}$$
- 13. Load Impedance for optimum output power.
- 14. Load Impedance for optimum efficiency.



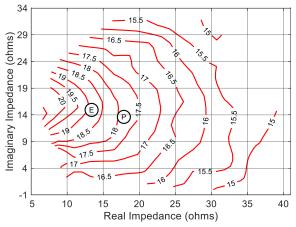
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Pulsed⁴ Load-Pull Performance 3.8GHz

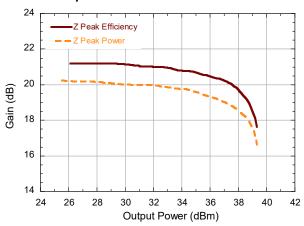
P2.5dB Loadpull Output Power Contours (dBm)



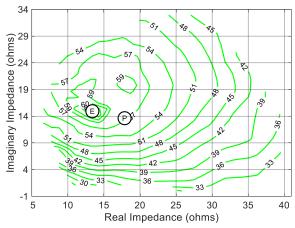
P2.5dB Loadpull Gain Contours (dB)



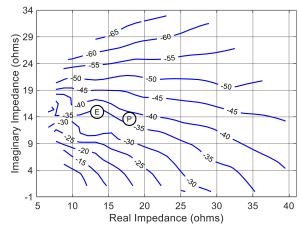
Gain vs. Output Power



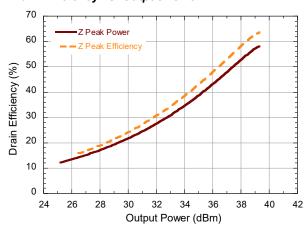
P2.5dB Loadpull Drain Efficiency Contours (%)



P2.5dB Loadpull AM/PM Contours (°)



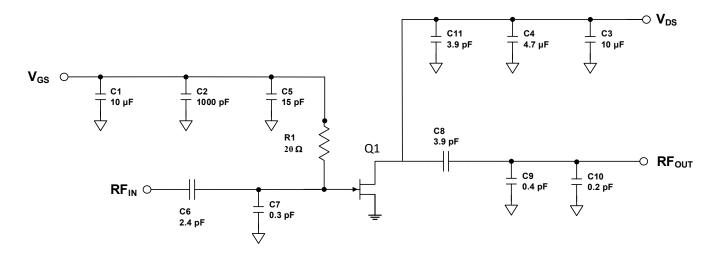
Drain Efficiency vs. Output Power





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Evaluation Test Fixture and Recommended Tuning Solution 3.7 - 4.0 GHz



Description

Parts measured on evaluation board (20-mil thick RO4350). Matching is provided using a combination of lumped elements and transmission lines as shown in the simplified schematic above. Recommended tuning solution component placement, transmission lines, and details are shown on the next page.

Bias Sequencing Turning the device ON

- 1. Set V_{GS} to pinch-off (V_P).
- 2. Turn on V_{DS} to nominal voltage (48 V).
- 3. Increase V_{GS} until I_{DS} current is reached.
- 4. Apply RF power to desired level.

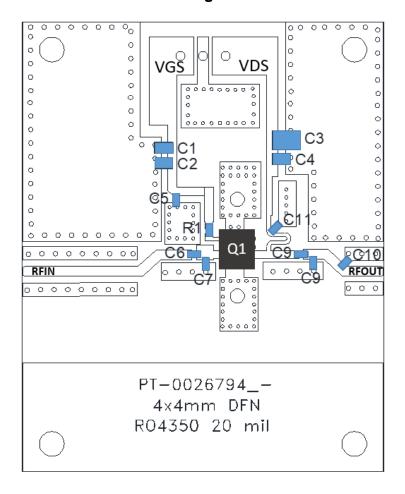
Turning the device OFF

- 1. Turn the RF power OFF.
- 2. Decrease V_{GS} down to V_P pinch-off.
- 3. Decrease V_{DS} down to 0 V.
- 4. Turn off V_{GS}.



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Evaluation Board and Recommended Tuning Solution 3.7 - 4.0 GHz



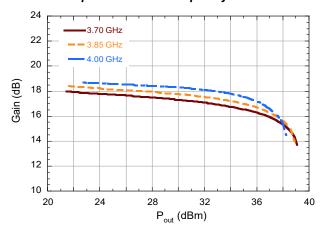
Reference Designator	Value	Tolerance	Manufacturer	Part Number		
C1	10 μF	+/- 20 %	TDK Corporation	C2012X5R1C106M085AC		
C2	1000 pF	+/- 10 %	KEMET	C0805C102K2RACTU		
C3	10 μF	+/- 10 %	Murata	GRM32EC72A106KE05L		
C4	4.7 µF	+/- 10 %	Murata	GRM21BC81H475KE11L		
C5	15 pF	+/- 5 %	Murata	GQM1875C2E150JB12D		
C6	2.4 pF	+/- 0.1 pF	Murata	GQM1875C2E2R4BB12D		
C7	0.3 pF	+/- 0.1 pF	Murata	GQM1875C2ER30BB12D		
C8, C11	3.9 pF	+/- 0.1 pF	Murata	GQM1875C2E3R9BB12D		
C9	0.4 pF	+/- 0.1 pF	Murata	GQM1875C2ER40BB12D		
C10	0.2 pF	+/- 0.1 pF	Murata	GQM1875C2ER20BB12D		
R1	20 Ω	+/- 0.5 %	Yageo	RT0805DRE0720RL		
PCB	RO4350, 20 mil, 1 oz Cu, Au Finish					



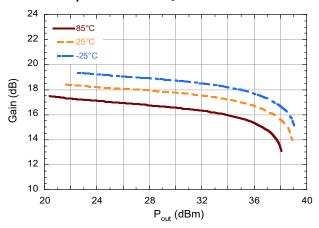
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Typical Performance Curves as Measured in the 3.7 - 4.0 GHz Evaluation Test Fixture: Pulsed⁴ 3.85 GHz, V_{DS} = 48 V, I_{DQ} = 20 mA, T_{C} = 25°C Unless Otherwise Noted

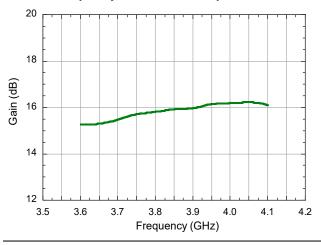
Gain vs. Output Power and Frequency



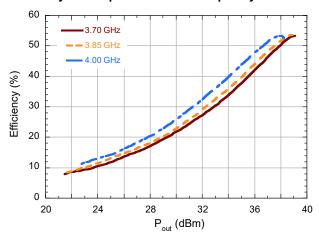
Gain vs. Output Power and Tc



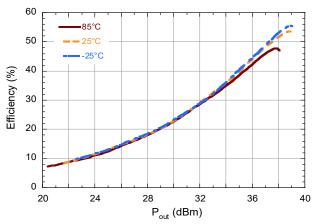
Gain vs. Frequency, 2.5dB Gain Compression



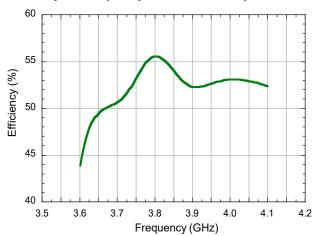
Efficiency vs. Output Power and Frequency



Efficiency vs. Output Power and Tc



Efficiency vs. Frequency, 2.5dB Gain Compression



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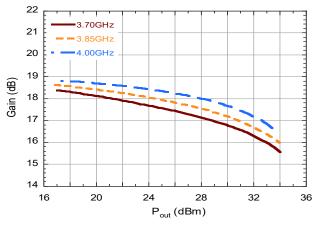
8



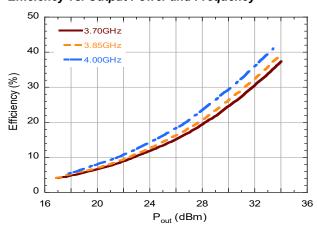
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Typical Performance as Measured in the 3.7 - 4.0 GHz Evaluation Board: WCMDA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF V_{DS} = 48 V, I_{DO} = 20 mA, T_{CASE} = 25 °C

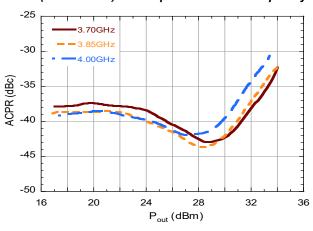
Gain vs. Output Power and Frequency



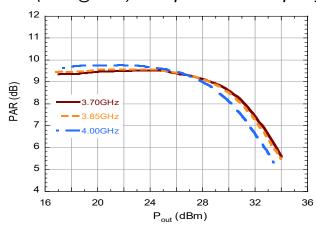
Efficiency vs. Output Power and Frequency



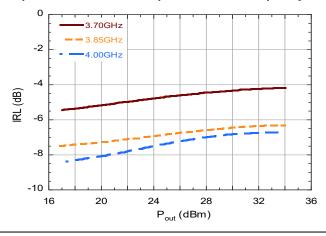
ACPR (Max ±5 MHz) vs. Output Power and Frequency



PAR (CCDF @ 0.01%) vs. Output Power and Frequency



Input Return Loss vs. Output Power and Frequency

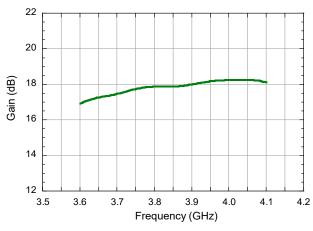




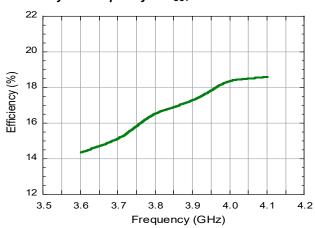
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Typical Performance as Measured in the 3.7 - 4.0 GHz Evaluation Board: WCMDA 3GPP TM1 64 DPCH 9.9 dB PAR @ 0.01% CCDF V_{DS} = 48 V, I_{DQ} = 20 mA, T_{CASE} = 25 °C

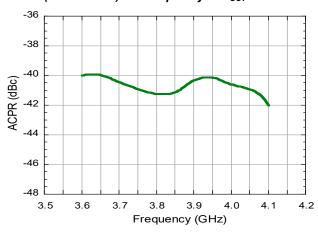
Gain vs. Frequency at $P_{OUT} = 26 \text{ dBm}$



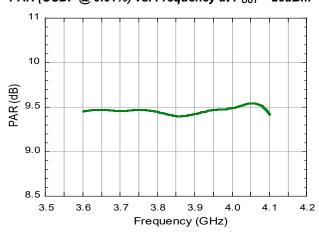
Efficiency vs. Frequency at $P_{OUT} = 26 \text{ dBm}$



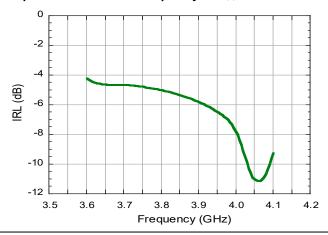
ACPR (Max ± 5 MHz) vs. Frequency at $P_{OUT} = 26$ dBm



PAR (CCDF @ 0.01%) vs. Frequency at Pout = 26dBm



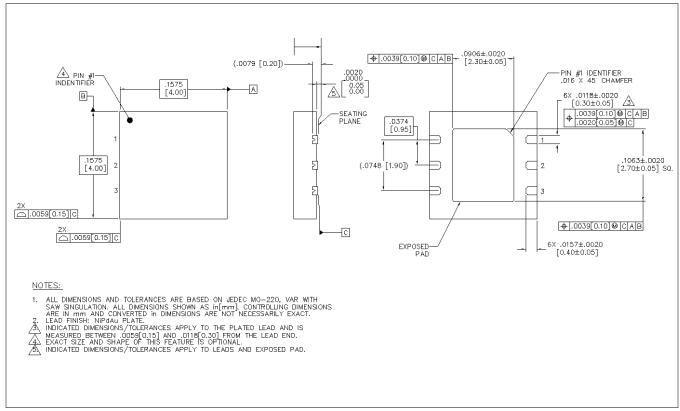
Input Return Loss vs. Frequency at $P_{OUT} = 26 \text{ dBm}$





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Lead-Free 4 x 4 mm 6-Lead Package Dimensions[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level (MSL) 3 requirements.

GaN Amplifier 48 V, 8 W 3.7 - 4.0 GHz



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