High Power PIN Diode 50 MHz - 2 GHz



MADP-011037

Rev. V3

Features

- >125 W CW Incident Power Handling @ 1 GHz
- <0.2 dB Insertion Loss @ 1 GHz
- >15 dB Isolation @ 1 GHz
- Lead-Free 3 mm 16-lead HQFN Package

Applications

ISM

Description

The MADP-011037 is a high power PIN diode assembled in a lead-free 3 mm 16-lead HQFN plastic package. This series device provides exceptional switch or attenuator performance from 50 MHz to 2 GHz.

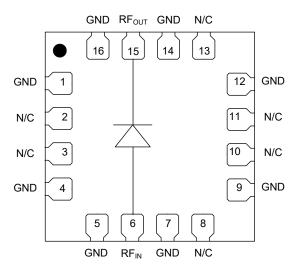
This compact device is ideally suitable for higher power switch and attenuator applications from HF through L band, where higher peak and CW power, lower loss, and higher linearity performance surface mount diode assemblies are required.

Ordering Information¹

Part Number	Package
MADP-011037-139000	500 piece bag
MADP-011037-000SMB	Sample Board

^{1.} All sample boards include 5 loose parts.

Functional Schematic



Pin Configuration²

Pin#	Pin Name	Description	
1, 4, 5, 7, 9, 12, 14, 16	GND	Ground	
2, 3, 8, 10, 11, 13	N/C	Connect to Ground	
6	RF _{IN}	Anode	
15	RF _{OUT}	Cathode	
17	Paddle ³	Ground	

MACOM recommends connecting unused package pins to ground

The exposed pad centered on the package bottom must be connected to RF,DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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Electrical Specifications: Freq. = 1250 MHz, T_A = 25°C, Z_0 = 50 Ω (unless otherwise noted)

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss	$I_F = 10 \text{ mA}$ $I_F = 25 \text{ mA}$ $I_F = 50 \text{ mA}$	dB	_	0.17 0.13 0.11	0.3 —
Input Return Loss	$I_F = 10 \text{ mA}$ $I_F = 25 \text{ mA}$ $I_F = 50 \text{ mA}$	dB	_	22 22 22	_
Isolation	$V_R = 0 V$ $V_R = 20 V$ $V_R = 50 V$	dB	 12 	14 14 14	_
CW Incident Power	1 GHz, CW @ + 50 mA	dBm	_	52	_
Minority Carrier Lifetime	$+I_F = 10 \text{ mA} / I_R = -6 \text{ mA}$ (50% Control Voltage, 90% Output Voltage)	μs	_	1.0	
CW Thermal Resistance (Θ _{JC})	(Infinite Heat Sink at Thermal Ground Plane) I High = 4 A, I low = 10 mA @ 10 kHz	°C/W	_	30	_
Power Dissipation	(Infinite Heat Sink at Thermal Ground Plane) +I _F = 50 mA @ 1 GHz	W	_	5	_
Forward Voltage	+50 mA DC	V		0.9	
Total Capacitance	-50 V @ 1 GHz	pF		0.30	
Reverse Leakage Current	-200 V	nA	_	-20	_

Absolute Maximum Ratings^{4,5}

Parameter	Absolute Maximum
CW Incident Power ⁶ 50 mA, 1 GHz @ +85°C	51 dBm
DC Forward Voltage +250 mA	1.2 V
DC Forward Current	250 mA
DC Reverse Voltage	-400 V
Junction Temperature ^{7,8}	+175°C
Operating Temperature	-65°C to +125°C
Storage Temperature	-65°C to +150°C

^{4.} Exceeding any one or combination of these limits may cause permanent damage to this device.

MACOM does not recommend sustained operation near these survivability limits.

^{6.} Incident Power measured with Source and Load VSWR < 1.2:1.

^{7.} Operating at nominal conditions with $T_J \le +175^{\circ}\text{C}$ will ensure MTTF > 1 x 10^6 hours.

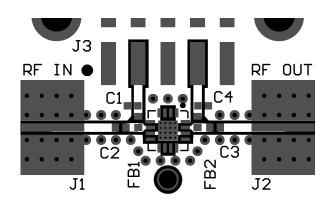
^{8.} Junction Temperature $(T_J) = T_A + (\Theta_{JC}) * (P_D)$.



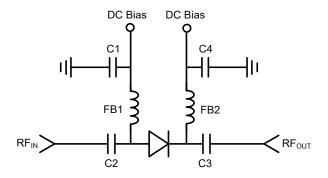
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PCB Layout



PCB Schematic



Parts List9

Part	Value	Case Style
C1, C4	62 pF	0402
C2, C3	100 pF	0402
FB1, FB2 ¹⁰	470 Ω @ 1 GHz	0402

^{9.} DC voltage with recommended components should not exceed 100 V.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 2 devices.

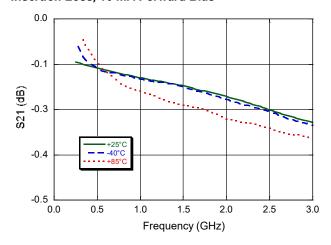
^{10. 470} Ω resistance is included in FB1 and FB2 (recommend Murata part number BLM15GG471SN1).



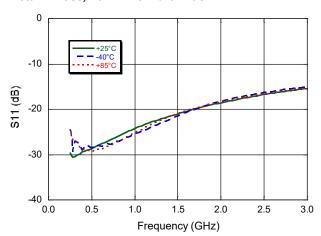
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Typical RF Small Signal Performance Curves

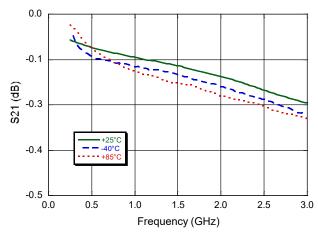
Insertion Loss, 10 mA Forward Bias



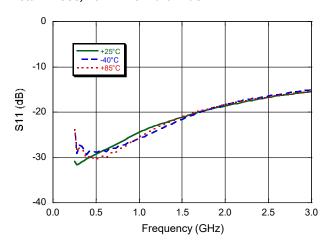
Return Loss, 10 mA Forward Bias



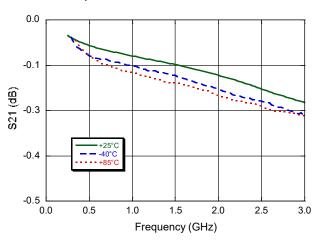
Insertion Loss, 25 mA Forward Bias



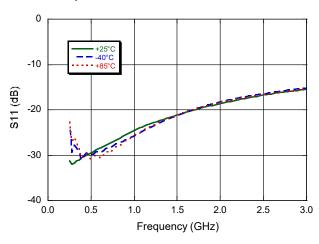
Return Loss, 25 mA Forward Bias



Insertion Loss, 50 mA Forward Bias



Return Loss, 50 mA Forward Bias



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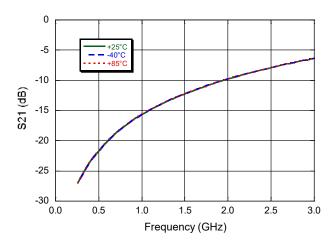
Visit www.macom.com for additional data sheets and product information.



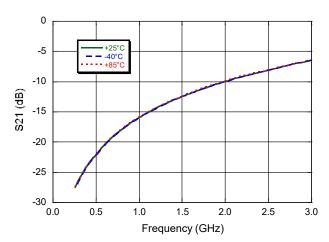
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Typical RF Small Signal Performance Curves

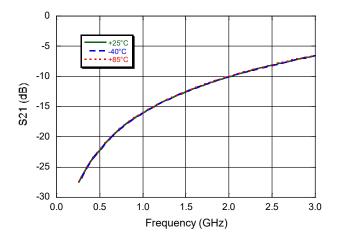
Isolation, 0 V Reverse Bias



Isolation, 20 V Reverse Bias



Isolation, 50 V Reverse Bias



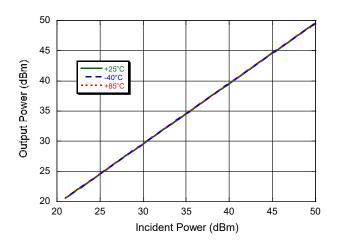


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Typical High Power Performance Curves

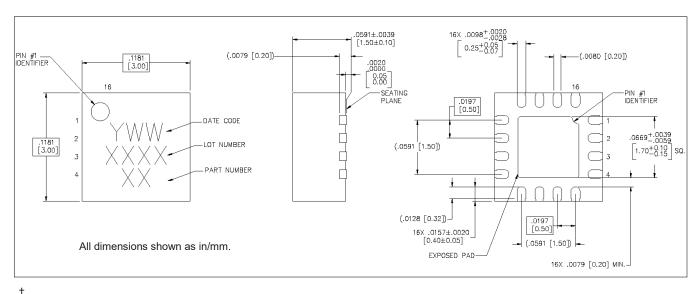
Output Power vs. Incident Power



Junction Temperature 1 GHz, CW @ T_A = +85°C



Lead-Free 3 mm 16-Lead HQFN[†]



 $^{^{\}dagger}$ Reference Application Note S2083 for lead-free solder reflow recommendations.

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