Driver Amplifier
1.7 - 2.8 GHz

MAAM-011322
Rev. V1

Features

- Driver Amplifier with Simple Bias Control Circuit
- Operation Frequency: 1.7 - 2.8 GHz
- No External Matching Components Required
- Gain: 19 dB
- Output P1dB: 24 dBm
- Output P3dB: 26 dBm
- Output IP3: 39 dBm
- Single Supply Voltage: 5 V
- Supply Current Adjustable with External Resistors
- Enable Logic Voltage: 1.8 V
- Lead-Free 3 mm 16 Lead SMT Package
- RoHS* Compliant

Applications

- 5G Massive MIMO
- Small Cell BTS
- Wireless Infrastructure
- Multi Market

Description

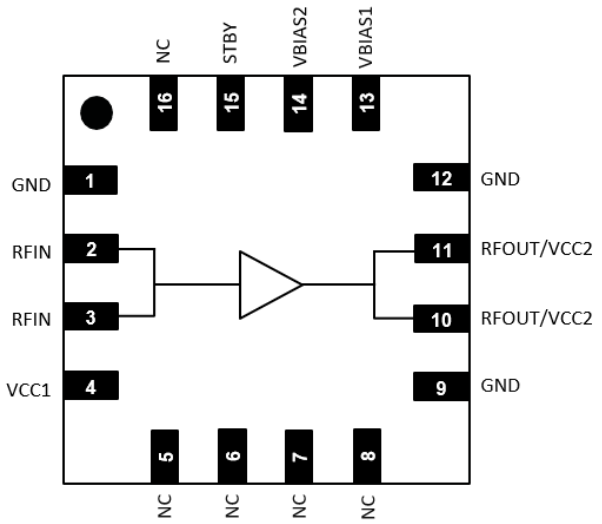
The MAAM-011322 is a wideband high linearity driver amplifier packaged in a compact 3 mm 16-Lead SMT package. This driver amplifier provides 19 dB gain and 24 dBm OP1dB with adjustable quiescent current and device ON/OFF function to support TDD system application. RF input and output ports are internally matched at the entire operating frequency range of 1.7 - 2.8 GHz.

Ordering Information[1]

Part Number	Package
MAAM-011322-TR1000	1000 piece reel
MAAM-011322-001SMB	Sample Board

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Names[2]

Pin #	Function
1, 9, 12	Ground
2, 3	RF Input
4	DC Supply Voltage
5 - 8, 16	No Connection[2]
10, 11	RF Output / DC Supply Voltage
13	Linearizer Current Adjust Bias Pin
14	Quiescent Current Adjust Bias Pin
15	Enable Logic Pin
17	Paddle[3]

2. MACOM recommends connecting No Connection (N/C) pins to ground.
3. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

Pin Description

Pin #	Name	description
1, 9, 12	GND	This pin is grounded internally.
2-3	RFIN	This pin is dc grounded with shunt matching inductor. A DC-blocking capacitor is required on this pin.
4	VCC1	Supply Voltage. Place bypass capacitor as close to pin as possible.
5-8, 16	NC	Not connected internally.
10-11	RFOUT/ VCC2	Supply Voltage through a choke coil. DC-blocking capacitor is required following the choke coil. Place bypass capacitor as close to the choke coil as possible.
13	VBIAS1	Optional linearizer current adjust bias pin by connecting an external resistor to ground.
14	VBIAS2	Optional amplifier quiescent current adjust bias pin by connecting an external resistor to ground.
15	STBY	Supply amplifier ON/OFF logic control voltage.
17	Paddle	Must be connected to RF, DC, and thermal ground. This pin is grounded internally.

AC Electrical Specifications: Freq. = 2.5 GHz, T_A = +25°C, VCC1 = VCC2 = +5 V, Z_0 = 50 Ω

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Gain	1.7 GHz 2.6 GHz 2.8 GHz	dB	— 18.0 —	19.6 19.3 18.4	—
Gain Flatness	1.7 - 2.8 GHz, Any 100 MHz	dB	—	0.3	—
Output P1dB	1.7 - 2.8 GHz	dBm	—	24	—
Output IP3	2.5 GHz, Df = 10 MHz, P_{OUT} / Tone = +12 dBm	dBm	—	39	—
Raw Linearity (ACPR)	LTE 5 MHz, PAPR = 9.9 dB, P_{OUT} = +15 dBm	dBc	—	-50	—
Input Return Loss	1.7 - 2.8 GHz	dB	—	10	—
Output Return Loss	1.7 - 2.8 GHz	dB	—	10	—
Noise Figure	1.7 - 2.8 GHz	dB	—	4	—
Power Consumption	VCC1, RFOUT/VCC2, Active state	W	—	0.58	—
Power Consumption	VCC1, RFOUT/VCC2, Standby state	W	—	0.025	—

DC Electrical Specifications: VCC1 = VCC2 = +5 V

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Standby to Active Mode Settling Time	RFIN to RFOUT gain settled within 0.1 dB of final value after STBY command	ns	—	300	—
Active to Standby Mode Settling Time	RFIN to RFOUT signal reduced at least 30 dB after STBY command	ns	—	300	—
Supply Voltage	VCC1, VCC2	V	4.75	5	5.25
Supply Current	VCC1, RFOUT/VCC2	mA	—	116	—
Logic Control Voltage	Logic High, STBY Logic Low, STBY	V	1.17 0	— —	3.3 0.63
Logic input Current	Logic High/Low, STBY	μA	-10	—	10

Truth Table

PIN	Device Control	
STBY	Logic High	Device Active Mode
	Logic Low	Device Standby mode

Recommended Operating Conditions

Parameter	Symbol	Unit	Min.	Typ.	Max.
DC Power Supply	VCC1, VCC2	V	4.75	5.0	5.25
Operating Temperature[4]	T_c	°C	-40	—	110
Junction Temperature[5,6]	T_J	°C	—	—	150

4. Tc is defined by exposed paddle temperature.
5. Operating at nominal conditions with $T_J \leq +150$ °C will ensure MTTF > 1 x 10^6 hours.
6. Junction Temperature (T_J) = T_C + Θjc * (V * I). Typical thermal resistance (Θjc) = 59.0 °C/W.
   a) For T_C = +25°C, T_J = 57.4 °C @ 5 V, 110 mA
   b) For T_C = +110°C, T_J = 148.3 °C @ 5 V,130 mA

Absolute Maximum Ratings[7,8]

Parameter	Symbol	Unit	Min.	Max.
Input Power	RFIN	dBm	—	26
DC Supply Voltage	VCC1, VCC2	V	-0.5	6.0
Logic Control Voltage	STBY	V	-0.5	3.6
Functional Temperature[4]	T_c	°C	-40	125
Storage Temperature[4]	T_c	°C	-65	150

7. Exceeding any one or combination of these limits may cause permanent damage to this device.
8. MACOM does not recommend sustained operation near these survivability limits.

Handling Procedures

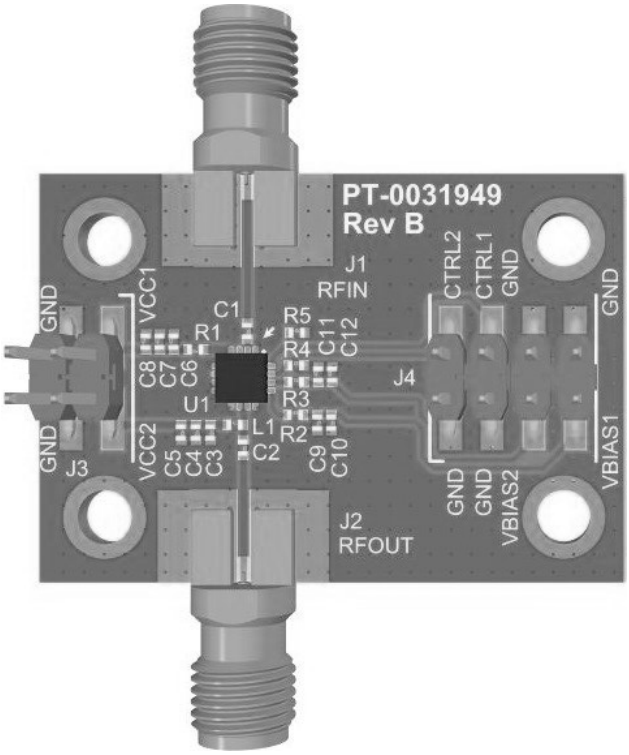
Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Parameter	Rating	Standard
Human Body Model (HBM)	1000 V (Class 1C)	ESDA/JEDEC JS-001
Charged Device Model (CDM)	1000 V (Class C3)	ESDA/JEDEC JS-002

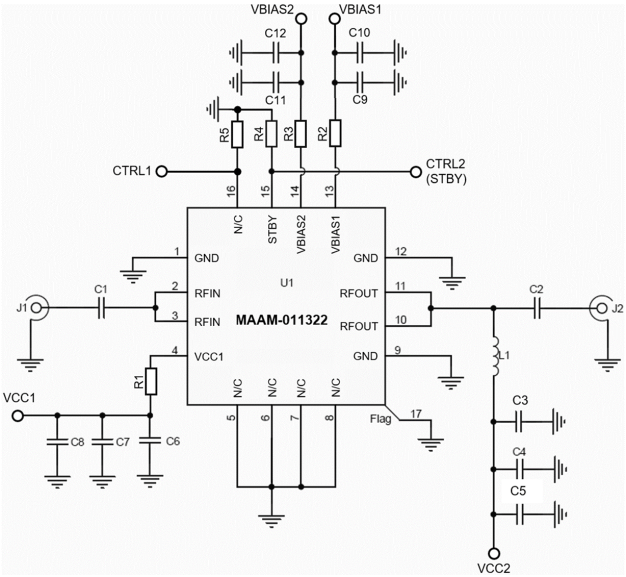
PCB Layout



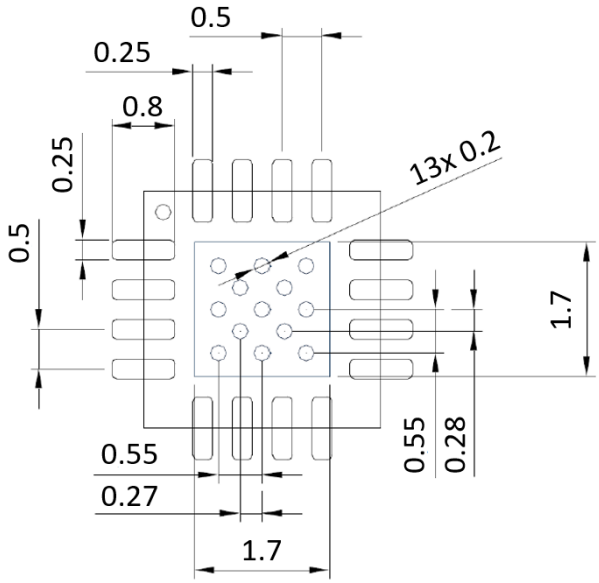
Parts List

Part	Value	Case Style
C1	12 pF	0402
C2	27 pF	0402
C3, C6	100 pF	0402
C4, C7	1 µF	0402
C5, C8 - C12	DNP	—
L1	24 nH	0402
R1	0 Ω	0402
R2 - R3, R5	DNP	—
R4	1 kΩ	0402
J1 - J2	142-0761-841	SMA, End Launch

Application Schematic



Recommended Thermal Land Pattern

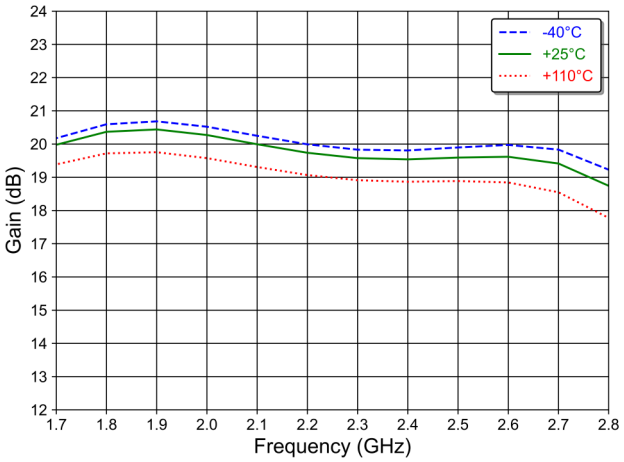


- 13 Ground Vias
- 0.2 mm Diameter, 1/2 oz. Copper

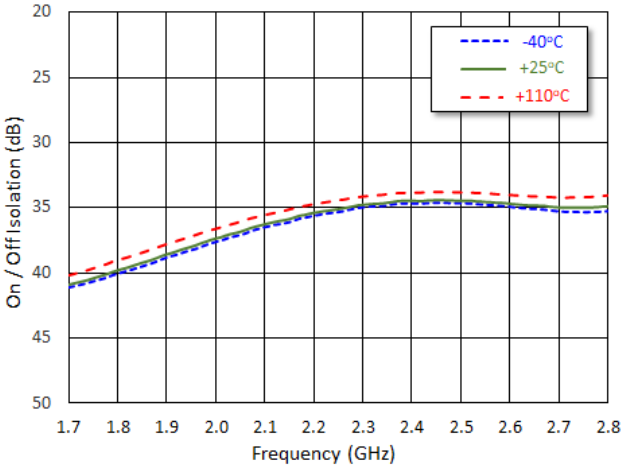
Typical Performance Curves

P_{IN} = -20 dBm, VCC1 = VCC2 = +5 V, Z_0 = 50 Ω (unless otherwise stated)

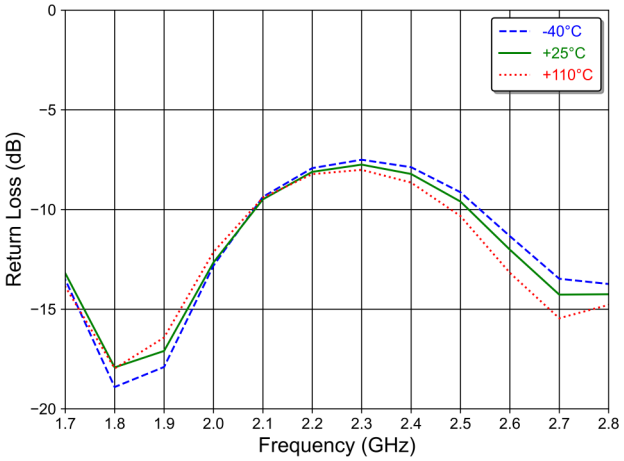
Gain[9], 1.7 - 2.8 GHz



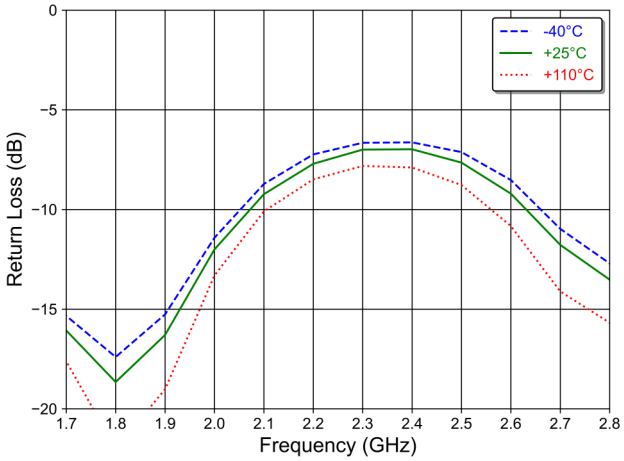
On/Off Isolation, 1.7 - 2.8 GHz



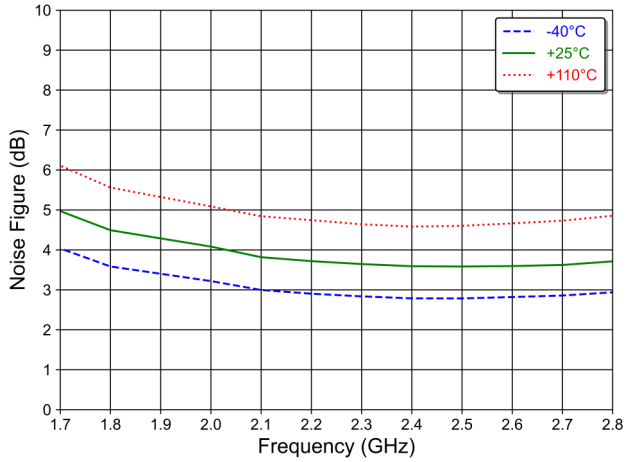
Input Return Loss, 1.7 - 2.8 GHz



Output Return Loss, 1.7 - 2.8 GHz



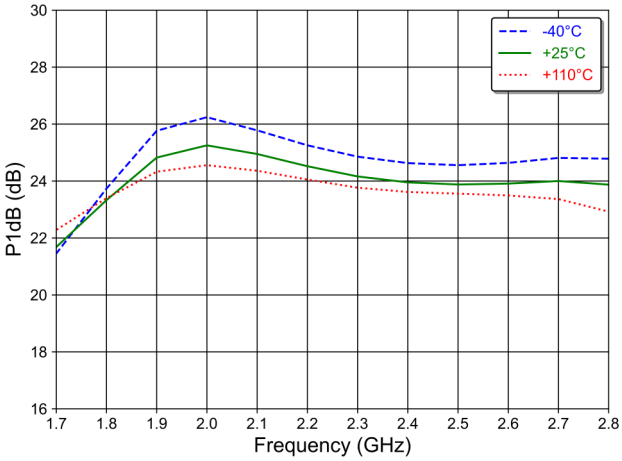
Noise Figure[9], 1.7 - 2.8 GHz



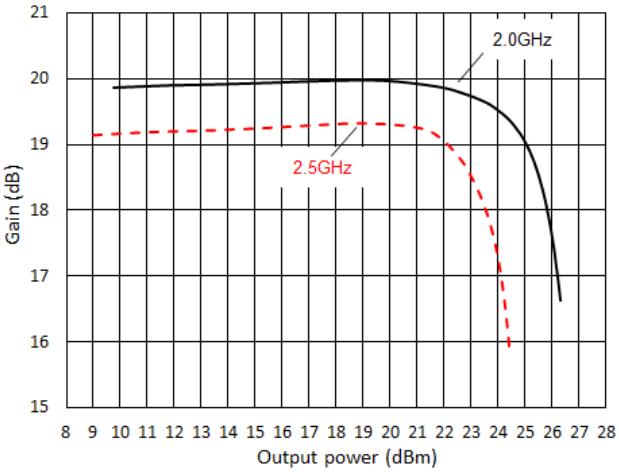
9. For Gain, Noise Figure, and Output $P1_{dB}$ plots, RF trace and connector losses are de-embedded .

Typical Performance Curves: VCC1 = VCC2 = +5 V, Z_0 = 50 Ω (unless otherwise stated)

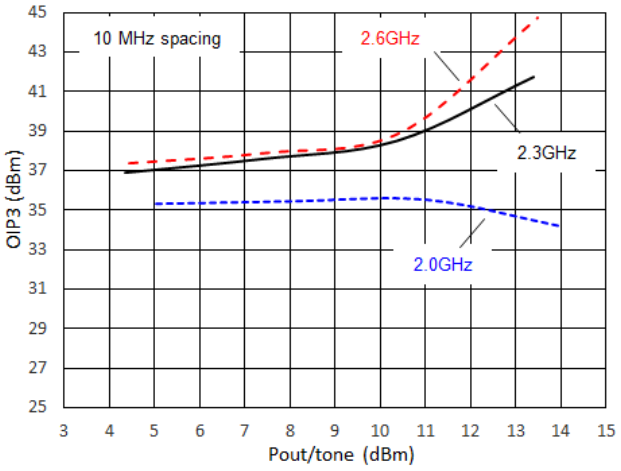
Output P_{1dB}[9], 1.7 - 2.8 GHz



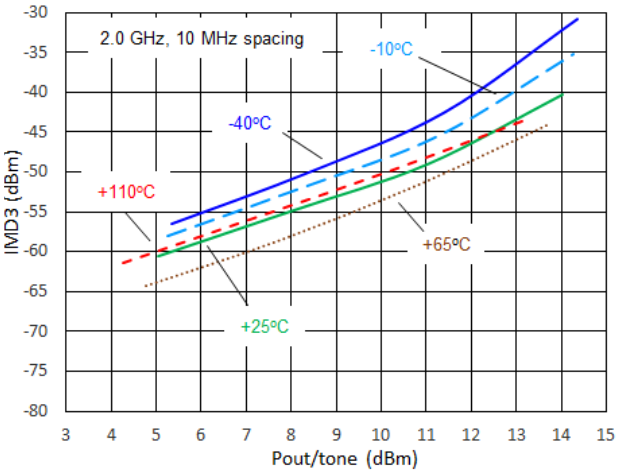
Gain vs. Output power, +25 °C



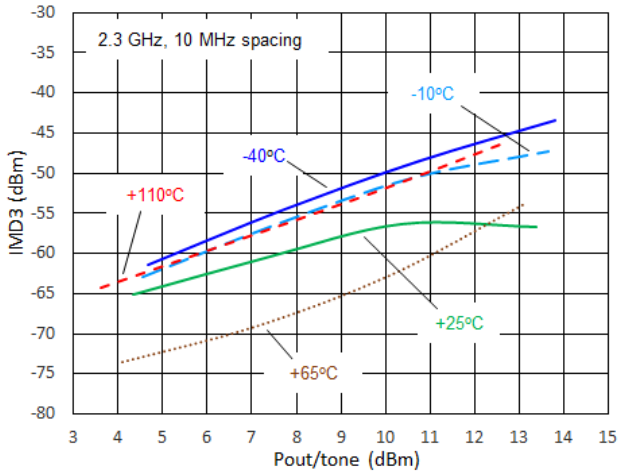
Output IP3 vs. Output power / tone, +25 °C



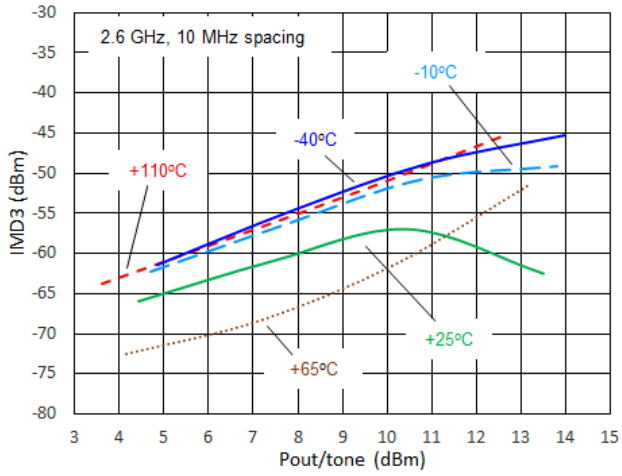
IMD3 vs. Output power / tone, 2.0 GHz



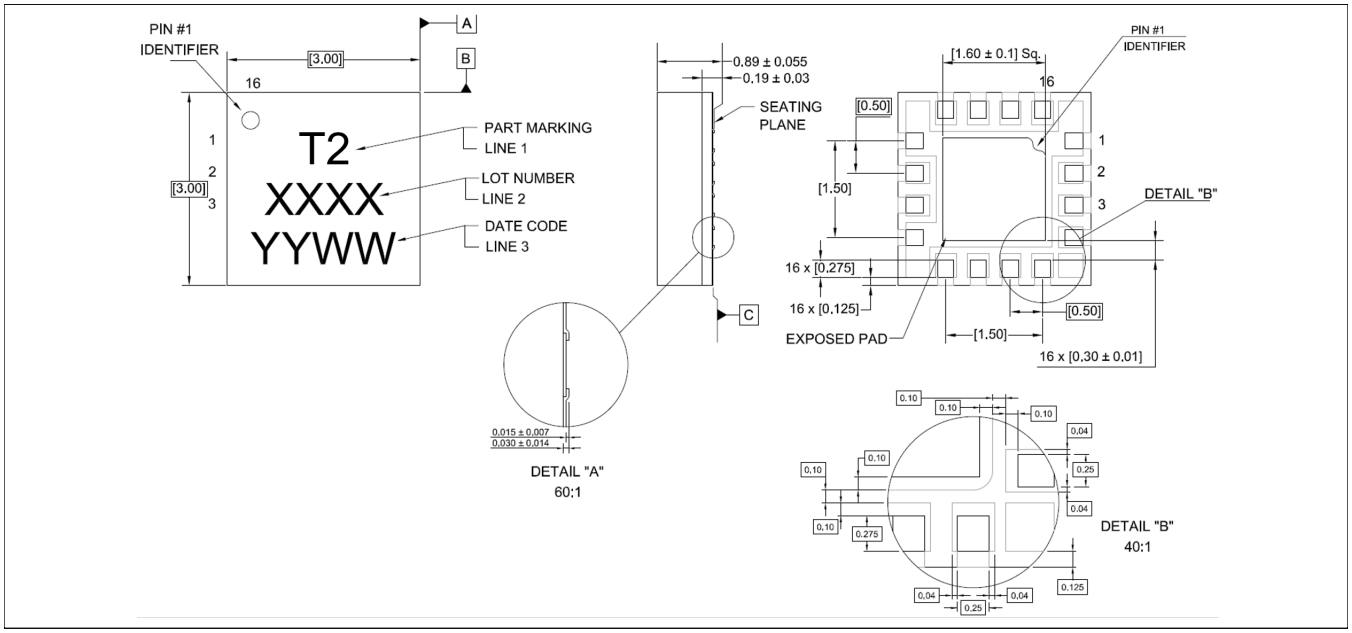
IMD3 vs. Output power / tone, 2.3 GHz



IMD3 vs. Output power / tone, 2.6 GHz



Lead-Free 3 mm 16-Lead SMT†



† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level (MSL) 3 requirements.
Plating is NiPbAu over copper.

Revision History

Rev	Date	Change Description
V1	9/25/23	First release