## Broadband CATV 5-Way Active Splitter with default loop-through Switch

 $50-1100 \mathrm{MHz}$
## Features

- Always ON loop-thru path
- 5-Way Splitter
- 1.5 dB Gain
- $+15 \mathrm{dBmV} /$ Channel Input
- 4.5 dB Noise Figure
- Lead-Free 3 mm 12-Lead PQFN Package
- RoHS* Compliant and $260^{\circ} \mathrm{C}$ Reflow Compatible


## Description

The MAAM-009779 CATV 5 -way active splitter with the default loop-through path is a GaAs MMIC which exhibits low noise figure and distortion in a lead-free 3 mm 12-lead PQFN plastic package. The design features $75 \Omega$ inputs and outputs.

The MAAM-009779 is ideally suited for multi-tuner set top boxes, home gateways, and other broadband internet based applications.

The MAAM-009779 is fabricated using M/A-COM Technology Solutions' E/D pHEMT process to realize default loop-through operation, low noise and low distortion. The process features full passivation for robust performance and reliability.

## Ordering Information ${ }^{1,2}$

| Part Number | Package |
| :---: | :---: |
| MAAM-009779-TR1000 | 1000 piece reel |
| MAAM-009779-TR3000 | 3000 piece reel |
| MAAM-009779-001SMB | Sample Test Board |

1. Reference Application Note M513 for reel size information.
2. All sample boards include 5 loose parts.

## Functional Schematic, Default On, Power Off



Functional Schematic, Power On


## Pin Configuration ${ }^{3}$

| Pin No. | Pin Name | Description |
| :---: | :---: | :---: |
| 1 | V $_{\mathrm{C}}$ | Voltage Control |
| 2 | IN | RF Input |
| 3 | OUT5 | RF Output 5 |
| 4 | S1 | Switch In |
| 5 | ALT | Alternate Output |
| 6 | OUT4 | RF Output 4 |
| 7 | OUT3 | RF Output 3 |
| 8 | OUT2 | RF Output 2 |
| 9 | OUT1 | RF Output 1 |
| 10 | FB/V DD $^{\text {Amplifier Feedback / }}$Drain Voltage |  |
| 11 | AIN | Amplifier Input |
| 12 | S2 | Switch Output |
| 13 | Paddle ${ }^{3}$ | RF and DC Ground |

3. The exposed pad centered on the package bottom must be connected to RF and DC ground.
[^0]Broadband CATV 5-Way Active Splitter with default loop-through Switch
$50-1100 \mathrm{MHz}$
Rev. V1
Electrical Specifications: Freq. $=1000 \mathrm{MHz}, \mathrm{T}_{\mathrm{A}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}, \mathrm{Z}_{0}=\mathbf{7 5} \mathbf{\Omega}^{4}$

| Parameter | Test Conditions | VD | Vc | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Gain | In to Out1, 2, 3, 4, or 5 | 5 | 3.3 | dB | 1.0 | 1.5 | 4.5 |
| Insertion Loss | In to Out5 | 0 | 0 | dB | - | 0.6 | 1.0 |
| Noise Figure | In to Out1, 2, 3, 4, or 5 | 5 | 3.3 | dB | - | 4.5 | - |
| Gain Flatness | In to Out1 | 5 | 3.3 | dB | - | 1.0 | 1.5 |
| Input Return Loss | Input | 5 | 3.3 | dB | - | 9 | - |
| Input Return Loss | Input | 0 | 0 | dB | - | 12 | - |
| Output Return Loss | Output | 5 | 3.3 | dB | - | 12 | - |
| Output Return Loss | Output | 0 | 0 | dB | - | 12 | - |
| Out to Out Isolation | Out1 to Out2, 3, 4 or 5 | 5 | 3.3 | dB | - | 22 | - |
| Out to Out Isolation | Out1 to 2,3 , or 4 | 0 | 0 | dB | - | 30 | - |
| CTB | 132 Channels, $+15 \mathrm{dBmV} /$ Channel at the input | 5 | 3.3 | dBc | - | -65 | - |
| CSO | 132 Channels, $+15 \mathrm{dBmV} /$ Channel at the input | 5 | 3.3 | dBc | - | -60 | - |
| Reverse Isolation | Out1, 2, 3, 4 to In | 5 | 3.3 | dB | - | 32 | - |
| Reverse Isolation | Out5 to In | 5 | 3.3 | dB | - | 23 | - |
| Reverse Isolation | Out1, 2, 3, 4 to In | 0 | 0 | dB | - | 40 | - |
| OIP2 | 500 MHz , 2-tone, 6 MHz spacing, -10 dBm Pout | 5 | 3.3/0 | dBm | - | 42 | - |
| OIP3 | 500 MHz , 2-tone, 6 MHz spacing, -10 dBm Pout | 5 | 3.3/0 | dBm | - | 22 | - |
| P1dB | 500 MHz | 5 | 3.3 | dBm | - | 6 | - |
| P1dB | 500 MHz | 5 | 0 | dBm | - | 25 | - |
| $\mathrm{I}_{\mathrm{DD}}$ |  | 5 | 3.3 | mA | - | 110 | 150 |
| $\mathrm{I}_{\mathrm{C}}$ |  | 5 | 3.3 | $\mu \mathrm{A}$ | - | 210 | 250 |

4. The unpowered state is the same as V control $=0 \mathrm{~V}$

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Absolute Maximum Ratings ${ }^{5,6,7}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Max Input Power | +5 dBm |
| $\mathrm{V}_{\mathrm{DD}}$ | +10.0 V |
| $\mathrm{~V}_{\text {CONTROL }}$ | +8.5 V |
| Operating Temperature Junction Temperature ${ }^{8}$ | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $+150^{\circ} \mathrm{C}$ |

5. Exceeding any one or combination of these limits may cause permanent damage to this device.
6. M/A-COM Technology does not recommend sustained operation near these survivability limits.
7. These operating conditions will ensure MTTF $>1 \times 10^{6}$ hours.
8. Junction Temperature $\left(T_{J}\right)=T_{A}+\Theta j c *\left(V{ }^{*}\right)$

Typical thermal resistance $(\Theta \mathrm{jc})=73^{\circ} \mathrm{C} / \mathrm{W}$.
a) For $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$,
$\mathrm{T}_{\mathrm{J}}=80^{\circ} \mathrm{C} @ 5.0 \mathrm{~V}, 150 \mathrm{~mA}$
b) For $T_{A}=85^{\circ} \mathrm{C}$,
$\mathrm{T}_{\mathrm{J}}=136{ }^{\circ} \mathrm{C} @ 5.0 \mathrm{~V}, 140 \mathrm{~mA}$

## Schematics Including Off-Chip Components ${ }^{9}$

Power On Condition


Truth Table ${ }^{10}$

| $\mathbf{V}_{\mathrm{DD}}$ | $\mathbf{V}_{\text {CONTROL }}$ | IN - OUT1, 2, 3 or $\mathbf{4}$ | IN - OUT5 |
| :---: | :---: | :---: | :---: |
| 1 | 1 | On | On |
| 0 | 0 | Off | On |

10.Logic " 1 " for $V_{D D}=+5$ volts and $V_{\text {CONTROL }}=+3.3$ volts typical.

## Power Off, Default on Condition


9. The exposed pad centered on the package bottom must be connected to ground for RF, DC and thermal considerations.

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## Recommended PCB Layout



## Block Diagram RF Signal Flow



PCB Land Pattern


Off-Chip Component Values

| Component | Value | Package |
| :---: | :---: | :---: |
| $\mathrm{C} 1-\mathrm{C} 12$ | $0.01 \mu \mathrm{~F}$ | 0402 |
| C 13 | 1.0 pF | 0402 |
| $\mathrm{~L}^{11}$ | $1 \mu \mathrm{H}$ | 1210 |
| L2 - L7 | 7.5 nH | 0402 |
| R1, R2 | $300 \Omega$ | 0402 |
| R3 - R6 | $180 \Omega$ | 0402 |
| R7 | $250 \Omega$ | 0402 |
| R8, R9 | $22 \mathrm{~K} \Omega$ | 0402 |

11. L1 supplied from EPCOS, part number B82422A1102K100


Gain


Input Return Loss (power on)


Output Return Loss (power on)


Gain to $\mathbf{3} \mathbf{G H z}$


Input Return Loss (power off)


Output Return Loss (power off)


## Typical Performance Curves

Insertion Loss to 1 GHz (power off)


Reverse Isolation to 3 GHz (power on)



Insertion Loss to $\mathbf{3} \mathbf{G H z}$ (power off)


Reverse Isolation to 3 GHz (power off


## Typical Performance Curves

## Out to Out Isolation to 1 GHz (power on)



Out to Out Isolation to 1 GHz (power off)


|  |  |  |
| :---: | :---: | :---: |

## Out to Out Isolation to $3 \mathbf{G H z}$ (power on)



Out to Out Isolation to $\mathbf{3} \mathbf{G H z}$ (power off)


## Broadband CATV 5-Way Active Splitter with default loop-through Switch

## Lead-Free 3 mm 12-Lead PQFN ${ }^{\dagger}$



[^1]
## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices. An external protection circuit using an anti-parallel diode pair can be used to protect the IC.
Please reference application note AN3028 on http://www.macomtech.com for further detail.

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[^0]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

[^1]:    $\dagger$ Reference Application Note S2083 for lead-free solder reflow recommendations.
    Meets JEDEC moisture sensitivity level 1 requirements.
    Plating is $100 \%$ matte tin over copper.

