

# Broadband CATV Single Ended 4-Way Active Splitter 50 - 1100 MHz

Rev. V1

#### Features

- 4-Way Splitter
- Single Ended Input and Outputs
- 2.5 dB Gain
- +15 dBmV /Channel Input
- 4.5 dB Noise Figure
- +3.3 Volt Bias
- Lead-Free 3 mm 12-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant and 260°C Reflow Compatible

#### Description

The MAAM-009452 CATV 4-way active splitter is a GaAs MMIC which exhibits low noise figure and distortion in a lead-free 3mm 12-lead PQFN plastic package. The design features 75  $\Omega$  inputs and outputs.

This device is ideally suited for multi-tuner set top boxes, home gateways, and other broadband internet based applications.

The MAAM-009452 is fabricated using M/A-COM Technology Solutions pHEMT process to realize low noise and low distortion. The process features full passivation for robust performance and reliability.

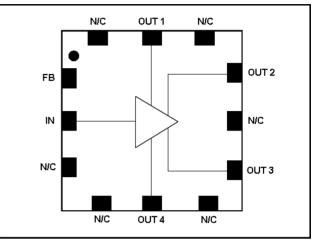
# Ordering Information <sup>1,2</sup>

Part Number	Package
MAAM-009452-TR1000	1000 piece reel
MAAM-009452-TR3000	3000 piece reel
MAAM-009452-001SMB	Sample Test Board

1. Reference Application Note M513 for reel size information.

2. All sample boards include 5 loose parts.

#### **Functional Schematic**



### **Pin Configuration**

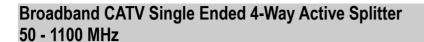
Pin No.	Pin Name	Description
1	FB	Feedback/Bias
2	IN	RF Input
3	N/C	No Connection
4	N/C	No Connection
5	OUT4	RF Output 4
6	N/C	No Connection
7	OUT3	RF Output 3
8	N/C	No Connection
9	OUT2	RF Output 2
10	N/C	No Connection
11	OUT1	RF Output 1
12	N/C	No Connection
13	Paddle <sup>3</sup>	RF and DC Ground

3. The exposed pad centered on the package bottom must be connected to RF and DC ground.

\* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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#### Parameter **Test Conditions** Units Min. Max. Typ. Gain IN to OUT1, OUT2, OUT3 & OUT4 dB 2.3 2.5 4.8 Gain Flatness IN to OUT1, OUT2, OUT3 & OUT4 dB 0.5 IN to OUT1. OUT2. OUT3 & OUT4 Noise Figure dB 4.5 Input Return Loss IN dB \_ 15 OUT1, OUT2, OUT3, OUT4 **Output Return Loss** dB 9 \_ -Composite Triple Beat, CTB 132 channels, +15 dBmV/channel at the input dBc -65 Composite Second Order, CSO 132 channels, +15 dBmV/channel at the input dBc -56 \_ \_ Reverse Isolation OUT1, OUT2, OUT3 & OUT4 to IN dB 28 \_ Output to Output Isolation Isolation between all RF outputs dB 21 --IN to OUT1, OUT2, OUT3, OUT4 dB P1dB 5 500 MHz, 2-tone, 6 MHz spacing, -15 dBm Pout dBm OIP3 25 OIP2 500 MHz, 2-tone, 6 MHz spacing, -15 dBm Pout dBm 40 \_ $I_{DD}$ V<sub>DD</sub>= +3.3 Volts mΑ 96 120

### Electrical Specifications: Freq. = 1000 MHz, $T_A = 25^{\circ}C$ , $V_{DD} = +3.3$ Volts, $Z_0 = 75 \Omega$

### Absolute Maximum Ratings <sup>4,5,6</sup>

Parameter	Absolute Maximum
RF Input Power	12 dBm
Voltage	10.0 volts
Operating Temperature	-20°C to +85°C
Junction Temperature <sup>7</sup>	+150°C
Storage Temperature	-65°C to +150°C

4. Exceeding any one or combination of these limits may cause permanent damage to this device.

- M/A-COM Technology Solutions does not recommend sustained operation near these survivability limits.
- 6. These operating conditions will ensure MTTF >  $1 \times 10^6$  hours.
- 7. Junction Temperature  $(T_J) = T_C + \Theta jc^* (V^* I)$ Typical thermal resistance  $(\Theta jc) = 101^\circ C/W$ .

a) For  $T_c = 25^{\circ}C$ ,

T<sub>J</sub> = 65°C @ 3.3 V, 120 mA

b) For  $T_c = 85^{\circ}C$ ,

#### Handling Procedures

Please observe the following precautions to avoid damage:

#### Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

An external protection circuit using an inexpensive anti-parallel diode pair can be used to protect the IC.

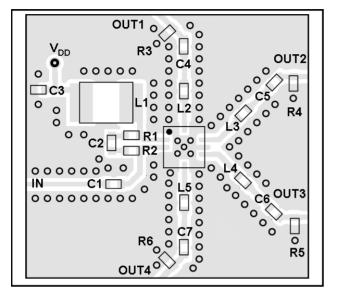
Please reference application note AN3028 on http://www.macomtech.com for further detail.

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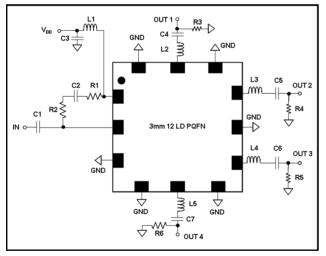
<sup>2</sup> 

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#### Recommended PCB

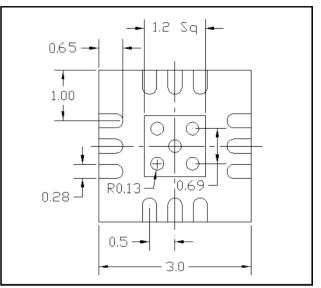


# Schematic Including Off-Chip Components<sup>8</sup>



8. The exposed pad centered on the package bottom must be connected to ground for RF, DC and thermal considerations.

### PCB Land Pattern



### **Off-Chip Component Values**

Component	Value	Package
C1 - C7	0.01 µF	0402
L1 <sup>9</sup>	1 µH	1210
L2 - L5	15 nH	0402
R1, R2	240 Ω	0402
R3 - R6	750 Ω	0402

9. L1 supplied from EPCOS, part number B82422A1102K100

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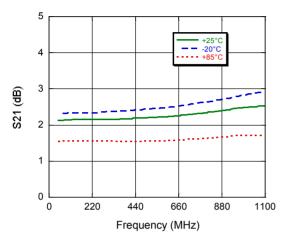
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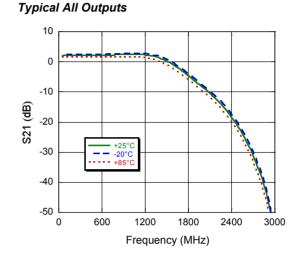
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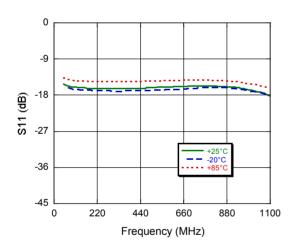
### **Typical Performance Curves**

Gain to 1100 MHz Typical All Outputs



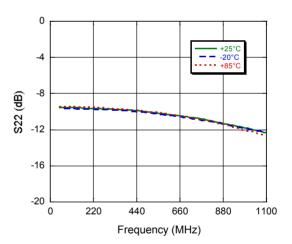


Input Return Loss

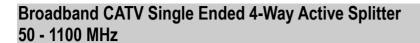




Gain to 3000 MHz



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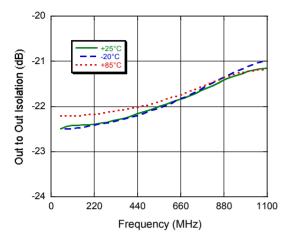




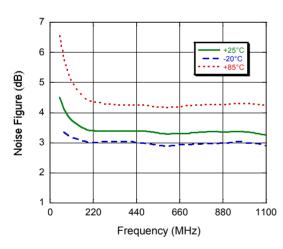
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# **Typical Performance Curves**

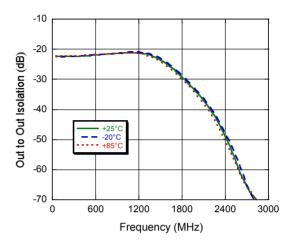
#### OUT-OUT Isolation to 1100 MHz Typical Between All Outputs



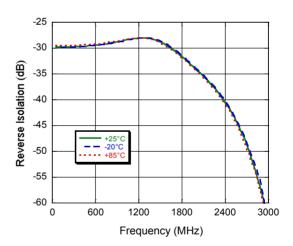
Noise Figure Typical All Outputs



OUT-OUT Isolation to 3000 MHz Typical Between All Outputs



Reverse Isolation to 3000 MHz Typical From All Outputs to Input



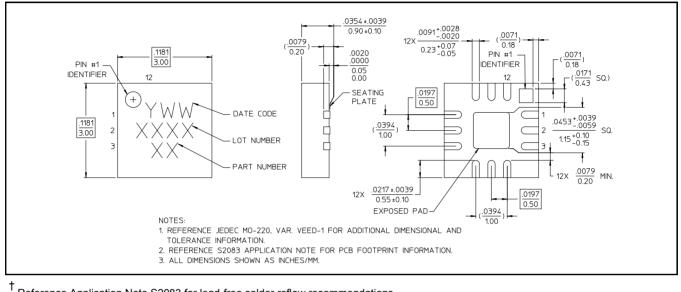
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# Lead-Free 3 mm 12-Lead PQFN<sup>†</sup>



Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

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