Ka Band, Low Noise Amplifier 27.0 - 31.5 GHz



MAAL-011240

Rev. V1

Features

• Low Noise Figure: 1.5 dB

Gain: 25.5 dBP1dB: 18 dBmOIP3: 27 dBm

Bias Voltage: V_{DD} = 3.5 V
 Bias Current: I_{DSQ} = 90 mA
 50 Ω Matched Input and Output
 3 mm AQFN-12LD Package

RoHS* Compliant

Applications

- Satellite Communications
- Low Earth Orbit Space Payloads
- GEO High Throughput Satellite
- Radar
- EW

Description

The MAAL-011240 is an easy to use low noise amplifier. It operates from 27 to 31.5 GHz and provides 1.5 dB noise figure, 25.5 dB gain and a P1dB of 18 dBm. The input and output are fully matched to 50 Ω with typical return loss >12 dB. This part is packaged in a 3 mm AQFN-12LD package.

This product is fabricated using a GaAs pHEMT process which features full passivation for enhanced reliability.

The MAAL-011240 can be used as a low noise amplifier stage or as a driver stage in higher power applications. This device is ideally suited for Ka-band communication systems.

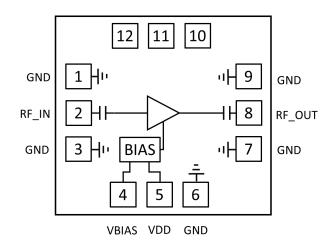
The MAAL-011240 is also available in die form under MAAL-011240-DIE part number.

Ordering Information¹

| Part Number | Package |
|--------------------|-----------------|
| MAAL-011240-TR1000 | 1000 piece reel |
| MAAL-011240-TR3000 | 3000 piece reel |
| MAAL-011240-SB1 | Sample Board |

1. Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration

| Pin# | Function | Description |
|-----------|-------------------|----------------------------|
| 1,3,6,7,9 | GND | Ground |
| 2 | RF _{IN} | RF Input |
| 4 | VBIAS | Bias Voltage |
| 5 | VDD | Drain Supply |
| 8 | RF _{OUT} | RF Output |
| 10,11,12 | NC | Not Connected ² |
| Paddle | GND ³ | Ground |

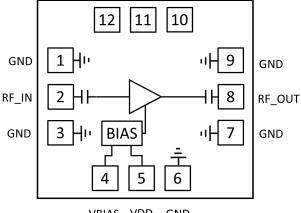
- These pins are not connected internally. MACOM recommends these are grounded on the application PCB.
- The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

^{*} Restrictions on Hazardous Substances, compliant to current RoHS EU directive.



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Pin Configuration and Functional Descriptions



VBIAS VDD GND

| Pin# | Pin Name | Description |
|-----------|----------|---|
| 1,3,6,7,9 | GND | These pins are grounded internally. It is recommended these are grounded on the application PCB. |
| 2 | RF_IN | RF Signal Input. This pad is matched to 50 Ω and is AC coupled |
| 4 | VBIAS | A voltage can be applied to this pin to set the required IDSQ as described in the application section |
| 5 | | Drain bias for the amplifier. External bypass capacitors are required as described in the applications schematic. |
| 8 | | RF Signal Output. This pad is matched to 50 Ω and is AC coupled. There is a shunt inductor to ground providing a DC ground path. |
| 10,11,12 | | These pins are not connected internally. MACOM recommends these are grounded on the application PCB. |
| Paddle | | RF, DC and thermal ground |



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Electrical Specifications: Freq. = 27.0 - 31.5 GHz, T_A = 25°C, V_D = 3.5 V, Z_0 = 50 Ω

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|---|---|-------|--------------|--------------|------|
| Small Signal Gain | P _{IN} = -20 dBm 27.0 GHz 31.5 GHz | dB | 23.0 23.5 | 25.0 25.5 | |
| Small Signal Gain Variation over Temperature | _ | dB/°C | _ | 0.02 | _ |
| Gain Flatness | _ | dB | _ | 0.5 | _ |
| Noise Figure | _ | dB | _ | 1.5 | _ |
| Input Return Loss | _ | dB | _ | 10 | |
| Output Return Loss | _ | dB | _ | 10 | |
| P1dB | 27.0 GHz 31.5 GHz | dBm | 16 16 | 18 18 | _ |
| Output 3rd Order Intercept | P _{IN} = -18 dBm/tone, 10 MHz spacing | dBm | _ | 27 | _ |
| Supply Current | _ | mA | _ | 90 | _ |

Absolute Maximum Ratings^{4,5}

| Parameter | Absolute Maximum |
|-------------------------------------|------------------|
| Input Power | 20 dBm |
| Drain Voltage | 5 V |
| Junction Temperature ^{6,7} | +160°C |
| Operating Temperature | -40°C to +85°C |
| Storage Temperature | -65°C to +125°C |

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- 6. Operating at nominal conditions with $T_J \le +150^{\circ}C$ will ensure MTTF > 1 x 10^6 hours.
- Junction Temperature (T_J) = T_C + Θjc * (V * I)
 Typical thermal resistance (Θjc) = 66.5 °C/W.
 a) For T_C = +25°C,
 T_J = 50.6 °C @ 3.5 V, 110 mA

b) For $T_C = +85^{\circ}C$, $T_J = 110.6 ^{\circ}C @ 3.5 \text{ V}$, 110 mA

Maximum Operating Conditions

| Parameter | Maximum |
|-------------------------------------|----------------|
| Input Power | 0 dBm |
| V_{DD} | 4.5 V |
| Junction Temperature ^{6,7} | +150°C |
| Operating Temperature | -40°C to +85°C |

Handling Procedures

Please observe the following precautions to avoid damage:

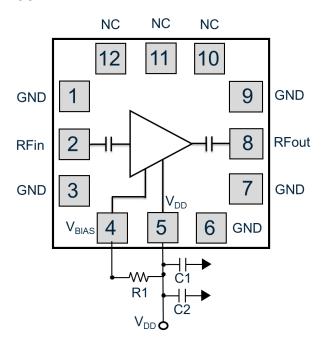
Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1A (250 V), Class C2a (500 V) CDM devices.



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Application Schematic



Parts List

| Part | Value | Case Style |
|------|--------|------------|
| C1 | 100 pF | 0402 |
| C2 | 0.1 μF | 0402 |
| R1 | - | 0402 |

Application Circuit and Operation

The basic application circuit is shown below. Place C1 capacitor as close to the package as physically possible. The position of the C2 capacitor is not as critical but should also be placed as closely as practically possible.

To ensure proper grounding the number of ground vias under the device should be maximized (within practical limits imposed by the PCB vendor).

Set IDQ by adjusting R1

The value of R1 sets IDQ according to the table below:

| R1 (Ω) | IDQ (mA) |
|--------|----------|
| 3.5k | 45 |
| 2.9k | 50 |
| 2k | 60 |
| 1.5k | 70 |
| 1.2k | 80 |
| 900 | 90 |
| 800 | 100 |
| 690 | 110 |
| 600 | 120 |

Operating the MAAL-011240

Turn-on

- 1. Apply V_D (+3.5 V)
- 2. Apply RF_{IN} signal

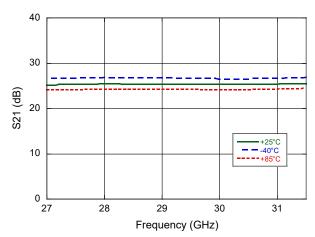
Turn-off

- 1. Remove RF_{IN} signal.
- 2. Decrease V_D to 0 V

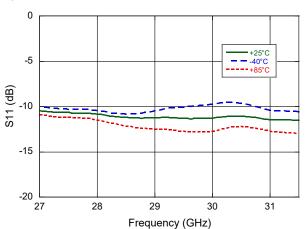


Typical Performance Curves @ V_D = 3.5 V, I_D = 90 mA, Z_0 = 50 Ω

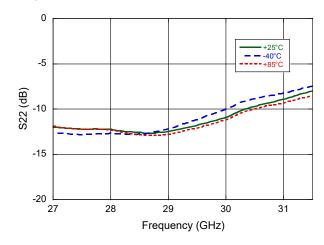
Gain



Input Return Loss



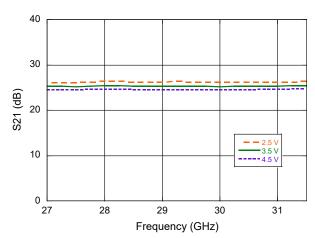
Output Return Loss



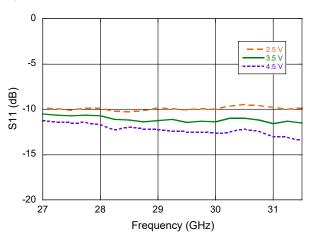


Typical Performance Curves @ $I_D = 90$ mA, $Z_0 = 50$ Ω

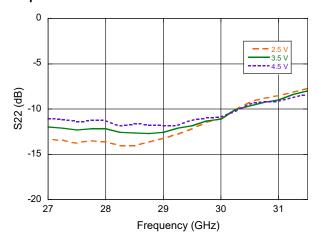
Gain



Input Return Loss

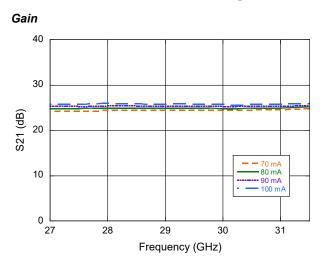


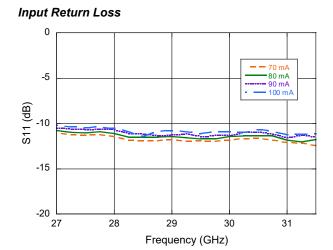
Output Return Loss



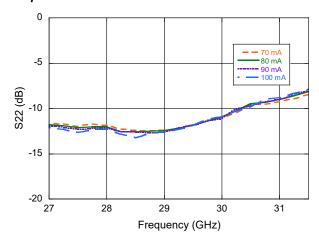


Typical Performance Curves @ V_D = 3.5 V, Z_0 = 50 Ω





Output Return Loss

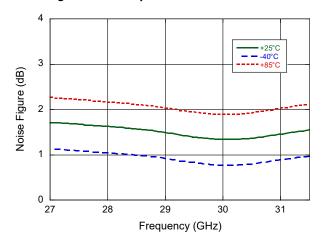




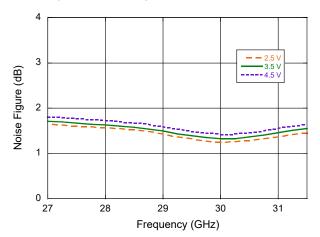
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Typical Performance Curves @ V_D = 3 V, I_D = 90 mA, 25°C, Z_0 = 50 Ω

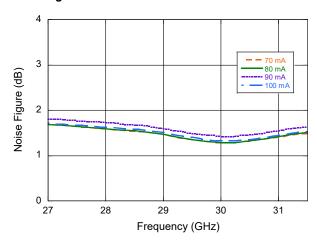
Noise Figure over Temperature



Noise Figure over Voltage



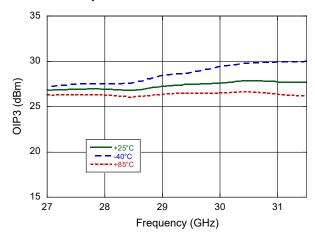
Noise Figure over Current



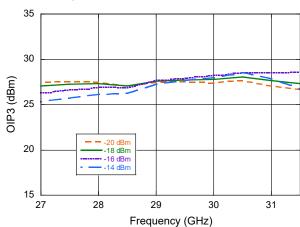


Typical Performance Curves @ V_D = 3 V, I_D = 90 mA, P_{IN} = -18 dBm, 25°C, Z_0 = 50 Ω

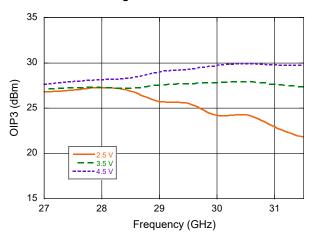
OIP3 over Temperature



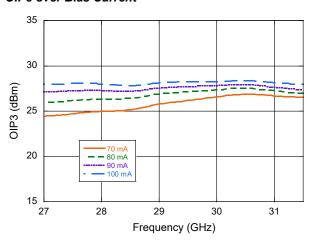
OIP3 over Input Power



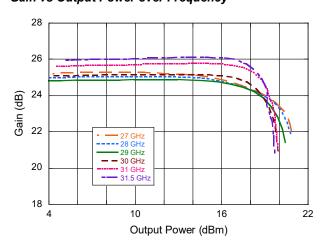
OIP3 over Bias Voltage



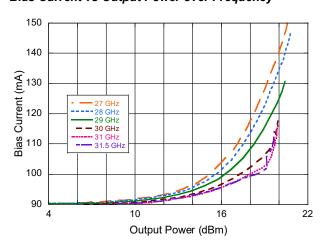
OIP3 over Bias Current



Gain vs Output Power over Frequency



Bias Current vs Output Power over Frequency



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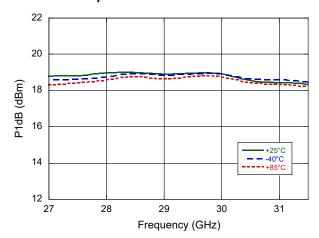
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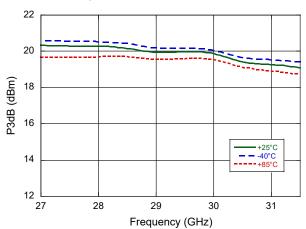


Typical Performance Curves @ V_D = 3.5 V, I_D = 90 mA, 25°C, Z_0 = 50 Ω

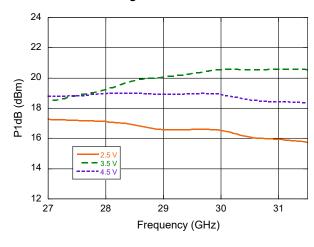
P1dB over Temperature



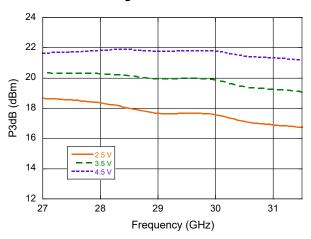
P3dB over Temperature



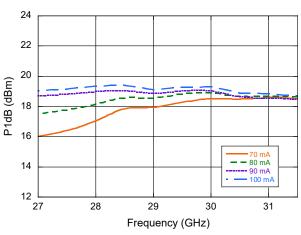
P1dB over Bias Voltage



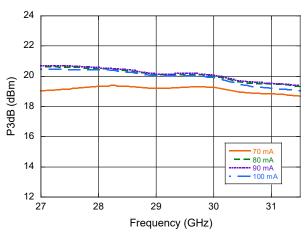
P3dB over Bias Voltage



P1dB over Bias Current



P3dB over Bias Current

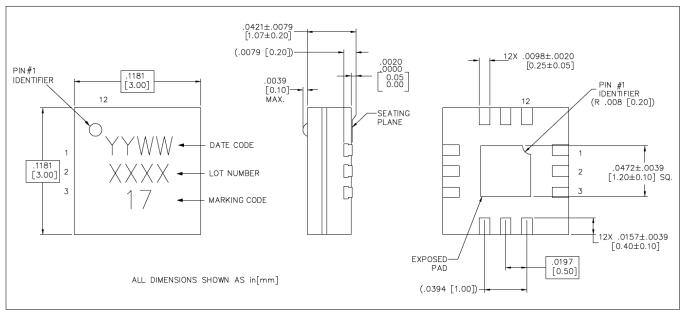


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Lead-Free 3 mm AQFN-12LD 8,9,10,11,12



- 8. All units in in(mm), unless otherwise noted, with a tolerance of $.xxxx = \pm .0005$ in and $.xxx = \pm .005$ in.
- 9. Lead finish: NiPdAu plating
- 10. Marking: line 1 yyww = date code, line 2 wafer lot number; line 3 part number
- 11. Reference Application Note S2083 for lead-free solder reflow recommendations.
- 12. Meets JEDEC moisture sensitivity level 3 requirements.

Ka Band, Low Noise Amplifier 27.0 - 31.5 GHz



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