

Features

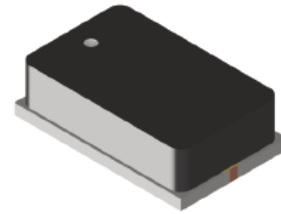
- Surface Mount Limiter in 8 mm x 5 mm x 2.5 mm Package
- Incorporates PIN Limiter Diodes
- DC Blocks & DC Return
- Higher Peak Power Handling than Plastic: 125 W
- Higher Average Power Handling than Plastic: 5 W CW
- Lower Insertion Loss: 0.4 dB
- Lower Flat Leakage Power: 21 dB
- RoHS* Compliant

Description

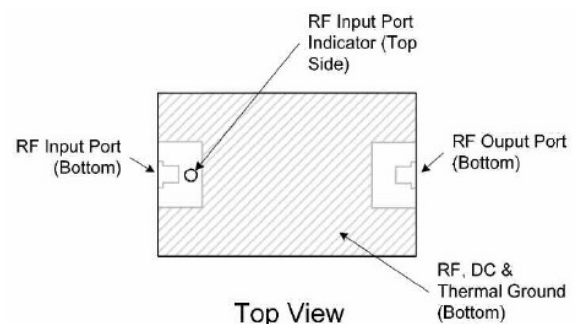
The LM501202-L-C-300 surface mount silicon PIN diode limiter is manufactured using a proven hybrid manufacturing process incorporating PIN diodes and passive devices integrated within a ceramic substrate. This low profile, compact, surface mount component offers superior low and high signal performance to comparable MMIC devices in QFN packages. The limiter modules are designed to optimize small signal insertion loss, noise figure and high signal flat leakage performance from 0.4 - 2.5 GHz.

Using PIN diodes with lower thermal resistance (<70°C/W), RF CW incident power levels of 37 dBm and RF peak incident power levels of 51 dBm @ 1 μs RF pulse width, 0.1% duty cycle are very achievable. In addition, this design concept provides lower flat leakage power (<21 dBm) and lower spike leakage energy (<0.2 Ergs) for superior LNA protection.

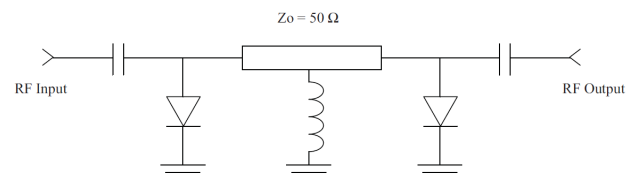
This LM501202-L-C-300 limiters are ideal for receiver protection in octave band radar applications, requiring high volume, surface mount, solder re-flow manufacturing. These products are durable, reliable, and capable of meeting all military, commercial, and industrial environments. The devices are fully RoHS compliant and are available in tube or tape-reel.



Pin Out



Limiter Schematic



Ordering Information

Part Number	Package
LM501202-L-C-300-T	tube packaging
LM501202-L-C-300-R	250 or 500 piece reel
LM501202-L-C-300-W	waffle packaging
LM501202-L-C-300-E	RF evaluation board

* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

Electrical Specifications: Freq. = 0.4 - 2.5 GHz, P_{IN} = 0 dBm, T_A = +25°C, Z₀ = 50 Ω

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Insertion Loss	0.4 GHz ≤ F ≤ 2.5 GHz, P _{IN} ≤ 0 dBm	dB	—	-0.4	-0.6
Return Loss	0.4 GHz ≤ F ≤ 2.5 GHz, P _{IN} ≤ 0 dBm	dB	-18	-20	—
P1dB	0.4 GHz ≤ F ≤ 2.5 GHz, P _{IN} ≤ 0 dBm	dBm	7	8	10
2nd Harmonic	Output Frequency = 2 GHz	dBc	45	50	—
Peak Incident Power	RF Pulse Width = 1 μs, 0.1% Duty	dBm	—	51	52
CW Incident Power	0.4 GHz ≤ F ≤ 2.5 GHz	dBm	—	36	37
Flat Leakage Power	P _{IN} = 50 dBm RF Pulse Width = 1 μs, 0.1% Duty	dBm	—	21	23
Spike Leakage Power	P _{IN} = 50 dBm peak, RF Pulse Width = 1 μs, 0.1% Duty	Ergs	—	0.3	0.5
Recovery Time	50% falling edge of RF Pulse to 1 dB IL, P _{IN} = 50 dBm peak, RF Pulse Width = 1 μs, 0.1% Duty	ns	—	75	150

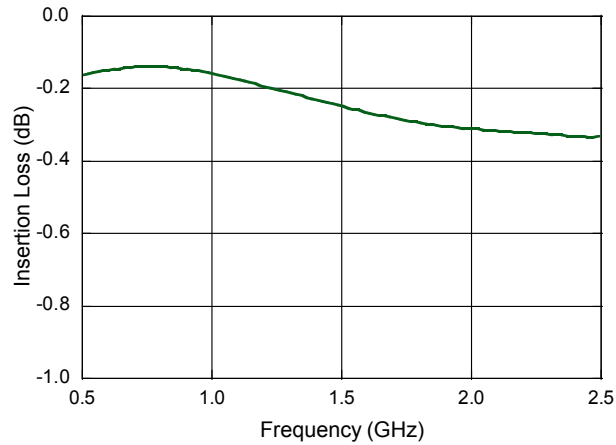
Absolute Maximum Ratings^{1,2}

Parameter	Absolute Maximum
RF CW Incident Power @ +85°C, Source & Load VSWR <1.2:1 Derate linearly to 0 W @ T _C = +150°C ³	37 dBm
RF Peak Incident Power @ +85°C, Source & Load VSWR <1.2:1 Derate linearly to 0 W @ T _C = +150°C ³	51 dBm
Insertion Loss Rate of Change with Operating Temperature	-0.003 dB / °C
Thermal Resistance Junction to bottom surface of package	70°C/W
Junction Temperature	+175°C
Operating Temperature	-65°C to +125°C
Storage Temperature	-65°C to +150°C
Assembly Temperature	260°C for 10 seconds

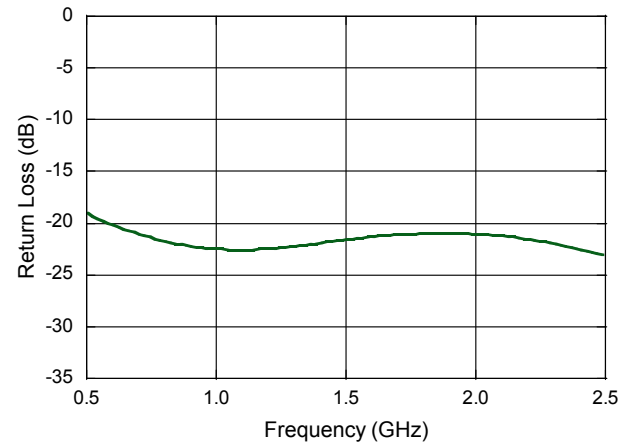
- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- T_C is defined as the temperature of the bottom surface of the package.

Typical Performance Curves

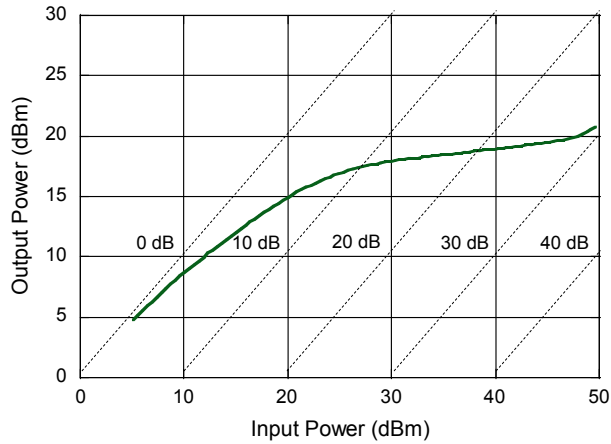
Insertion Loss vs. Frequency



Return Loss vs. Frequency



CW Output Power vs. CW Input Power



Handling Procedures

Please observe the following precautions to avoid damage:

Static and Moisture Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 0 (HBM) devices.

The moisture sensitivity level rating for this device is MSL 2.

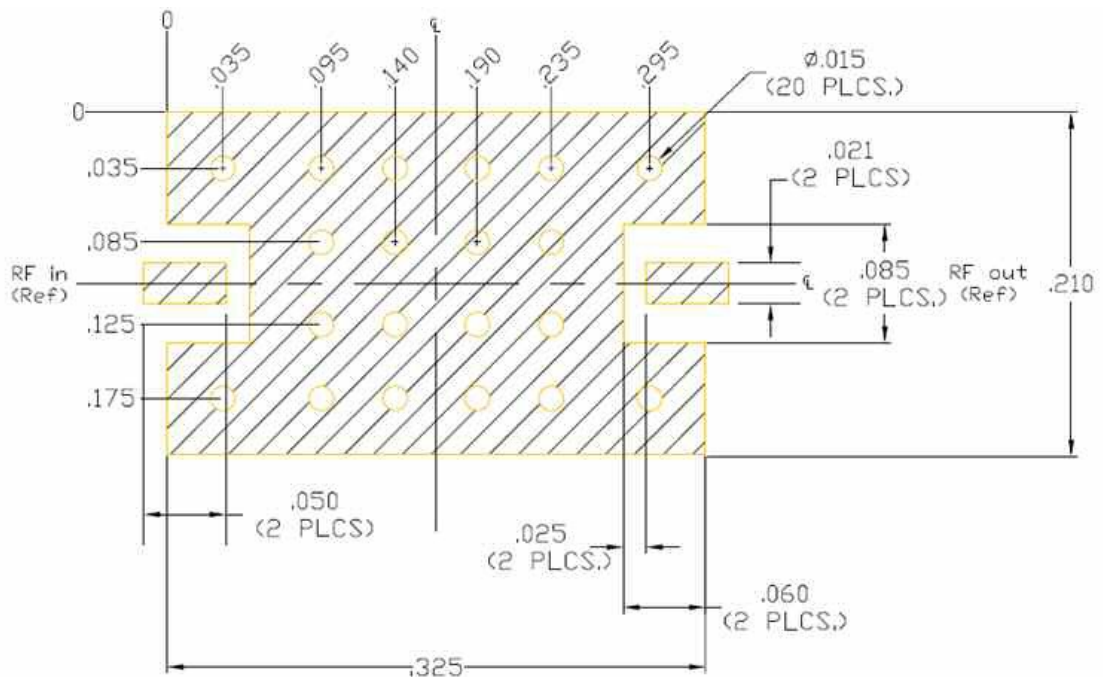
Environmental Capabilities

This limiter is capable of meeting the environmental requirements of MIL-STD-750 and MIL-STD-202.

Assembly Instructions

LM501202-L-C-300 may be placed onto circuit boards with pick and place manufacturing equipment from tube or tape-reel dispensing. The devices are attached to the circuit board using conventional solder re-flow or wave soldering procedures with RoHS type or Sn63/Pb37 type solders per Table 1 and Graph 1 Time-Temperature recommended profile.

RF Circuit Solder Footprint, case style 300 (CS300)

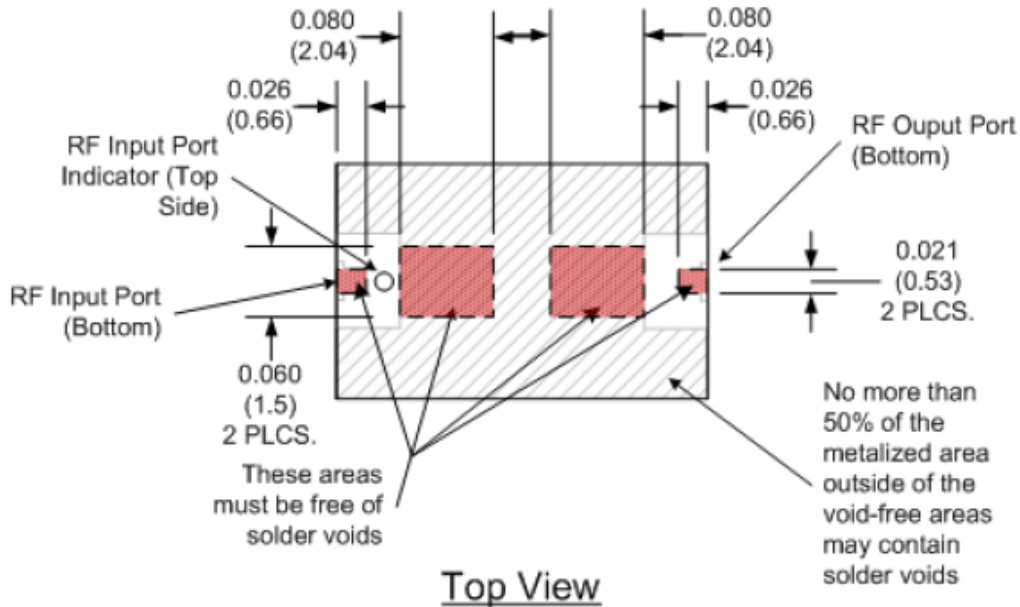


Recommended RF circuit is Rogers R04350B, 10 mils thick.

The hatched metal area on circuit side of device is RF, DC and thermal grounded. Vias should be solid copper fill and gold plated for optimum heat transfer from backside of switch module through circuit vias to metal thermal ground.

Criteria for Proper Mounting on PCB

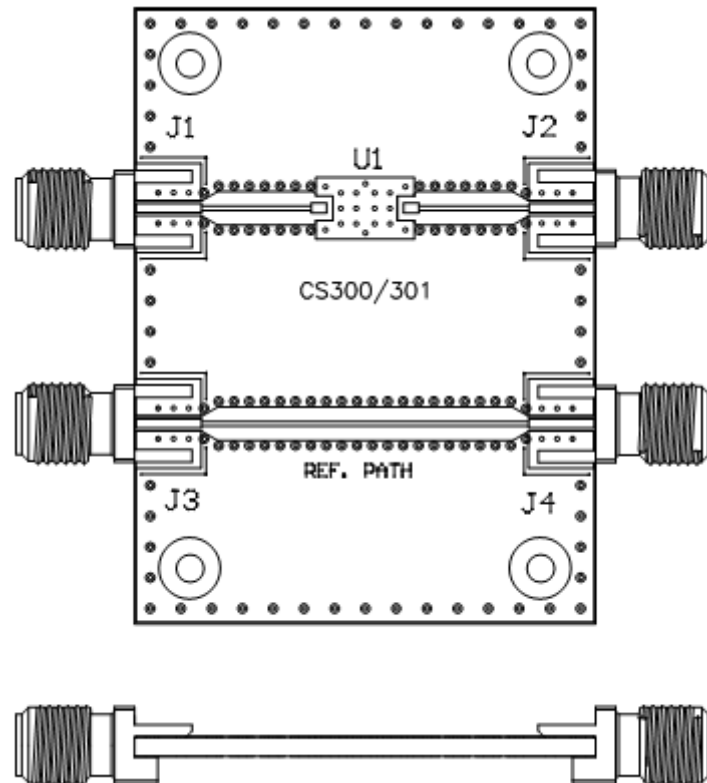
When a large signal is incident upon the input of the LM501202-L-C-300, the impedance of the coarse limiter diodes is forced to a low value by the charge which is injected into these diodes by the combination of the current from the internal detector stage and the large RF voltage initially present across these diodes. As the impedance of these diodes decreases, an increasingly large impedance mismatch with the impedance of the transmission line to which the limiter is connected is created. Ultimately, the impedance of the coarse limiter diodes is reduced to a few ohms or less. This mismatch creates a standing wave, with a current maximum and voltage minimum located at the position of the coarse limiter diodes. While the large majority of the input signal power is reflected back to its source due to the impedance mismatch, the significant RF current that flows at the current maximum causes Joule heating to occur in the coarse limiter diodes. In order to maintain the junction temperature of these diodes below their maximum rated value, there must be a path with minimal thermal resistance from the coarse diodes to the external system heat sink. Also, there must be a minimal electrical resistance and inductance between the underside of the limiter module package and the system ground in order to achieve maximum RF isolation between the input and the output of the limiter module.



Dimensions in inches (mm).

For these reasons, it is imperative that there are no voids in the electrical and thermal paths directly under the coarse limiter diodes. Care must be taken when mounting the LM501202-L-C-300 to avoid voids in the solder joint in the area along the lengthwise axis of the package, under and between the filled vias in the AlN substrate of the module which are shown in the diagram (above). It is also important to ensure no solder voids exist between the limiter module RF ports and the PCB to which the limiter module is attached. No greater than 50% of the remaining metalized area on the bottom of the package may contain solder voids.

Switch Evaluation Board Layout



The evaluation board for the LM501202-L-C-300 limiter is shown above. This evaluation board comprises two sections: the evaluation circuit for the limiter module; and, a reference transmission line.

The limiter module is mounted in position U1. Its RF input is connected to J1 and its output port is connected to J2, via two 50- Ω microstrip transmission lines.

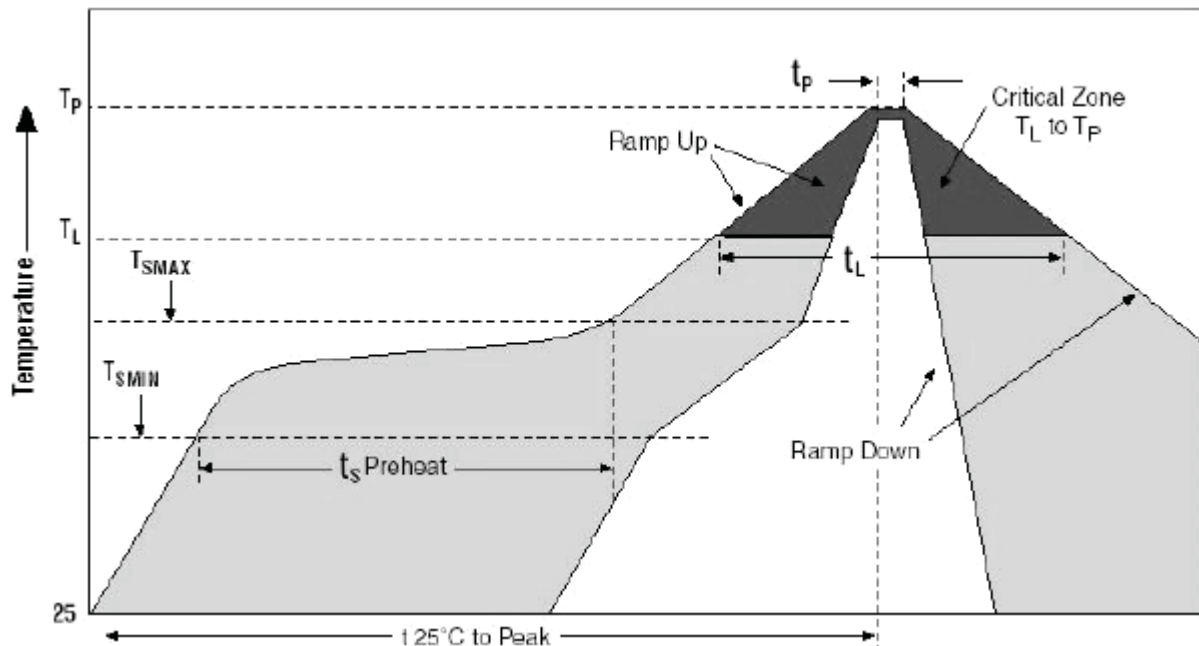
The reference path 50- Ω microstrip transmission line structure can be utilized to determine the insertion loss of the transmission line structures between the limiter module output and J2, so that their respective insertion losses may be subtracted from the total insertion loss measured between J1 and J2. This enables the resolution of the insertion loss of the limiter module only.

The evaluation board is supplied mounted on a heat sink. The maximum RF input power specified in the Absolute Maximum Ratings table must not be exceeded.

Table 1: Time-Temperature Profile for Sn 60 / Pb 40 or RoHS Type Solders

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (TL to TP)	3°C/second maximum	3°C/second maximum
Preheat - Temperature Minimum (TSMIN) - Temperature Maximum (TSMAX) - Time (Minimum to maximum) (ts)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
TSMAX to TL - Ramp-up Rate	—	3°C/second maximum
Time Maintained above: - Temperature (TL) - Time (tL)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature (TP)	225 +0 / -5°C	245 +0 / -5°C
Time within 5°C of actual Peak Temperature (TP)	10-30 seconds	20-40 seconds
Ramp-down Rate	6°C/second maximum	6°C/second maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Graph1: Solder Re-Flow Time-Temperature Function



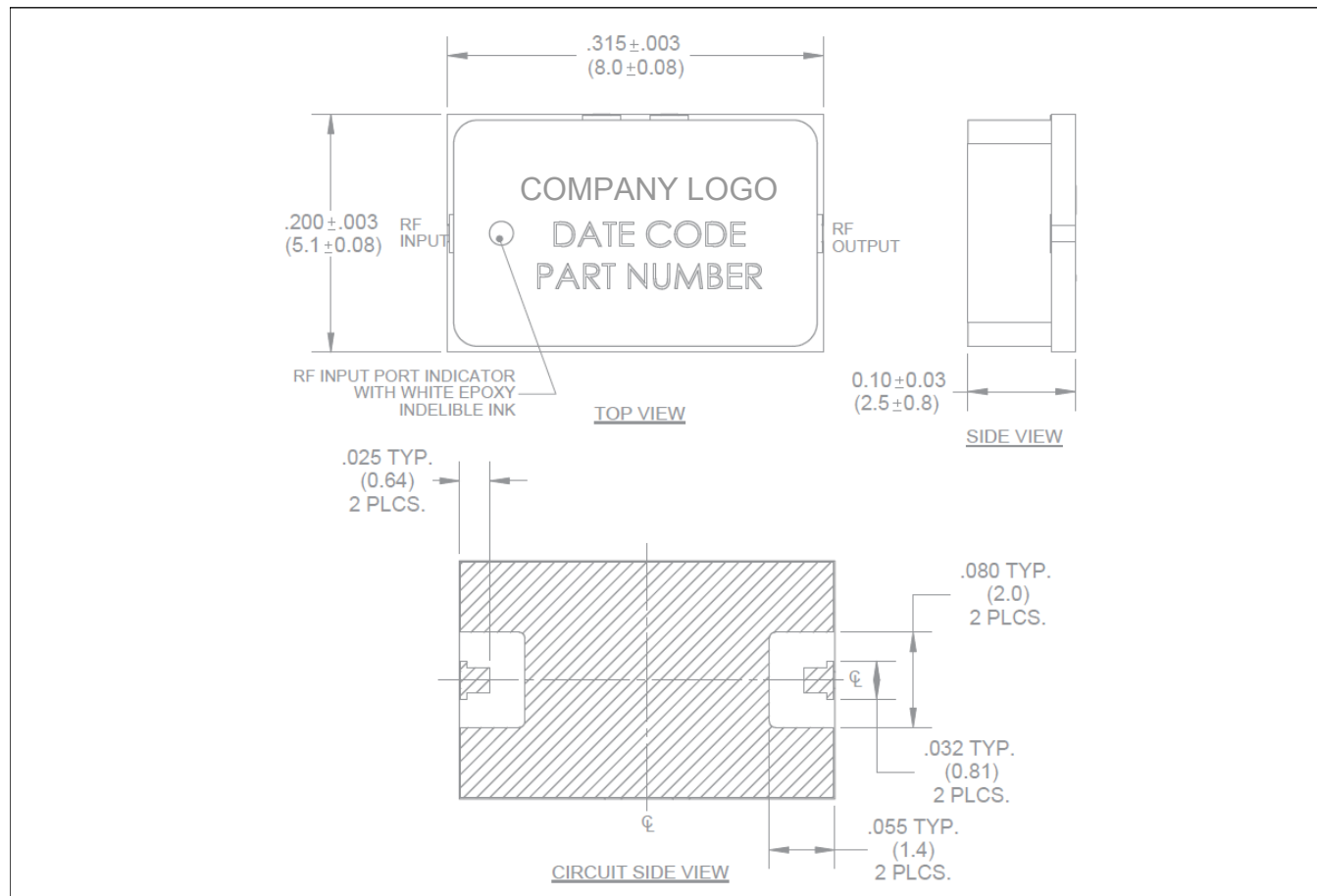
LM501202-L-C-300



PIN Diode Limiter
0.4 - 2.5 GHz

Rev. V1

Outline Drawing, Case Style 300 (CS300)



The hatched metal area on circuit side of device is RF, DC and thermal grounded.

Dimensions are in inches (mm)

Substrate Material: 20 mil thick Alumina Nitride (ALN)

RF Cover: Black Ceramic

Top Side and Backside Metallization: 100 μ IN. typical plated over Ti-Pd.

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