# Dual Ultra Low Noise, High IP3 Amplifier 0.1 - 3.0 GHz



CGY2106XHV Rev. V1

### **Features**

Noise Figure: 0.45 @ 0.9 GHz

Gain: 19.0 @ 0.9 GHz
OIP3: 35 @ 0.9 GHz
P1dB: 19 @ 0.9 GHz

Dual MMIC LNA with Excellent Tracking

• Highly Reliable pHEMT MMIC Process

100% RF Tested

• Samples & Demonstration Boards Available

Space & MIL-STD Available

• Lead-Free 4 mm 16-Lead QFN

RoHS\* Compliant

### **Applications**

 Base Station (LTE, GSM, CDMA, WCDMA, TDS-CDMA, CDMA2000, WiMAX, etc.)

Tower Mounted Amplifiers

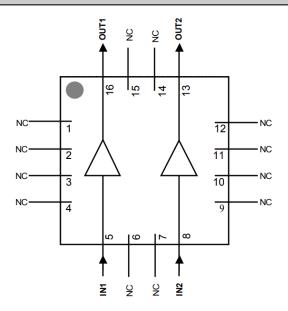
Repeaters

### **Description**

The CGY2106XHV is an extremely low noise figure dual amplifier with state of the art noise figure and linearity suitable for applications from 0.1 to 3 GHz.

This device consists of two identical amplifiers on the same MMIC, and is ideal for use in a balanced configuration. Extremely low noise, high gain and high IP3 results have been achieved on several demonstrators. The minimum noise figure of itself is 0.12 dB at 0.9 GHz.

The MMIC is manufactured using a qualified 0.13 µm pHEMT GaAs D01PH technology. The D01PH process is one of the European Space Agency (ESA) European preferred part list (EPPL) technologies. The device is available in a 4 mm QFN plastic package.



# Pin Configuration<sup>1</sup>

Pin #	Function		
1 - 4,6,7,9 - 12,14,15	No Connection		
5	RF Input 1		
8	RF Input 2		
13	RF Output 2		
16	RF Output 1		
17 <sup>2</sup>	Paddle		

 MACOM recommends connecting No Connection (N/C) pins to ground

The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

### **Ordering Information**

Part Number	Package
CGY2106XHV	

<sup>\*</sup> Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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### Electrical Specifications: Freq. = 0.1 - 3.0 GHz, $T_A = +23^{\circ}\text{C}$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	@ Package Lead, 0.9 GHz @ Reference Board <sup>3,4</sup> , 0.9 GHz	dB	_	18.4 18.6	_
Noise Figure	@ Package Lead, 0.9 GHz @ Reference Board <sup>3,4</sup> , 0.9 GHz	dB	0.12 —	 0.45	_
Bias Voltage	@ Package Lead, 0.9 GHz @ Reference Board <sup>3,4</sup> , 0.9 GHz	dB	_	2.5 2.5	_
Bias Current	@ Package Lead, 0.9 GHz, $V_{EE}$ = -0.7 V @ Reference Board <sup>3,4</sup> , 0.9 GHz $V_{EE1}$ = $V_{EE2}$ = -0.64 V	dB	_	50 50	_
Isolation	@ Package Lead, 0.9 GHz IN1/IN2	dB	_	35	_
Reverse Isolation	@ Reference Board <sup>3,4</sup> , 0.9 GHz OUT/IN	dB	_	24	_
OIP3	@ Reference Board <sup>3,4</sup> , 0.9 GHz	dBm	_	35	_
P1dB	@ Reference Board <sup>3,4</sup> , 0.9 GHz	dBm	_	19	_
Input Return Loss	@ Reference Board <sup>3,4</sup> , 0.9 GHz 50 Ω Source	dB	_	-27	_
Output Return Loss	@ Reference Board <sup>3,4</sup> , 0.9 GHz 50 Ω Load	dB	_	-27	_

<sup>3.</sup> Balanced configuration with on-board bias resistors.

# Absolute Maximum Ratings<sup>5,6</sup>

Parameter	Absolute Maximum	
Input Power	10 dBm	
Gate Voltage	-3 to 0 V	
Drain Voltage	0 to 6 V	
Drain Current	200 mA	
Junction Temperature	+150°C	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-55°C to +150°C	

<sup>5.</sup> Exceeding any one or combination of these limits may cause permanent damage to this device.

## **Handling Procedures**

Please observe the following precautions to avoid damage:

### **Static Sensitivity**

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

<sup>4.</sup> Measured reference plane are the input and output SMA connectors.

MACOM does not recommend sustained operation near these survivability limits.



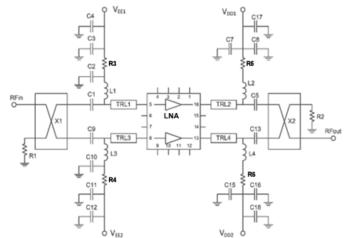
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## Balanced Reference Board, 0.9 GHz

# 

DC connector

# Circuit Diagram, 0.9 GHz

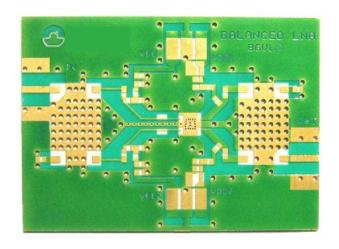


## Parts List<sup>7</sup>

Part	Value Case Style			
C1, C9	47 pF	????		
C2, C10	220 pF	0603		
C3, C7, C11, C15	100 pF	0603		
C4, C8, C12, C16	10 nF	0603		
C5, C13	3 pF	0603		
C17, C18	47 μF	1210		
L1, L3	30 nH	0603		
L2, L4	51 nH	0603		
R1, R2	50 Ω	0603		
R3, R4	470 Ω	0603		
R5, R6	0 Ω	0603		
TRL1, TRL3	94 Ω	10000 x 300 μm		
TRL2, TRL4	50 Ω	4000 x 1100 μm		
Board material is RO4350, height 508 μm				

#### Capacitor C17 and C18 prevent low frequency oscillations when the board is biased from laboratory power supplies. They are not required when on-board voltage regulators are used.

## Reference Circuit Board, 0.9 GHz

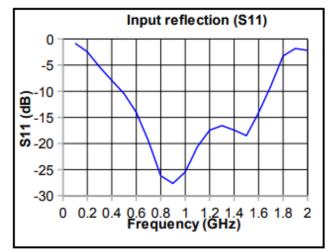


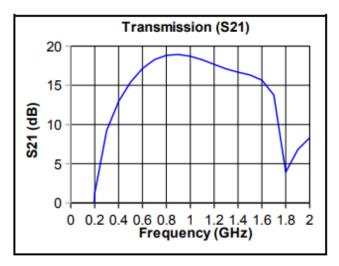


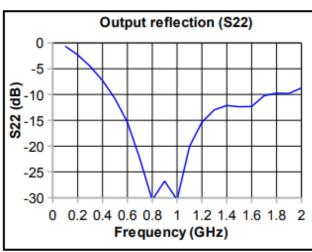
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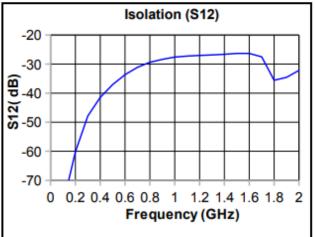
# **Typical Performance Curves:**

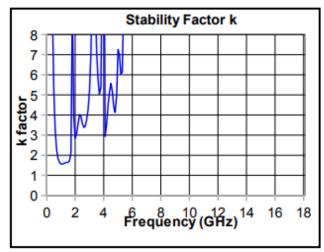
Balanced Reference Board,  $V_{DD1} = V_{DD2} = 2.5 \text{ V}$ ,  $I_{D1} + I_{D2} = 100 \text{ mA}$ ,  $T_A = +23^{\circ}\text{C}$ 











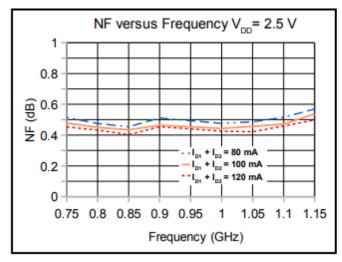
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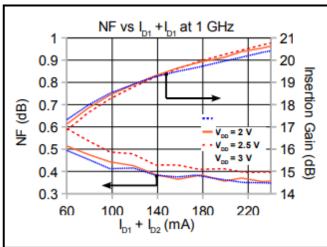


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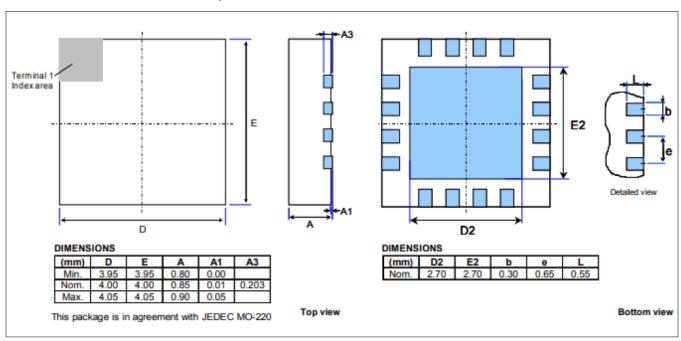
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### Lead-Free 4 mm 16-Lead PQFN



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